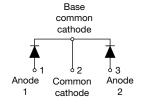


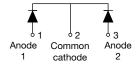
High Performance Schottky Rectifier New Generation 3, D-61 Package, 2 x 55 A

VS-115CNQ015APbF





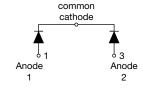




D-61-8-SM

VS-115CNQ015ASLPbF





Base

D-61-8-SL

PRODUCT SUMMARY				
Package	D-61-8, D-61-8-SM, D-61-8-SL			
I _{F(AV)}	2 x 55 A			
V_{R}	15 V			
V _F at I _F	0.37 V			
I _{RM} max.	1200 mA at 100 °C			
T _J max.	125 °C			
Diode variation	Common cathode			
E _{AS}	54 mJ			

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- · Center tap module
- · Optimized for OR-ing applications
- Ultralow forward voltage drop
- High frequency operation
- High power discrete
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- New fully transfer-mold low profile, small footprint, high current package
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

Note

This datasheet provides information about parts that are RoHS-compliant and/or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information/tables in this datasheet for details.

DESCRIPTION

The center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	110	A		
V _{RRM}		15	V		
I _{FSM}	t _p = 5 μs sine	5050	A		
V _F	55 A _{pk} , T _J = 75 °C (per leg)	0.33	V		
TJ	Range	-55 to +125	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VS-115CNQ015APbF	UNITS	
Maximum DC reverse voltage	V _R	T _J = 100 °C	15	V	
Maximum working peak reverse voltage	V_{RWM}	T _J = 125 °C	5	V	

VS-115CNQ015APbF Series

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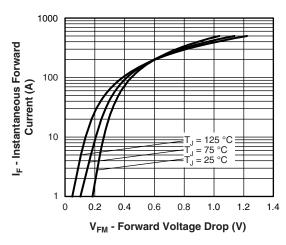
ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per leg		50 % duty cycle at T _C = 112 °C, rectangular waveform		55	А
forward current See fig. 5	per device	I _{F(AV)}			110	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	5050	
			10 ms sine or 6 ms rect. pulse		830	Α
Non-repetitive avalanche energy per leg		E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 4.5 \text{mH}$		54	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A=3$ x V_R typical		2	А	

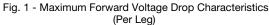
ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	55 A	T _{.1} = 25 °C	0.37	V
Maximum forward voltage drop per leg		110 A	1)=25 0	0.46	
See fig. 1		55 A	T _J = 75 °C	0.33	
		110 A		0.43	
	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	20	mΛ
Maximum reverse leakage current per leg		T _J = 100 °C		1200	
See fig. 2		T _J = 100 °C	V _R = 12 V	900	mA
		T _J = 100 °C	V _R = 5 V	540	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		5500	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/ _I		V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction tempera	ature range	TJ		-55 to +125	°C	
Maximum storage tempera	ture range	T _{Stg}		-55 to +150	C	
Maximum thermal resistance, junction to case per leg		- R _{thJC}	DC operation See fig. 4	0.5		
Maximum thermal resistance, junction to case per package			DC operation	0.25	°C/W	
Typical thermal resistance, case to heatsink (D-61-8 only)		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
Approximate weight	A construction with			7.8	g	
Approximate weight	Approximate weight			0.28	OZ.	
Mounting torque	minimum			40 (35)	kgf · cm	
(D-61-8 only)	maximum			58 (50)	(lbf \cdot in)	
			Case style D-61-8	115CN	Q015A	
Marking device			Case style D-61-8-SM	115CNQ	015ASM	
			Case style D-61-8-SL	115CNC	015ASL	





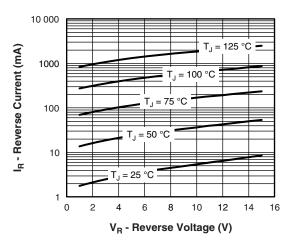


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

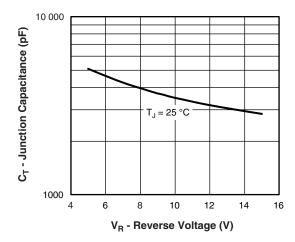


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

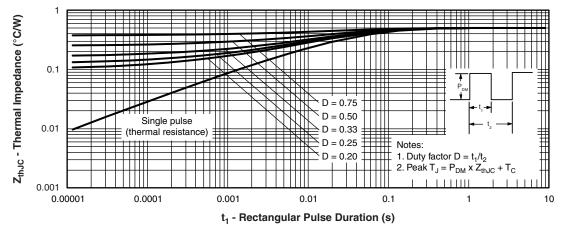


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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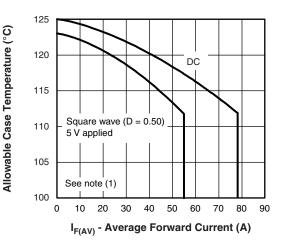


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

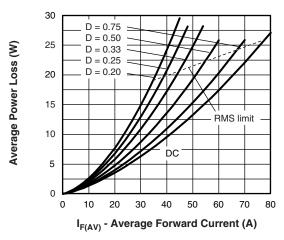


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

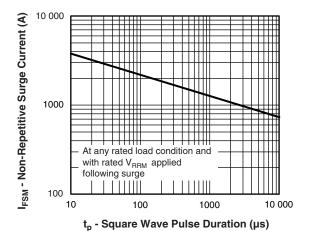


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

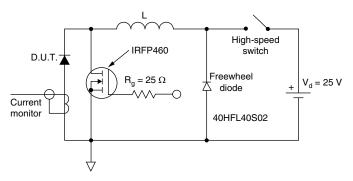


Fig. 8 - Unclamped Inductive Test Circuit

Note

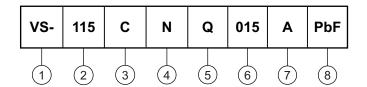
 $^{(1)}$ Formula used: $T_C = T_J$ - (Pd + Pd_{REV}) x R_{th,JC}; Pd = Forward power loss = $I_{F(AV)}$ x V_{FM} at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 5 V

VS-115CNQ015APbF Series

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (110 A)

Circuit configuration:

C = common cathode

4 - Package:

N = D-61

5 - Schottky "Q" series

6 - Voltage rating (015 = 15 V)

7 - Package style:

• A = D-61-8

• ASM = D-61-8-SM

• ASL = D-61-8-SL

8

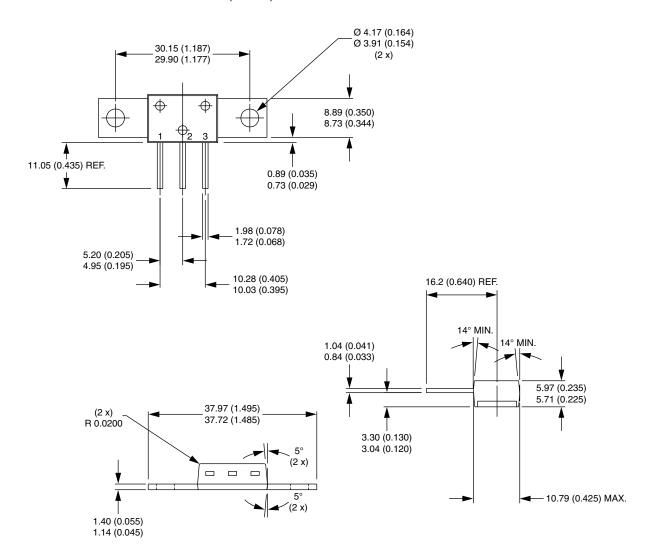
Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95354</u>					
Part marking information	www.vishay.com/doc?95356				



D-61-8, D-61-8-SM, D-61-8-SL

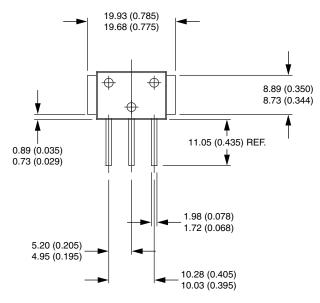
DIMENSIONS - D-61-8 in millimeters (inches)

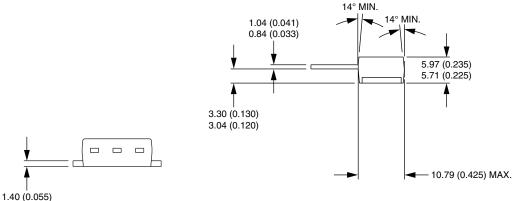




DIMENSIONS - D-61-8-SM in millimeters (inches)

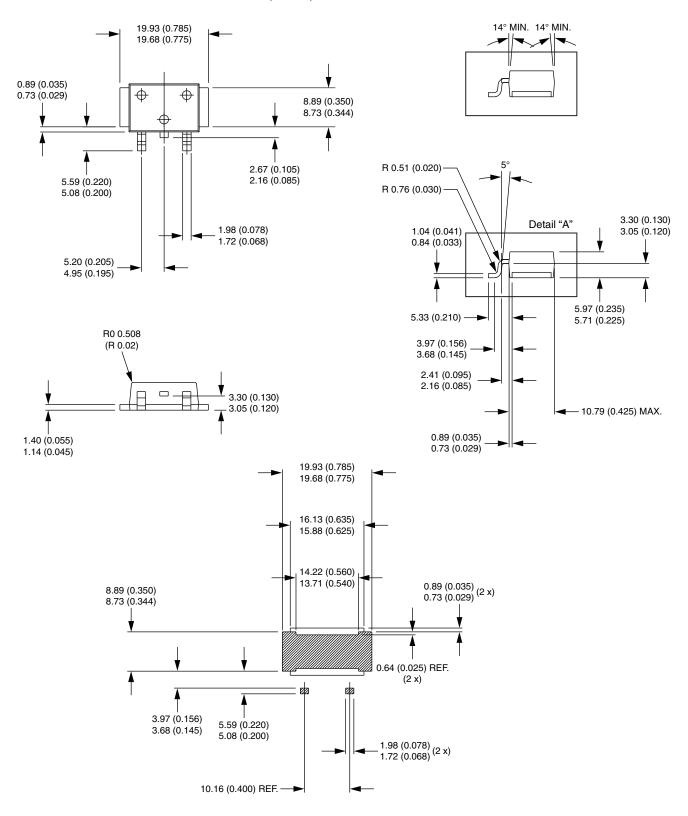
1.14 (0.045)







DIMENSIONS - D-61-8-SL in millimeters (inches)





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