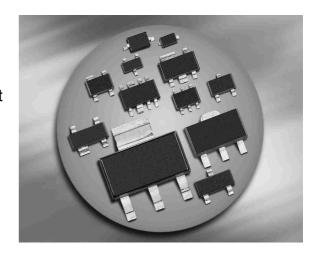


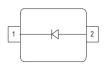
### **Silicon Tuning Diodes**

- High Q hyperabrupt tuning diode
- Designed for low tuning voltage operation
- For VCO's in mobile communications equipment
- Pb-free (RoHS compliant) package





# **BBY52-02L BBY52-02W**



Туре	Package	Configuration	<b>L</b> S(nH)	Marking
BBY52-02L	TSLP-2-1	single, leadless	0.4	K
BBY52-02W	SCD80	single	0.6	KK

### **Maximum Ratings** at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Value	Unit					
Diode reverse voltage	$V_{R}$	7	V					
Forward current	I <sub>F</sub>	20	mA					
Operating temperature range	$T_{op}$	-55 150	°C					
Storage temperature	$T_{ m stg}$	-55 150						

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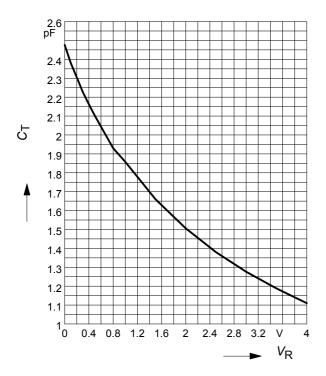
**Electrical Characteristics** at  $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol		Values		
		min.	typ.	max.	
DC Characteristics	•	•			•
Reverse current	$I_{R}$				nA
<i>V</i> <sub>R</sub> = 6 V		-	-	10	
$V_{R}$ = 6 V, $T_{A}$ = 85 °C		-	-	200	
AC Characteristics	·			·	
Diode capacitance	C <sub>T</sub>				pF
$V_{R} = 1 \text{ V}, f = 1 \text{ MHz}$		1.4	1.85	2.2	
$V_{R} = 2 \text{ V}, f = 1 \text{ MHz}$		0.95	1.5	2	
$V_{R} = 3 \text{ V}, f = 1 \text{ MHz}$		0.9	1.35	1.75	
$V_{R} = 4 \text{ V}, f = 1 \text{ MHz}$		0.85	1.15	1.45	
Capacitance ratio	C <sub>T1</sub> /C <sub>T4</sub>	1.1	1.6	2.1	
$V_{R} = 1 \text{ V}, V_{R} = 4 \text{ V}, f = 1 \text{ MHz}$					
Series resistance	r <sub>S</sub>	-	0.9	1.7	Ω
$V_{R} = 1 \text{ V}, f = 1 \text{ GHz}$					



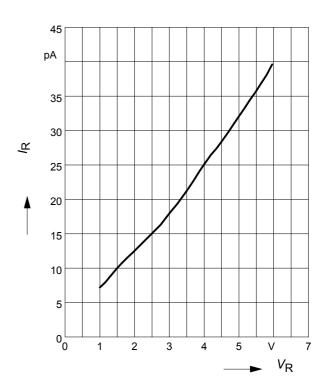
## **Diode capacitance** $C_T = f(V_R)$

f = 1MHz



### Reverse current $I_R = f(V_R)$

$$T_{\mathsf{A}} = 25^{\circ}\mathsf{C}$$

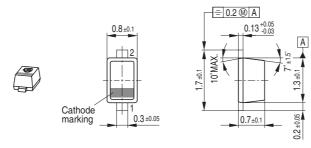


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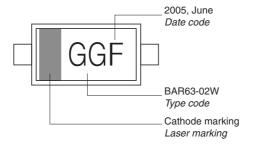
### Package Outline



### Foot Print



### Marking Layout (Example)

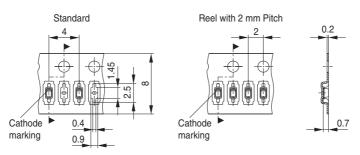


### Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel

Reel ø180 mm = 8.000 Pieces/Reel (2 mm Pitch)

Reel ø330 mm = 10.000 Pieces/Reel



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# Date Code marking for discrete packages with one digit (SCD80, SC79, SC751) CES-Code

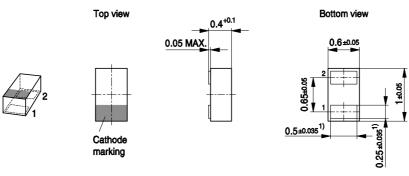
Month	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014
01	а	р	Α	Р	а	р	Α	Р	а	р	Α	Р
02	b	q	В	Q	b	q	В	Q	b	q	В	Q
03	С	r	С	R	С	r	С	R	С	r	С	R
04	d	S	D	S	d	S	D	S	d	S	D	S
05	е	t	Е	Т	е	t	Е	Т	е	t	Е	Т
06	f	u	F	U	f	u	F	U	f	u	F	U
07	g	٧	G	V	g	٧	G	٧	g	٧	G	V
08	h	Х	Н	Х	h	Х	Н	Х	h	Х	Н	Х
09	j	У	J	Υ	j	у	J	Υ	j	У	J	Y
10	k	Z	K	Z	k	Z	K	Z	k	Z	K	Z
11	I	2	L	4	I	2	L	4	I	2	L	4
12	n	3	N	5	n	3	N	5	n	3	N	5

<sup>1)</sup> New Marking Layout for SC75, implemented at October 2005.

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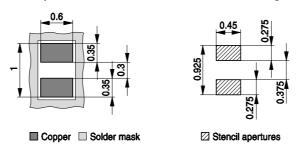
### Package Outline



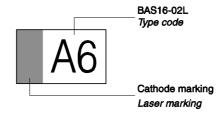
1) Dimension applies to plated terminal

### **Foot Print**

For board assembly information please refer to Infineon website "Packages"

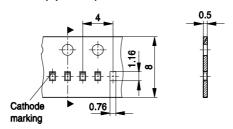


### Marking Layout (Example)



### Standard Packing

Reel ø180 mm = 15.000 Pieces/Reel Reel ø330 mm = 50.000 Pieces/Reel (optional)



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