Device Information

ISL6366

Dual 6-Phase + 1-Phase PWM Controller for VR12/IMVP7 Applications

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Datasheet



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V _{IN} (min) (V)	4.75
V _{IN} (max) (V)	5.25
V _{OUT} (min) (V)	.25
V _{OUT} (max) (V)	1.52
I _{OUT} (max) (A)	>200
V _{BIAS} (V)	5
Applications	VR12/IMVP7
Max # of outputs	2
Max # of phases	6
Droop	Υ
Integrated MOSFET Driver	N

Product Information

Key Features

Intel VR12/IMVP7 Compliant

SerialVID with Programmable IMAX, TMAX, BOOT, ADDRESS OFFSET Registers

Intersil's Proprietary Enhanced Active Pulse Positioning (EAPP) Modulation Scheme (Patented)

Variable Frequency Control During Load Transients to Reduce Beat Frequency Oscillation

Linear Control with Evenly Distributed PWM Pulses for Better Phase Current Balance During Load

Transients

Voltage Feed-Forward and Adjustable Ramp Options

High Frequency and PSI Compensation Options

Dual Outputs

Output 1 (VR0): 1 to 6-Phase for Core or Memory (Coupled Inductor Compatible)

Output 2 (VR1): Single Phase for Graphics, System Agent, or Processor I/O

Differential Remote Voltage Sensing

±0.5% Closed-loop System Accuracy Over Load, Line and Temperature

Phase Doubler Compatibility (NOT Phase Dropping via PWM Lines)

Proprietary Active Phase Adding and Dropping with Diode Emulation Scheme For Enhanced Light Load

Programmable Slew Rate of Fast Dynamic VID with Dynamic VID Compensation (DVC) for VR0

Dynamic VID Compensation (DVS) for VR1 at No Droop

Droop and Diode Emulation Options

Programmable 1 or 2-Phase Operation in PSI1/2/3 Mode

Programmable Standard or Coupled-Inductor Operation

Precision Resistor or DCR Differential Current Sensing

Integrated Programmable Current Sense Resistors

Integrated Thermal Compensation

Accurate Load-Line (Droop) Programming

Accurate Channel-Current Balancing

Accurate Current Monitoring

Average Overcurrent Protection and Channel Current Limit With Internal Current Comparators

Precision Overcurrent Protection on IMON & IMONS Pins

Independent Oscillators, up to 1MHz Per Phase, for Cost, Efficiency, and Performance Optimization

Dual Thermal Monitoring and Thermal Compensation

Start-up Into Pre-Charged Load

Pb-Free (RoHS Compliant)

Description

The ISL6366 is a dual PWM controller; its 6-phase PWMs control the microprocessor core or memory voltage regulator, while its single-phase PWM controls the peripheral voltage regulator for graphics, system agent, or processor I/O.

The ISL6366 utilizes Intersil's proprietary Enhanced Active Pulse Positioning (EAPP) modulation scheme to achieve extremely fast transient response with fewer output capacitors.

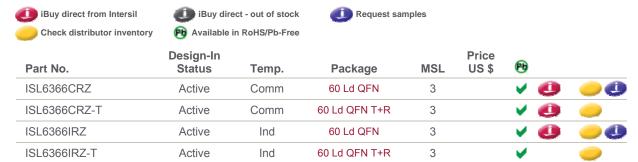
The ISL6366 is designed to be compliant to Intel VR12/IMVP7 specifications. It accurately monitors the load current via the IMON pin and reports this information via the IOUT register to the microprocessor, which sends a PSI# signal to the controller at low power mode via SVID bus. The controller enters 1 or 2-phase operation in low power mode (PSI1); in the ultra low power mode (PSI2,3), it operates in single phase with diode emulation option. In low power modes, the magnetic core and switching losses are significantly reduced, yielding high efficiency at light load. After the PSI# signal is

de-asserted, the dropped phase(s) are added back to sustain heavy load transient response and efficiency.

Today's microprocessors require a tightly regulated output voltage position versus load current (droop). The ISL6366 senses the output current continuously by measuring the voltage across a dedicated current sense resistor or the DCR of the output inductor. The sensed current flows out of the FB pin to develop a precision voltage drop across the feedback resistor for droop control. Current sensing also provides information for channelcurrent balancing, average overcurrent protection and individual phase current limiting. The TM and TMS pins sense an NTC thermistor's temperature, which is internally digitized for thermal monitoring and for integrated thermal compensation of the current sense elements of the respective regulator.

The ISL6366 features remote voltage sensing and completely eliminates any potential difference between remote and local grounds. This improves regulation and protection accuracy. The threshold-sensitive enable input is available to accurately coordinate the start-up of the ISL6366 with other voltage rails.

Pricing / Packaging / Samples / Ordering



The price listed is the manufacturer's suggested retail price for quantities of 1K units. However, prices in today's market are fluid and may change without notice.

MSL = Moisture Sensitivity Level - per IPC/JEDEC J-STD-020

SMD = Standard Microcircuit Drawing

Technical Documentation

Datasheet(s):

EN Dual 6-Phase + 1-Phase PWM Controller for VR12/IMVP7 Applications

Tools And Support

iSim Design Simulation

No Models Available

Application Block Diagrams

Blade Server Storage Area Network (SAN)

Related Devices



ISL6353	Multiphase PWM Regulator for VR12 DDR Memory Systems
ISL6363	Multiphase PWM Regulator for VR12™ Desktop CPUs
ISL6364	Dual 4-Phase + 1-Phase PWM Controller for VR12/IMVP7 Applications
ISL6364C	Dual 4-Phase + 1-Phase PWM Controller for VR12 Desktop Applications
ISL95831	3+1 Voltage Regulator for IMVP-7/VR12 CPUs
ISL95835	3+1 and 1+1 Voltage Regulator for IMVP-7/VR12™ CPUs
ISL95837	3+1 and 1+1 Voltage Regulator for IMVP-7/VR12™ CPUs

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