

## **High Performance Schottky Rectifier, 2 A**



DO-214AC (SMA)

PRODUCT SUMMARY			
Package	DO-214AC (SMA)		
I <sub>F(AV)</sub>	2 A		
V <sub>R</sub>	60 V		
V <sub>F</sub> at I <sub>F</sub>	See Electrical table		
I <sub>RM</sub>	7.5 mA at 125 °C		
T <sub>J</sub> max.	150 °C		
Diode variation	Single die		
E <sub>AS</sub>	2.0 mJ		

#### **FEATURES**





 Guard ring for enhanced ruggedness and long term reliability

RoHS COMPLIANT

- · Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-20MQ060NPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I <sub>F(AV)</sub>	Rectangular waveform	2	Α	
V <sub>RRM</sub>		60	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	40	Α	
V <sub>F</sub>	2 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.68	V	
TJ	Range	-55 to +150	°C	

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-20MQ060NPbF	UNITS
Maximum DC reverse voltage	$V_{R}$	60	V
Maximum working peak reverse voltage	$V_{RWM}$		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at T <sub>C</sub> = 110 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		2	2 1.5
See fig. 4	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 120 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		1.5	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	40	_
non-repetitive surge current See fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	10	Α
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 1  \text{A},  L = 4  \text{mH}$		2.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1.0	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		2 A		0.78	V
		1.5 A	T <sub>J</sub> = 25 °C	0.71	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	1 A		0.63	
See fig. 1	V <sub>FM</sub> (')	2 A		0.68	
		1.5 A	T <sub>J</sub> = 125 °C	0.63	
		1 A		0.57	
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V - Poted V	0.5	- mA
See fig. 2		T <sub>J</sub> = 125 °C	$V_R$ = Rated $V_R$	7.5	
Threshold voltage	V <sub>F(TO)</sub>	$T_{,l} = T_{,l}$ maximum		0.45	V
Forward slope resistance	r <sub>t</sub>			86.8	mΩ
Typical junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 10 V <sub>DC</sub> , T <sub>J</sub> = 25 °C, test signal = 1 MHz		31	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs	

### Note

 $^{(1)}$  Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (similar D-64)	2	Н

### Note

$$^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$

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## Vishay Semiconductors

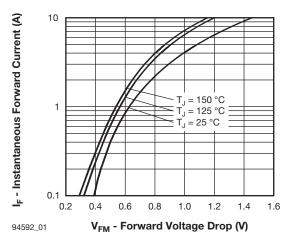


Fig. 1 - Maximum Forward Voltage Drop Characteristics

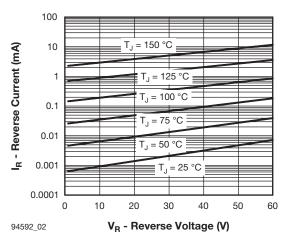


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

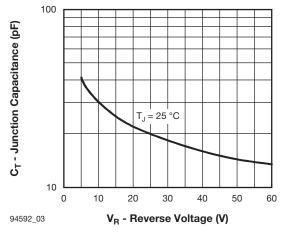


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

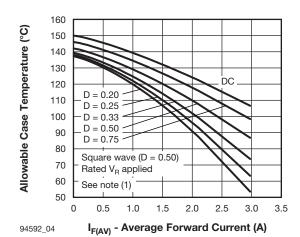


Fig. 4 - Maximum Average Forward Current vs.
Allowable Lead Temperature

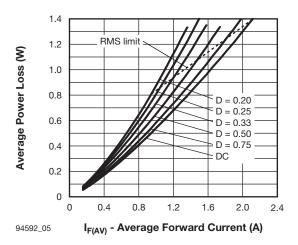


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

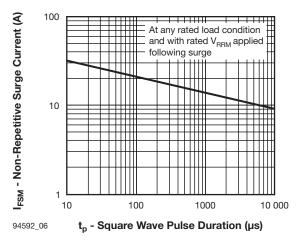


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

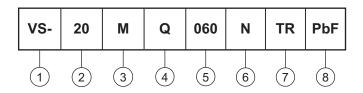
### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 



### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (20 = 2 A)

3 - M = SMA

4 - Q = Schottky "Q" series

Voltage rating (060 = 60 V)

6 - N = new SMA

7 - TR = tape and reel

8 - PbF = lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE   MINIMUM ORDER QUANTITY   PACKAGING DESCRIPTION				
VS-20MQ060NTRPbF	5AT	7500	13" diameter plastic tape and reel		

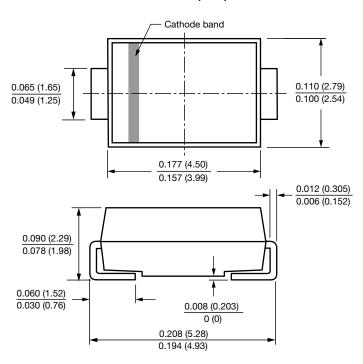
LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95400</u>			
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		
SPICE model	www.vishay.com/doc?95396		



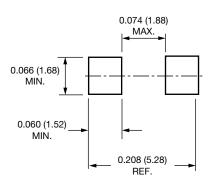
## **SMA**

### **DIMENSIONS** in inches (millimeters)

### **DO-214AC (SMA)**



### Mounting Pad Layout





### **Legal Disclaimer Notice**

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