1.5V Drive Pch MOSFET

RZQ045P01

●Structure

Silicon P-channel MOSFET

Features

- 1) Low on-resistance.
- 2) High power package.
- 3) Low voltage drive. (1.5V)

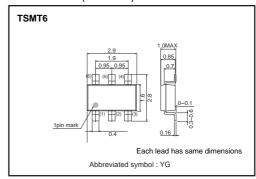
Applications

Switching

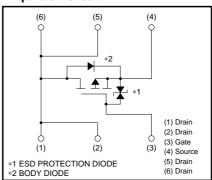
Packaging specifications

	Package	Taping
Туре	Code	TR
	Basic ordering unit (pieces)	3000
RZQ045P0	0	

●Dimensions (Unit:mm)



●Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol		Limits	Unit
Drain-source voltage		V _{DSS}		-12	V
Gate-source voltage	V _{GSS}		±10	V	
Drain current	Continuous	ID		±4.5	Α
	Pulsed	IDP	*1	±12	Α
Source current	Continuous	Is		-1	Α
(Body diode)	Pulsed	I _{SP}	*1	-12	Α
Total power dissipation	PD	*2	1.25	W	
Channel temperature	Tch		150	°C	
Range of Storage temperature		Tstg		-55 to +150	°C

^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a) *	100	°C/W

^{*} Mounted on a ceramic board.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	±10	μА	V _{GS} =±10V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR) DSS}	-12	_	_	V	I _D = -1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	_	_	-1	μΑ	V _{DS} = -12V, V _{GS} =0V
Gate threshold voltage	VGS (th)	-0.3	_	-1.0	٧	Vps= -6V, Ip= -1mA
Static drain-source on-state resistance	R _{DS} (on)	_	25	35	mΩ	I _D = -4.5A, V _G S= -4.5V
		-	31	43	mΩ	I _D = -2.2A, V _G S= -2.5V
		_	39	58	mΩ	I _D = -2.2A, V _G S= -1.8V
		_	50	100	mΩ	I _D = -0.9A, V _G s= -1.5V
Forward transfer admittance	Y _{fs} *	6.5	_	_	S	V _{DS} = -6V, I _D = -4.5A
Input capacitance	Ciss	-	2450	_	pF	V _{DS} = -6V
Output capacitance	Coss	-	320	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	_	290	-	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	_	12	_	ns	I _D = -2.2A
Rise time	tr *	_	75	_	ns	VDD≒ -6V VGS= -4.5V
Turn-off delay time	td (off) *	-	390	_	ns	VGS= −4.5 V Ri≒ 2.7Ω
Fall time	t _f *	_	215	_	ns	R _G =10Ω
Total gate charge	Qg *	_	31	-	nC	V _{DD} ≒−6V R _L ≒1.3Ω
Gate-source charge	Q _{gs} *	_	4.5	_	nC	V _{GS} = -4.5V R _G =10Ω
Gate-drain charge	Q _{gd} *	1	4.0	_	nC	I _D = -4.5A

^{*}Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp *	_	_	-1.2	V	Is= -4.5A, Vgs=0V

^{*}Pulsed

Electrical characteristic curves

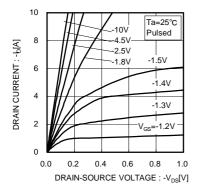


Fig.1 Typical Output Characteristics(I)

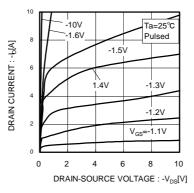


Fig.2 Typical Output Characteristics(${\rm I\hspace{-.1em}I}$)

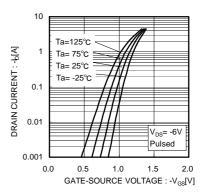


Fig.3 Typical Transfer Characteristics

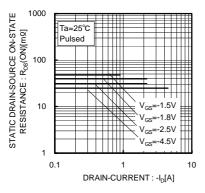


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(I)

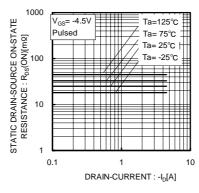


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(II)

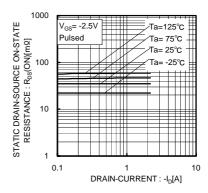


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current(Ⅲ)

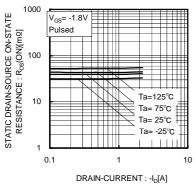


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current(IV)

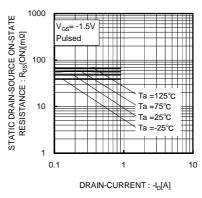


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current(V)

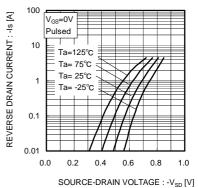


Fig.9 Reverse Drain Current vs. Sourse-Drain Voltage

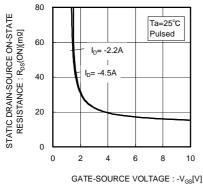


Fig.10 Static Drain-Source On-State
Resistance vs. Gate-Source Voltage

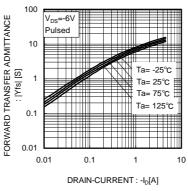


Fig.11 Forward Transfer Admittance vs. Drain Current

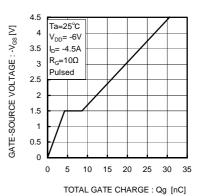
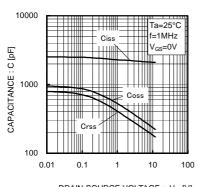
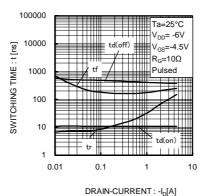


Fig.12 Dynamic Input Characteristics



DRAIN-SOURCE VOLTAGE : -V_{DS}[V]
Fig.13 Typical Capacitance
vs. Drain-Source Voltage



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Fig.14 Switching Characteristics

Measurement circuits

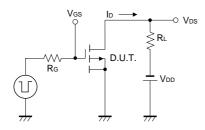


Fig.15 Switching Time Measurement Circuit

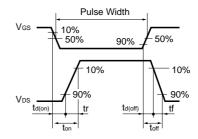


Fig.16 Switching Waveforms

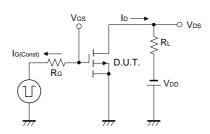


Fig.17 Gate Charge Measurement Circuit

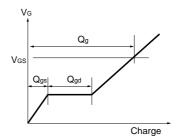


Fig.18 Gate Charge Waveform

●Notice

This product might cause chip aging and breakdown under the large electrified environment . Please consider to design ESD protection circuit.

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