

**Buck chopper  
Super Junction MOSFET  
SiC chopper diode**

$V_{DSS} = 600V$   
 $R_{DSon} = 24m\Omega \text{ max @ } T_j = 25^\circ C$   
 $I_D = 95A \text{ @ } T_c = 25^\circ C$

### Application

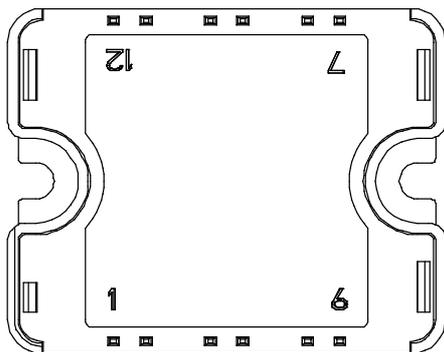
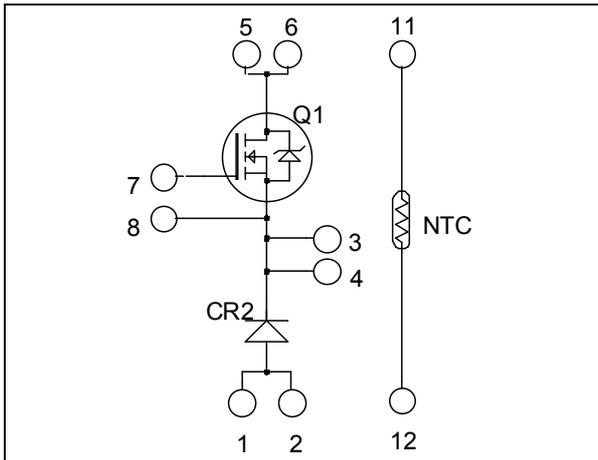
- AC and DC motor control
- Switched Mode Power Supplies

### Features

- **COOLMOS**  
Power Semiconductors
  - Ultra low  $R_{DSon}$
  - Low Miller capacitance
  - Ultra low gate charge
  - Avalanche energy rated
  - Very rugged
- **CR2 SiC Schottky Diode**
  - Zero reverse recovery
  - Zero forward recovery
  - Temperature Independent switching behavior
  - Positive temperature coefficient on VF
- Very low stray inductance
- Internal thermistor for temperature monitoring
- High level of integration

### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant



Pins 1/2 ; 3/4 ; 5/6 must be shorted together

### Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
$V_{DSS}$	Drain - Source Breakdown Voltage	600	V
$I_D$	Continuous Drain Current	$T_c = 25^\circ C$	95
		$T_c = 80^\circ C$	70
$I_{DM}$	Pulsed Drain current	260	A
$V_{GS}$	Gate - Source Voltage	$\pm 20$	V
$R_{DSon}$	Drain - Source ON Resistance	24	$m\Omega$
$P_D$	Maximum Power Dissipation	$T_c = 25^\circ C$	462
$I_{AR}$	Avalanche current (repetitive and non repetitive)	15	A
$E_{AR}$	Repetitive Avalanche Energy	3	mJ
$E_{AS}$	Single Pulse Avalanche Energy	1900	

**CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on [www.microsemi.com](http://www.microsemi.com)

All ratings @  $T_j = 25^\circ\text{C}$  unless otherwise specified

**Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 600V			350	μA
		T <sub>j</sub> = 25°C				
		V <sub>GS</sub> = 0V, V <sub>DS</sub> = 600V			600	
R <sub>DS(on)</sub>	Drain – Source on Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 47.5A			24	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 5mA	2.1	3	3.9	V
I <sub>GSS</sub>	Gate – Source Leakage Current	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0V			200	nA

**Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit	
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V ; V <sub>DS</sub> = 25V f = 1MHz		14.4		nF	
C <sub>oss</sub>	Output Capacitance				17		
Q <sub>g</sub>	Total gate Charge	V <sub>GS</sub> = 10V V <sub>Bus</sub> = 300V I <sub>D</sub> = 95A		300		nC	
Q <sub>gs</sub>	Gate – Source Charge				68		
Q <sub>gd</sub>	Gate – Drain Charge				102		
T <sub>d(on)</sub>	Turn-on Delay Time	<b>Inductive Switching (125°C)</b> V <sub>GS</sub> = 10V V <sub>Bus</sub> = 400V I <sub>D</sub> = 95A R <sub>G</sub> = 2.5Ω		21		ns	
T <sub>r</sub>	Rise Time				30		
T <sub>d(off)</sub>	Turn-off Delay Time				100		
T <sub>f</sub>	Fall Time				45		
E <sub>on</sub>	Turn-on Switching Energy	<b>Inductive switching @ 25°C</b> V <sub>GS</sub> = 10V ; V <sub>Bus</sub> = 400V I <sub>D</sub> = 95A ; R <sub>G</sub> = 2.5Ω		810		μJ	
E <sub>off</sub>	Turn-off Switching Energy				1040		
E <sub>on</sub>	Turn-on Switching Energy	<b>Inductive switching @ 125°C</b> V <sub>GS</sub> = 10V ; V <sub>Bus</sub> = 400V I <sub>D</sub> = 95A ; R <sub>G</sub> = 2.5Ω		1320		μJ	
E <sub>off</sub>	Turn-off Switching Energy				1270		

**CR2 SiC diode ratings and characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit	
V <sub>R RM</sub>	Maximum Peak Repetitive Reverse Voltage		600			V	
I <sub>RM</sub>	Maximum Reverse Leakage Current	V <sub>R</sub> = 600V	T <sub>j</sub> = 25°C		200	800	μA
			T <sub>j</sub> = 175°C		400	4000	
I <sub>F</sub>	DC Forward Current			40		A	
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 40A	T <sub>j</sub> = 25°C		1.6	1.8	V
			T <sub>j</sub> = 175°C		2.0	2.4	
Q <sub>C</sub>	Total Capacitive Charge	I <sub>F</sub> = 40A, V <sub>R</sub> = 300V di/dt = 1200A/μs		56		nC	
C	Total Capacitance	f = 1MHz, V <sub>R</sub> = 200V		260		pF	
		f = 1MHz, V <sub>R</sub> = 400V		200			

## Thermal and package characteristics

Symbol	Characteristic	Min	Typ	Max	Unit	
R <sub>thJC</sub>	Junction to Case Thermal Resistance	Transistor		0.27	°C/W	
		SiC Diode		0.8		
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t=1 min, 50/60Hz	4000			V	
T <sub>J</sub>	Operating junction temperature range	-40		150	°C	
T <sub>STG</sub>	Storage Temperature Range	-40		125		
T <sub>C</sub>	Operating Case Temperature	-40		100		
Torque	Mounting torque	To heatsink	M4	2	3	N.m
Wt	Package Weight				80	g

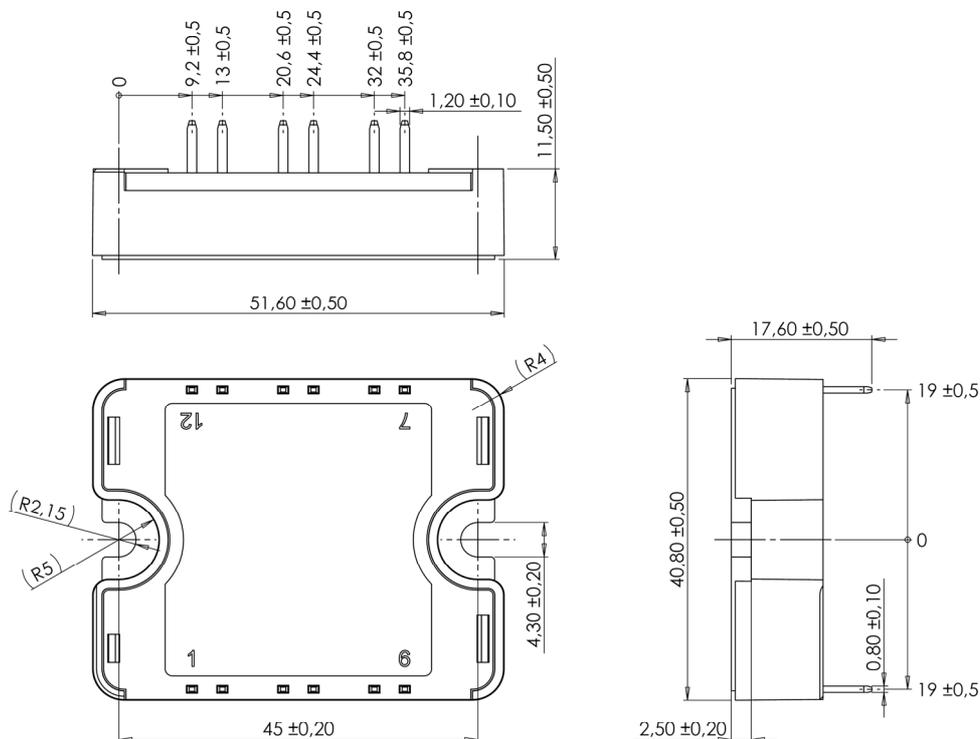
## Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information).

Symbol	Characteristic	Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C		50		kΩ
ΔR <sub>25</sub> /R <sub>25</sub>			5		%
B <sub>25/85</sub>	T <sub>25</sub> = 298.15 K		3952		K
ΔB/B	T <sub>C</sub> = 100°C		4		%

$$R_T = \frac{R_{25}}{\exp \left[ B_{25/85} \left( \frac{1}{T_{25}} - \frac{1}{T} \right) \right]}$$

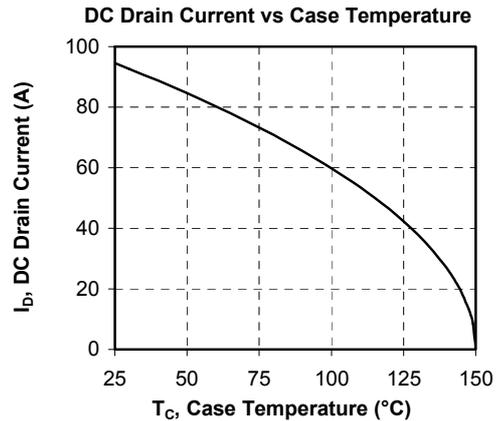
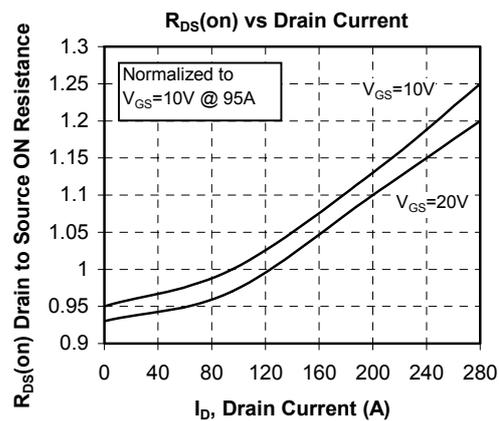
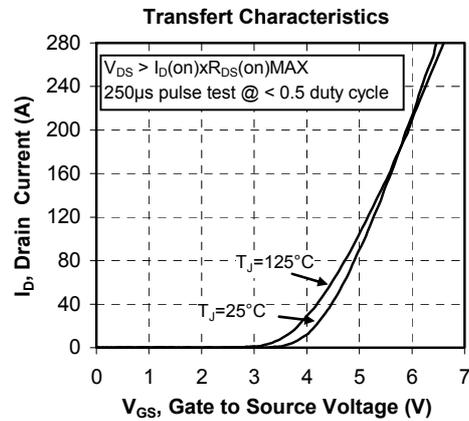
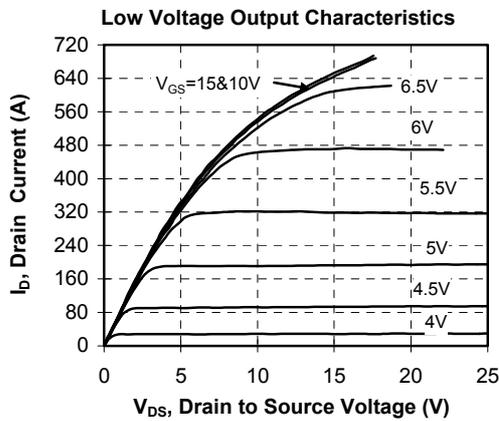
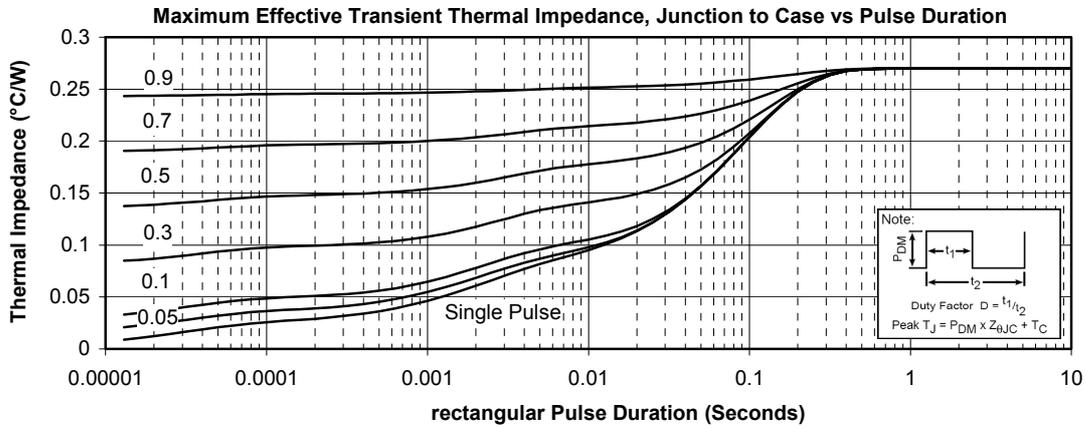
T: Thermistor temperature  
 R<sub>T</sub>: Thermistor value at T

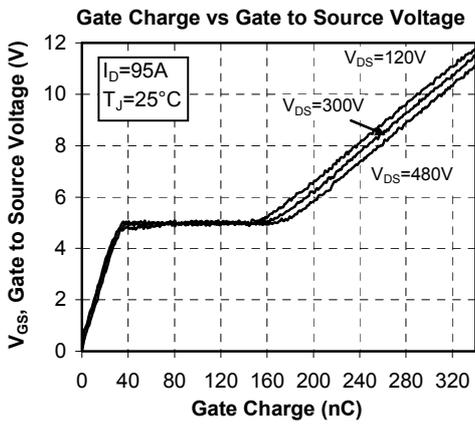
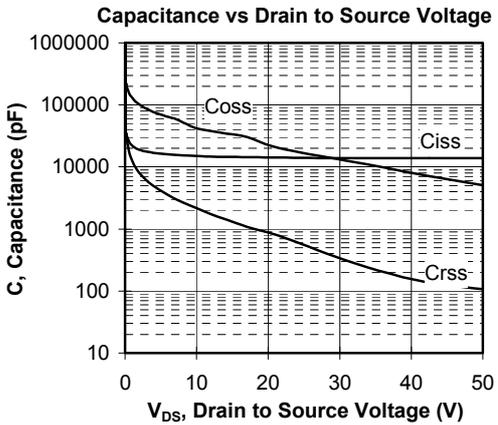
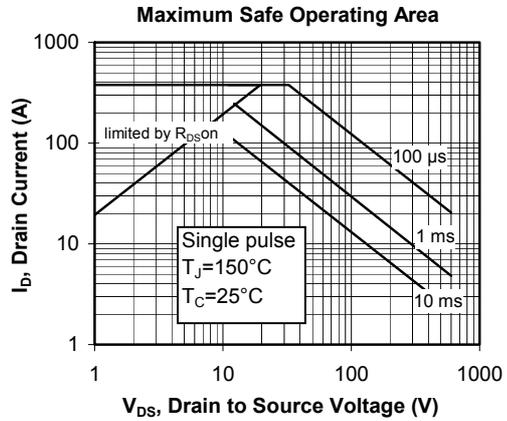
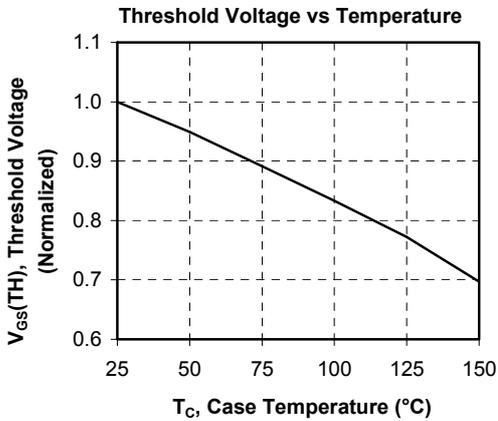
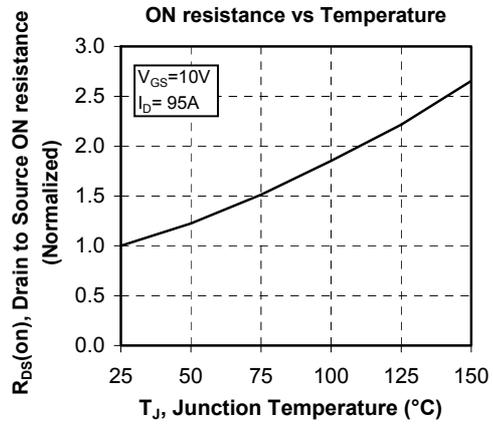
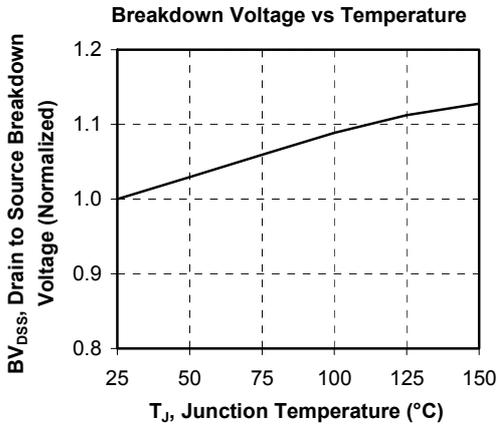
## SP1 Package outline (dimensions in mm)

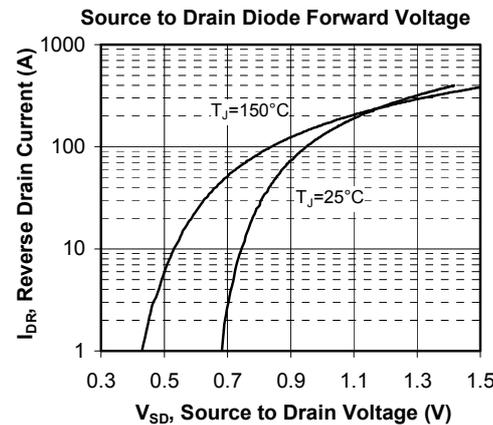
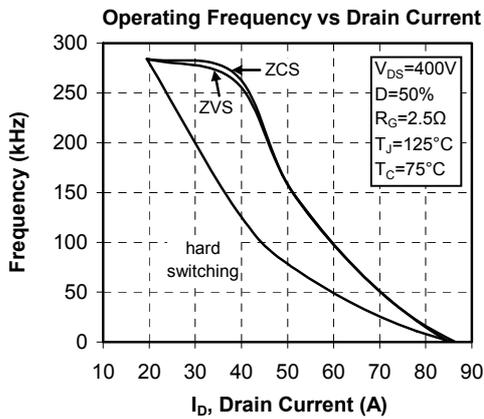
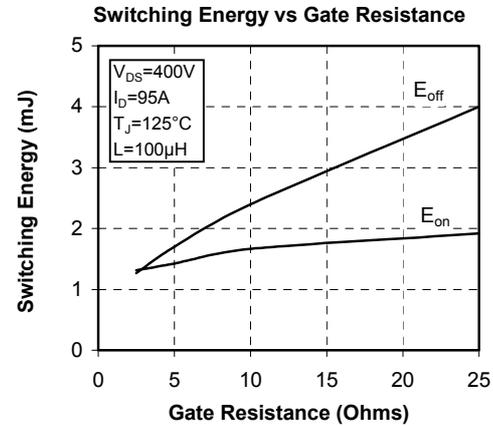
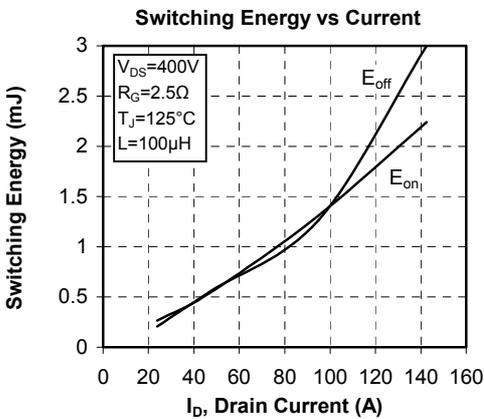
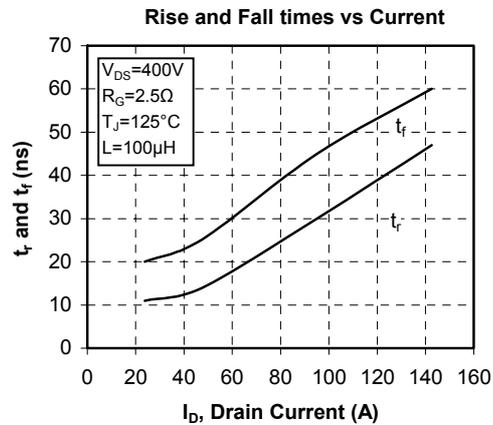
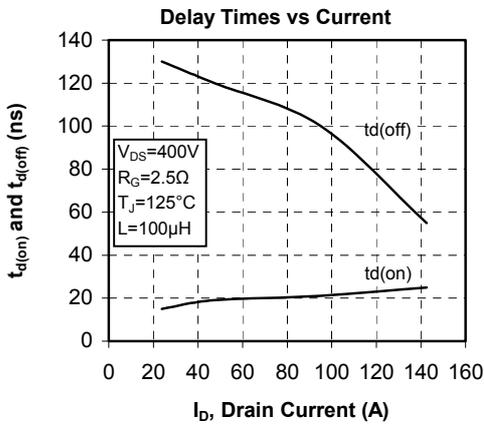


See application note 1904 - Mounting Instructions for SP1 Power Modules on [www.microsemi.com](http://www.microsemi.com)

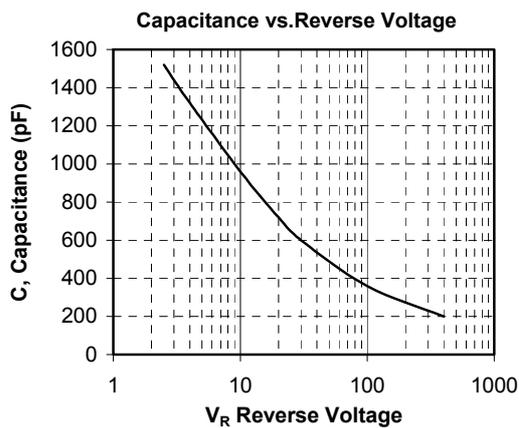
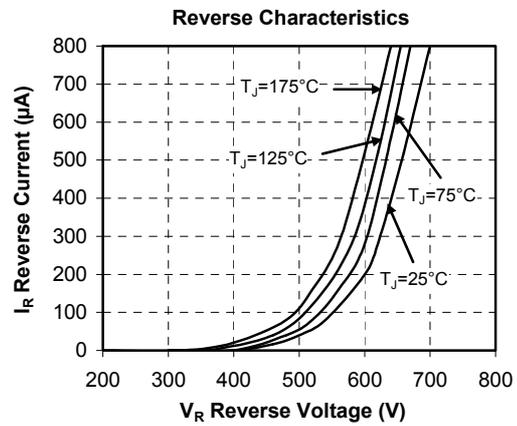
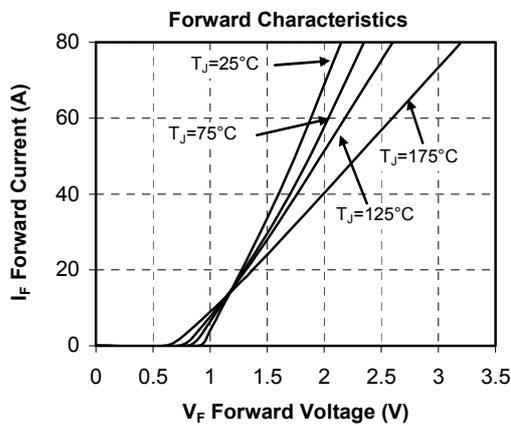
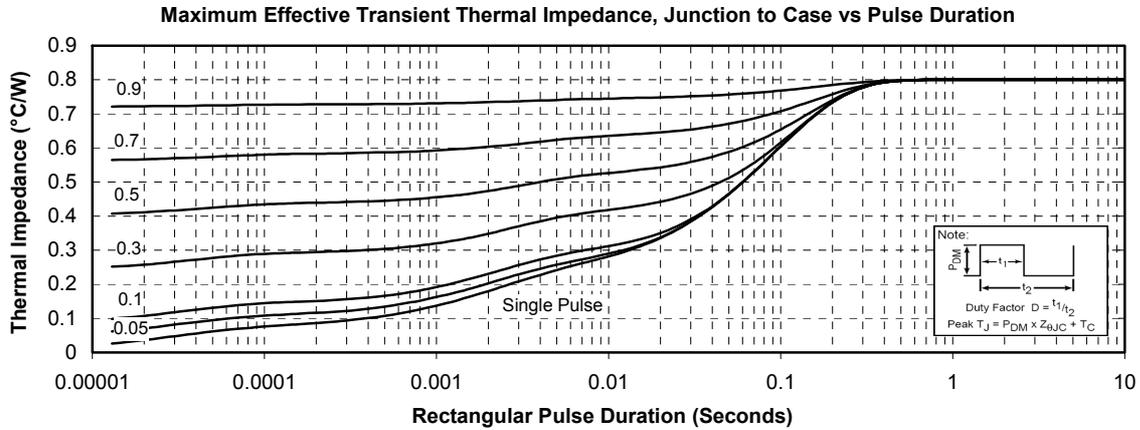
## Typical CoolMOS Performance Curve







## Typical CR2 SiC Diode Performance Curve



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