# **SBE812**

#### ON Semiconductor®

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# **Schottky Barrier Diode**

60V, 1A, Low IR, Non-Monolithic Dual VEC8 Common Cathode

# **Applications**

· High frequency rectification (switching regulators, converters, choppers)

### **Features**

- · Small switching noise
- · Low leakage current and high reliability due to highly reliable planar structure
- · Ultrasmall package allows applied sets to be made small and thin

# **Specifications**

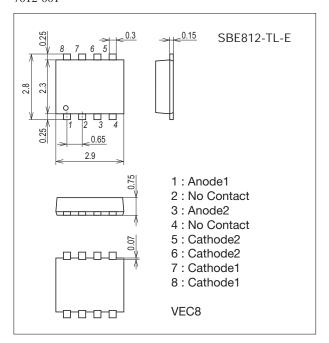
Absolute Maximum Ratings at Ta=25°C (Value per element)

Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	VRRM		60	V
Nonrepetitive Peak Reverse Surge Voltage	VRSM		65	V
Average Output Current	I <sub>O</sub>		1.0	А
Surge Forward Current	IFSM	50Hz sine wave, 1 cycle	10	Α
Junction Temperature	Tj		-55 to +125	°C
Storage Temperature	Tstg		-55 to +125	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### **Package Dimensions**

unit: mm (typ) 7012-001



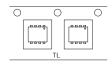
#### **Product & Package Information**

• Package : VEC8

• JEITA, JEDEC : -

• Minimum Packing Quantity : 3,000 pcs./reel

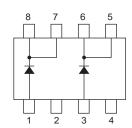
### Packing Type: TL



# Marking



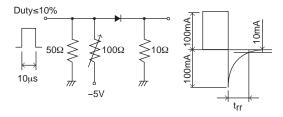
## **Electrical Connection**



# Electrical Characteristics at Ta=25°C (Value per element)

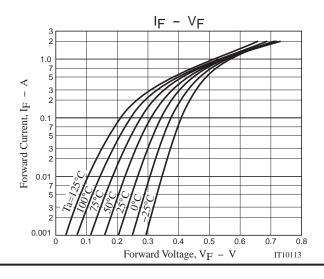
Parameter	Symbol	Conditions	Ratings			Unit
		Conditions	min	typ	max	Utill
Reverse Voltage	VR	I <sub>R</sub> =0.2mA	60			V
Forward Voltage	VF	I <sub>F</sub> =1A		0.55	0.60	V
Reverse Current	IR	V <sub>R</sub> =30V			30	μΑ
Interterminal Capacitance	С	V <sub>R</sub> =10V, f=1MHz		35		pF
Reverse Recovery Time	t <sub>rr</sub>	IF=100mA, See specified Test Circuit.			10	ns
	Rth(j-a)1	When Mounted in Cu-foiled area of 1.92mm <sup>2</sup> x0.03mm	80			°C / W
Thermal Resistance		on glass epoxy substrate				
	Rth(j-a)2	When mounted on ceramic substrate (1000mm <sup>2</sup> x0.8mm)		75		°C/W

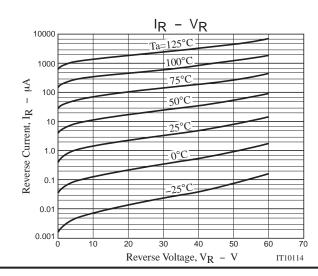
# t<sub>rr</sub> Test Circuit

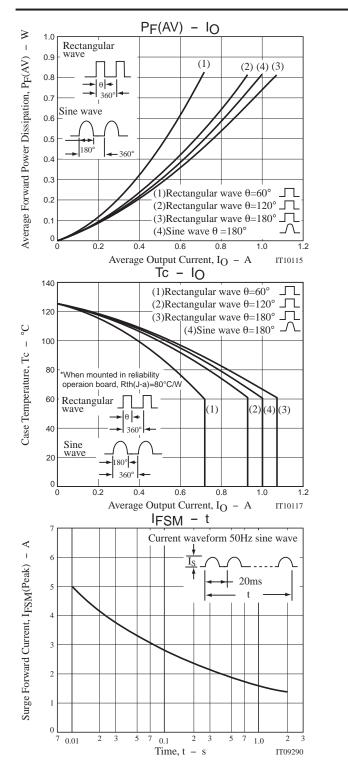


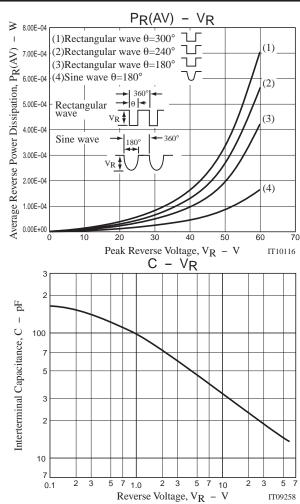
# **Ordering Information**

Device	Package	Shipping	memo	
SBE812-TL-E	812-TL-E VEC8		Pb Free	







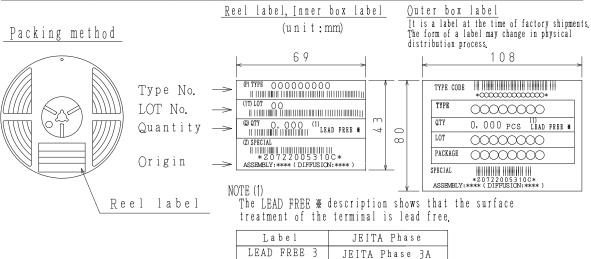


#### **Taping Specification**

#### SBE812-TL-E

# 1. Packing Format

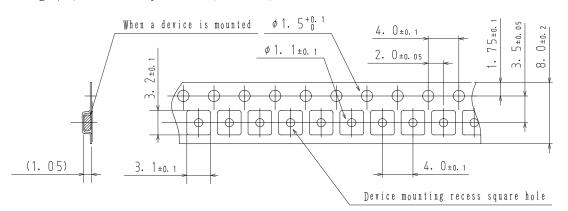
Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format		
	Туре	Reel	Inner box	Outer box	Inner $BOX(C-1)$	Outer BOX (A-7)	
VEC8	СРН6	3, 000	15,000	90,000	5 reels contained	6 inner boxes contained	
					Dimensions:mm (external)	Dimensions:mm (external)	
					183×72×185	440×195×210	



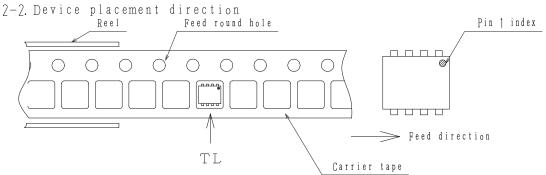
JEITA Phase 3

# 2. Taping configuration

### 2-1. Carrier tape size (unit:mm)



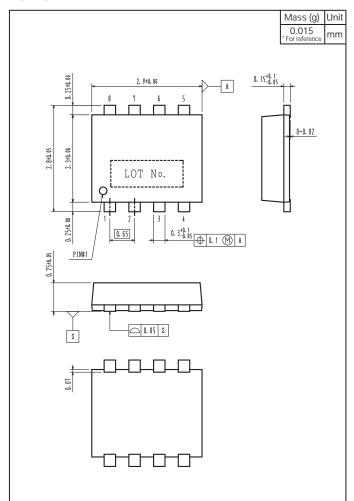
LEAD FREE 4



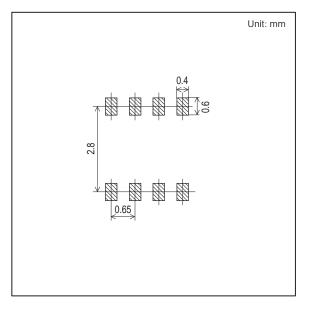
Those with oen electrode terminal on the feed hole side·····TL

# **Outline Drawing**

SBE812-TL-E



# Land Pattern Example



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