

2SK0662 (2SK662)

Silicon N-channel junction FET

For low-frequency and low-noise amplification

■ Features

- High mutual conductance g_m
- Low noise type
- SMini type package, allowing downsizing of the sets and automatic insertion through the tape/magazine packing

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Drain-sourse voltage	V _{DS}	30	V	
Drain-gate voltage (Source open)	$V_{\rm DGO}$	30	V	
Drain current	I_{D}	20	mA	
Gate current	I_G	10	mA	
Power dissipation	P_{D}	150	mW	
Channel temperature	T _{ch}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	

Package

- Code
 - SMini3-G1
- Pin Name
 - 1: Source
 - 2: Drain
 - 3: Gate
- Marking Symbol: 10

■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

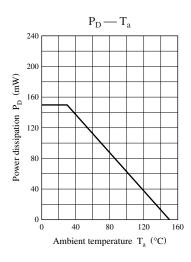
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source current *	I_{DSS}	$V_{DS} = 10 \text{ V}, V_{GS} = 0$	0.5	0,	12	mA
Gate-source cutoff current	I_{GSS}	$V_{GS} = 30 \text{ V}, V_{DS} = 0$		0	100	nA
Gate-source cutoff voltage	V_{GSC}	$V_{DS} = 10 \text{ V}, I_D = 10 \mu\text{A}$	0.1		1.5	V
Mutual conductance	g _{m1}	$V_{DS} = 10 \text{ V}, I_D = 0.5 \text{ mA}, f = 1 \text{ kHz}$	4			mS
	g _{m2}	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ kHz}$	4			
Short-circuit forward transfer capacitance (Common source)	C _{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$		14		pF
Reverse transfer capacitance (Common source)	C _{rss}	28 110 :		3.5		pF
Noise voltage	NV	$V_{DS} = 30 \text{ V}, I_D = 1 \text{ mA}, G_V = 80 \text{ dB}$ $R_g = 100 \text{ k}\Omega$, Function = FLAT		60		mV

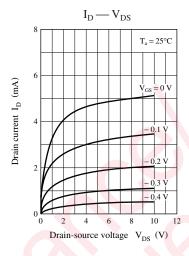
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

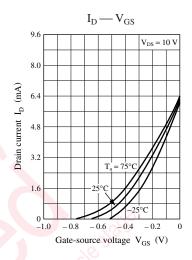
2. *: Rank classification

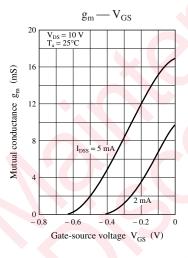
Rank	Р	Q	R
I _{DSS} (mA)	0.5 to 3.0	2.0 to 6.0	4.0 to 12.0

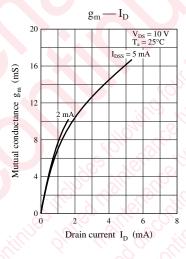
Note) The part number in the parenthesis shows conventional part number.

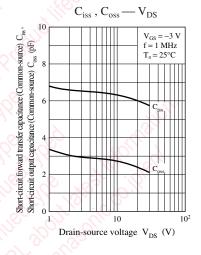


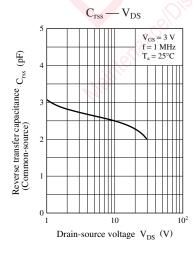


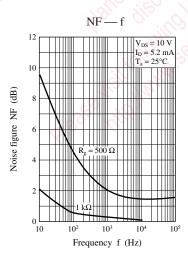






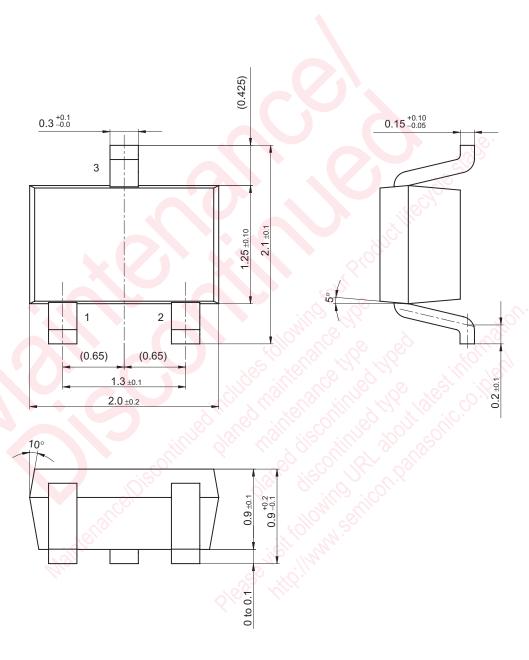






2 SJF00009CED

SMini3-G1 Unit: mm



SJF00009CED 3

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