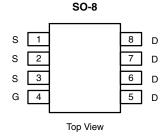


Vishay Siliconix

P-Channel 30 V (D-S) MOSFET

| PRODUCT SUMMARY | | | | | |
|---------------------|---------------------------------------|-----------------------------|-----------------------|--|--|
| V _{DS} (V) | $R_{DS(on)}\left(\Omega\right)$ Max. | I _D ^a | Q _g (Typ.) | | |
| | 0.0065 at V _{GS} = - 10 V | - 29 | | | |
| - 30 | 0.0082 at V _{GS} = - 6 V | - 23 | 66 nC | | |
| | 0.0112 at $V_{GS} = -4.5 \text{ V}$ | - 20 | | | |



Ordering Information:

Si4491EDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Extended V_{GS} range (± 25 V) for adaptor switch applications
- Extremely low R_{DS(on)}
- TrenchFET® Power MOSFET
- 100 % R_q and UIS Tested
- Typical ESD Performance: 4000 V (HBM)
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

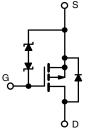
APPLICATIONS

- · Adaptor Switch, Load Switch
- **Power Management**
- Notebook Computers and Portable **Battery Packs**



COMPLIANT HALOGEN

FREE



P-Channel MOSFET

| Parameter | Symbol | Limit | Unit | |
|---|-----------------------------------|--------------------|------------------------|----|
| Drain-Source Voltage | V _{DS} | - 30 | V | |
| Gate-Source Voltage | | V _{GS} | ± 25 | v |
| | T _C = 25 °C | | - 25.8 | |
| Continuous Drain Current (T _{.1} = 150 °C) | T _C = 70 °C | 1 . [| - 20.7 | |
| Continuous Diam Current (1) = 130 C) | T _A = 25 °C | l _D | - 17.3 | |
| | T _A = 70 °C | | - 13.9 ^{b, c} | Α |
| Pulsed Drain Current (t = 300 μs) | | I _{DM} | - 60 | |
| Continuous Source-Drain Diode Current | T _C = 25 °C | la la | - 5.8 ^{b, c} | |
| Continuous Source-Drain Diode Current | T _A = 25 °C | - I _S - | - 2.6 ^{b, c} | |
| Single Pulse Avalanche Current | L = 0.1 mH | I _{AS} | - 40 | |
| Single Pulse Avalanche Energy | L = 0.1 IIII | E _{AS} | 80 | mJ |
| | T _C = 25 °C | | 6.9 | |
| Maximum Power Dissipation | T _C = 70 °C | P_{D} | 4.4 | w |
| Maximum Power Dissipation | T _A = 25 °C |] ' | 3.1 ^{b, c} | VV |
| | T _A = 70 °C | 1 | 2 ^{b, c} | |
| Operating Junction and Storage Temperature | T _J , T _{stg} | - 55 to 150 | °C | |

| THERMAL RESISTANCE RATINGS | | | | | | | | |
|---|--------------|-------------------|---------|------|-------|--|--|--|
| Parameter | Symbol | Typical | Maximum | Unit | | | | |
| Maximum Junction-to-Ambient ^{b, d} | t ≤ 10 s | R _{thJA} | 33 | 40 | °C/W | | | |
| Maximum Junction-to-Foot (Drain) | Steady State | R _{thJF} | 15 | 17 | 0, ** | | | |

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 90 °C/W.

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| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|-------------------------|---|-------|--------|--------|-------|--|
| Static | • | | | | | | |
| Drain-Source Breakdown Voltage | V _{DS} | $V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$ | - 30 | | | V | |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_{J}$ | I - 250 uA | | - 24 | | mV/°C | |
| V _{GS(th)} Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | I _D = - 250 μA | | 6 | | | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}$, $I_{D} = -250 \mu A$ | - 1.2 | | - 2.8 | V | |
| Oata Oassaa Laalaasa | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$ | | | ± 150 | 1 | |
| Gate-Source Leakage | | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | | | ± 15 | A | |
| 7 0 | I _{DSS} | V _{DS} = - 30 V, V _{GS} = 0 V | | | - 1 | μΑ | |
| Zero Gate Voltage Drain Current | | V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C | | | - 10 | | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$ | - 20 | | | Α | |
| | , , | V _{GS} = - 10 V, I _D = - 13 A | | 0.0054 | 0.0065 | | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | V _{GS} = - 6 V, I _D = - 10 A | | 0.0068 | 0.0082 | Ω | |
| | ==(:, | V _{GS} = - 4.5 V, I _D = - 8 A | | 0.0093 | 0.0112 | | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = - 15 V, I _D = - 13 A | | 44 | | S | |
| Dynamic ^b | | 20 | | L | | | |
| Input Capacitance | C _{iss} | | | 4620 | | pF | |
| Output Capacitance | C _{oss} | V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz | | 880 | | | |
| Reverse Transfer Capacitance | C _{rss} | | | 820 | | | |
| · · | V. | V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 17.3 A | | 102 | 153 | | |
| Total Gate Charge | Qg | | | 66 | 80 | nC | |
| Gate-Source Charge | Q _{gs} | V _{DS} = - 15 V, V _{GS} = - 5 V, I _D = - 17.3 A | | 16 | | | |
| Gate-Drain Charge | Q_{gd} | | | 28 | | | |
| Gate Resistance | R_{g} | f = 1 MHz | 0.3 | 1.3 | 2.6 | Ω | |
| Turn-On Delay Time | t _{d(on)} | | | 70 | 105 | | |
| Rise Time | t _r | $V_{DD} = 0 \text{ V}, R_L = 1.5 \Omega$ | | 70 | 105 | | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong$ - 10 A, V_{GEN} = - 4.5 V, R_g = 1 Ω | | 45 | 68 | | |
| Fall Time | t _f | | | 27 | 41 | | |
| Turn-On Delay Time | t _{d(on)} | | | 18 | 30 | ns | |
| Rise Time | t _r | $V_{DD} = -15 \text{ V}, R_L = 1.5 \Omega$ | | 15 | 25 | | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong$ - 10 A, V_{GEN} = - 10 V, R_g = 1 Ω | | 52 | 80 | | |
| Fall Time | t _f | | | 14 | 25 | | |
| Drain-Source Body Diode Characteristic | s | | | L | | | |
| Continuous Source-Drain Diode Current | I _S | T _C = 25 °C | | | - 5.8 | ۸ | |
| Pulse Diode Forward Current | I _{SM} | | | | - 60 | Α | |
| Body Diode Voltage | V_{SD} | I _S = - 10 A, V _{GS} = 0 V | | - 0.78 | - 1.2 | V | |
| Body Diode Reverse Recovery Time | t _{rr} | | | 35 | 53 | ns | |
| Body Diode Reverse Recovery Charge | Q _{rr} | I _F = - 10 A, dl/dt = 100 A/μs, T _J = 25 °C | | 25 | 38 | nC | |
| Reverse Recovery Fall Time | t _a | | | 19 | | | |
| Reverse Recovery Rise Time | t _b | | | 16 | | ns | |

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

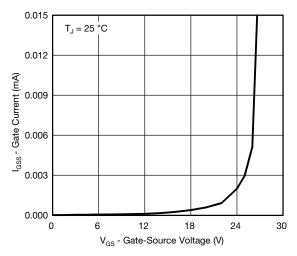
a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing.

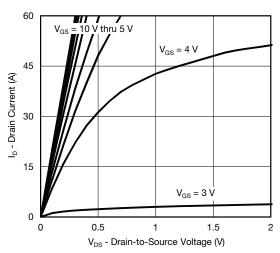


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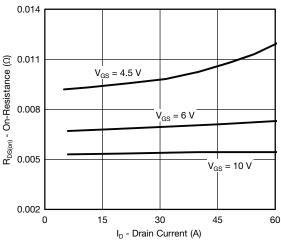
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



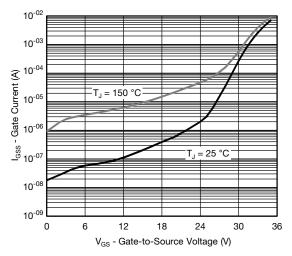
Gate Current vs. Gate-Source Voltage



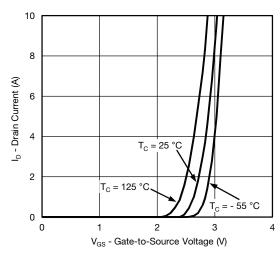
Output Characteristics



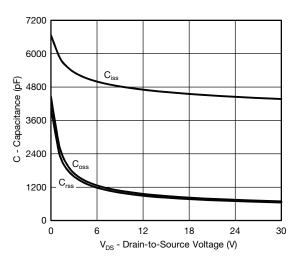
On-Resistance vs. Drain Current



Gate Current vs. Gate-Source Voltage



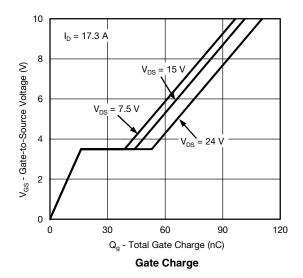
Transfer Characteristics

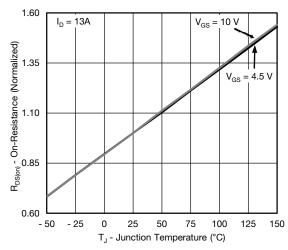


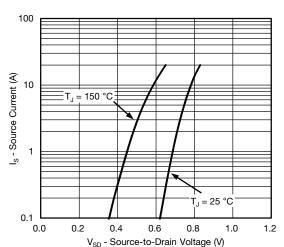
Capacitance

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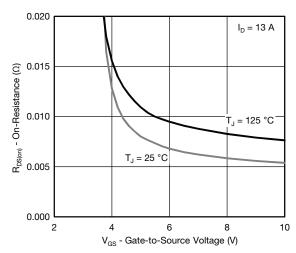
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



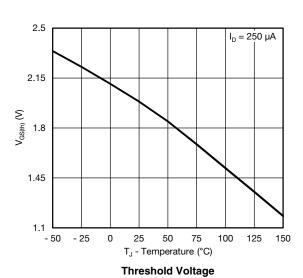




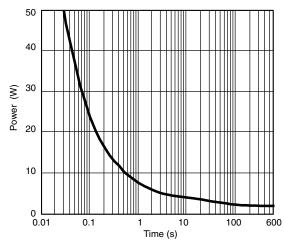
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

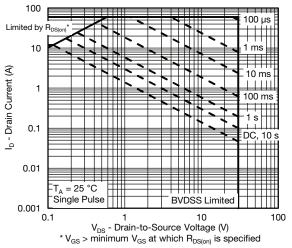


Single Pulse Power, Junction-to-Ambient

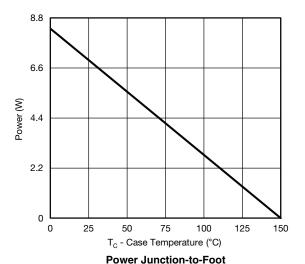


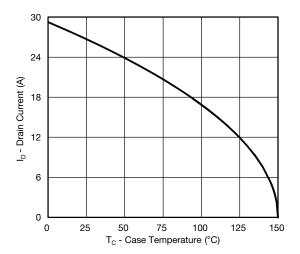
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

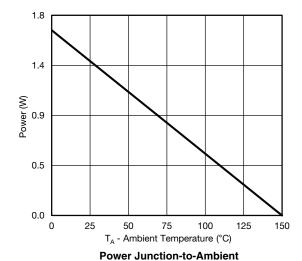


Safe Operating Area, Junction-to-Ambient





Current Derating*

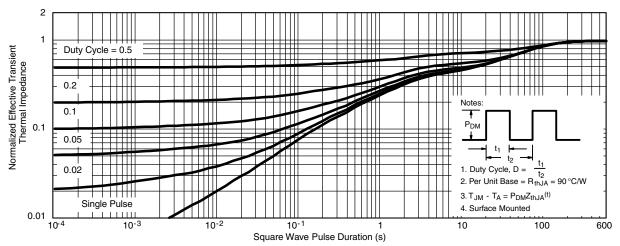


 $^{^*}$ The power dissipation P_D is based on $T_{J(max.)}$ = 150 $^{\circ}$ C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

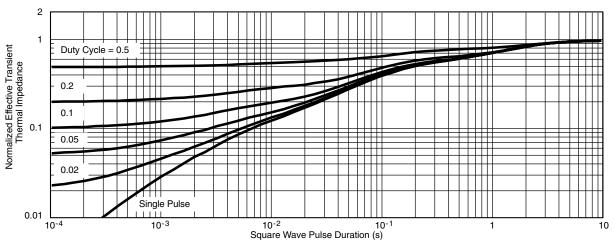
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

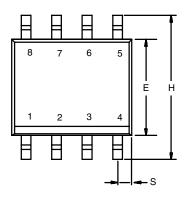


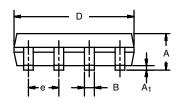
Normalized Thermal Transient Impedance, Junction-to-Foot

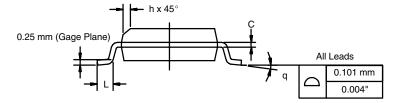
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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







| | MILLIM | IETERS | INCHES | | | |
|--------------------------------|--------|--------|--------|-----------|--|--|
| DIM | Min | Max | Min | Max | | |
| Α | 1.35 | 1.75 | 0.053 | 0.069 | | |
| A ₁ | 0.10 | 0.20 | 0.004 | 0.008 | | |
| В | 0.35 | 0.51 | 0.014 | 0.020 | | |
| С | 0.19 | 0.25 | 0.0075 | 0.010 | | |
| D | 4.80 | 5.00 | 0.189 | 0.196 | | |
| Е | 3.80 | 4.00 | 0.150 | 0.157 | | |
| е | 1.27 | BSC | 0.050 | 0.050 BSC | | |
| Н | 5.80 | 6.20 | 0.228 | 0.244 | | |
| h | 0.25 | 0.50 | 0.010 | 0.020 | | |
| L | 0.50 | 0.93 | 0.020 | 0.037 | | |
| q | 0° | 8° | 0° | 8° | | |
| S | 0.44 | 0.64 | 0.018 | 0.026 | | |
| ECN: C-06527-Rev. I. 11-Sep-06 | | | | | | |

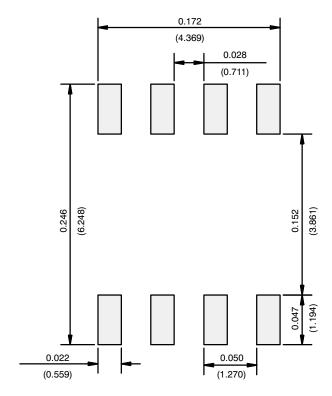
DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06

LON NOTE



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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