

BCR5AS-14A

Triac
Medium Power Use

R07DS0671EJ0100 Rev.1.00 Jul 23, 2012

Features

• $I_{T (RMS)}$: 5 A

• I_{FGTI}, I_{RGTI}, I_{RGT III}: 30 mA

- Non-Insulated Type
- Planar Passivation Type

Outline

RENESAS Package code: PRSS0004ZG-A (Package name: MP-3A)





- 1. T₁ Terminal
- 2. T₂ Terminal
- 3. Gate Terminal
- 4. T₂ Terminal

Applications

Hybrid IC, Solid state relay, Switching mode power supply, light dimmer, electronic switch, electric fans, electronic blanket, and Washing machine, small motor controller □ and other general purpose control applications

Maximum Ratings

Parameter	Symbol	Voltage class	Unit
r arameter	Symbol	14	
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	700	V
Non-repetitive peak off-state voltage ^{Note1}	V_{DSM}	840	V

Notes: 1. Gate open.

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	5	Α	Commercial frequency, sine full wave
				360°conduction, Tc = 103°C
Surge on-state current	I _{TSM}	50	Α	60Hz sinewave 1 full cycle, peak value,
				non-repetitive
I ² t for fusion	l ² t	10.4	A ² s	Value corresponding to 1 cycle of half
				wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	3	W	
Average gate power dissipation	P _{G (AV)}	0.3	W	
Peak gate voltage	V_{GM}	10	V	
Peak gate current	I_{GM}	2	Α	
Junction Temperature	Tj	-40 to +125	°C	
Storage temperature	Tstg	-40 to +125	°C	
Mass	_	0.26	g	Typical value

Electrical Characteristics

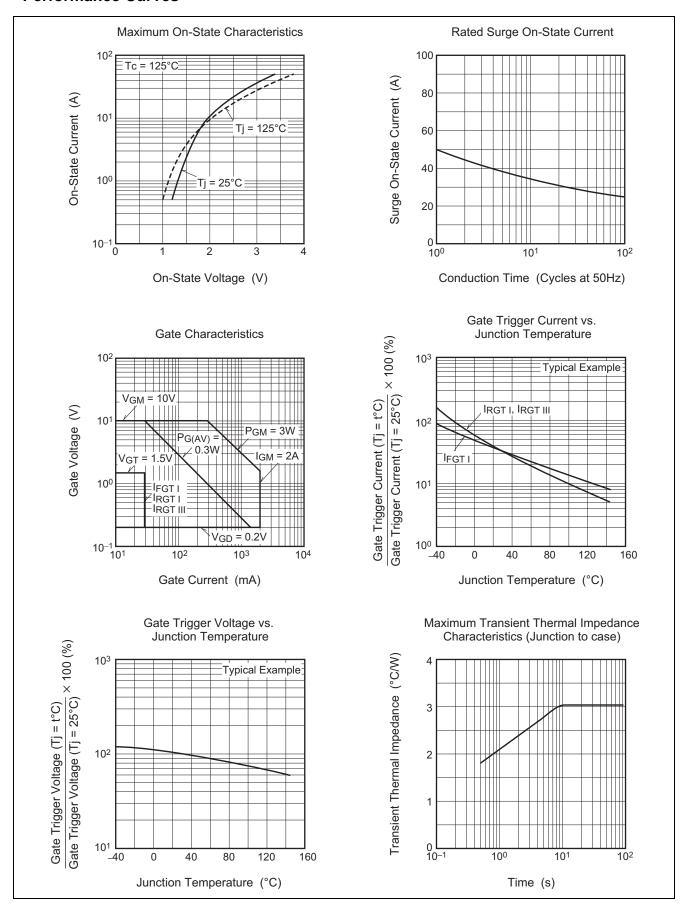
Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state of	current	I _{DRM}	_	_	2.0	mA	Tj = 125°C, V _{DRM} applied
On-state voltage		V _{TM}	_	_	1.8	V	Tc = 25°C, I _{TM} = 7 A, instantaneous measurement
Gate trigger voltage ^{Note2}	I	$V_{FGT_{\mathrm{I}}}$	_	_	1.5	V	$Tj = 25$ °C, $V_D = 6$ V, $R_L = 6$ Ω,
	II	$V_{RGT_{\mathrm{I}}}$		_	1.5	V	$R_G = 330 \Omega$
	III	$V_{RGT_{III}}$	_	_	1.5	V	
Gate trigger curent ^{Note2}	I	I_{FGTI}	_	_	30	mA	Tj = 25°C, V_D = 6 V, R_L = 6 Ω,
	II	I_{RGTI}	_	_	30	mA	$R_G = 330 \Omega$
	III	I _{RGTIII}	_	_	30	mA	
Gate non-trigger voltage		V_{GD}	0.2	_	_	V	Tj = 125°C, V _D = 1/2 V _{DRM}
Thermal resistance		R _{th (j-c)}	_	_	3.0	°C/W	Junction to case ^{Note3}
Critical-rate of rise of off-s commutation voltage ^{Note4}	tate	(dv/dt)c	5	_	_	V/μs	Tj = 125°C

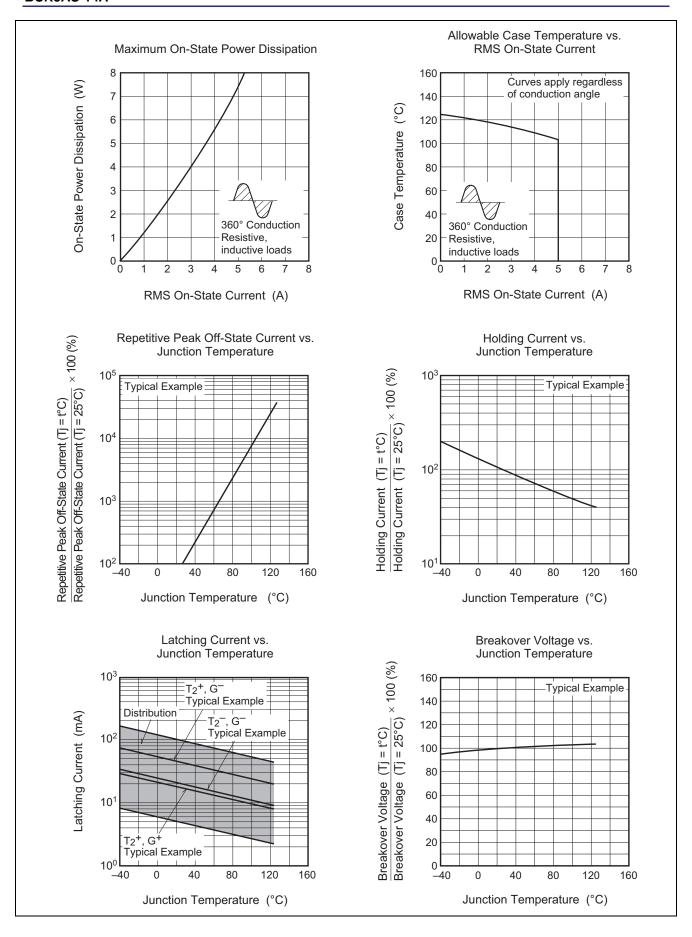
Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

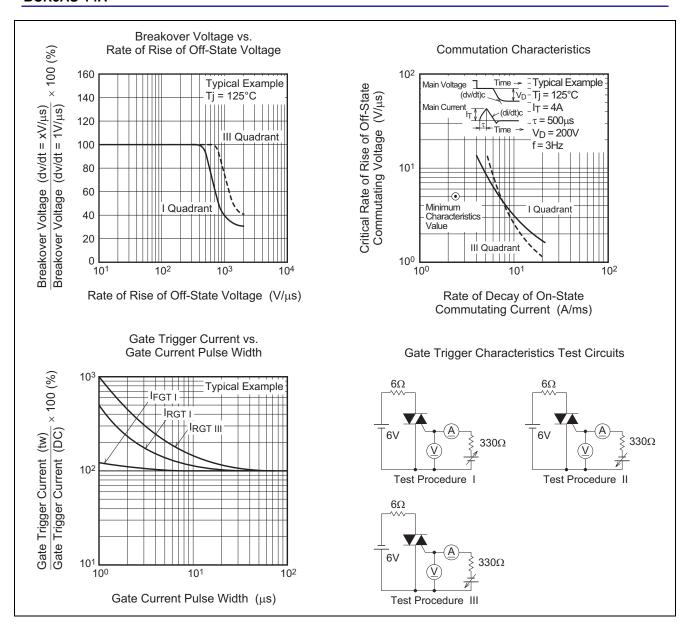
- 3. Case temperature is measured on the T2 tab.
- 4. Test conditions of the critical-rate of rise of off-state commutation voltage is shown in the table below.

Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125°C	Supply Voltage →Time
2. Rate of decay of on-state commutating current (di/dt)c = -2.5 A/ms	Main Current (di/dt)c → Time
3. Peak off-state voltage $V_D = 400 \text{ V}$	Main Voltage Time

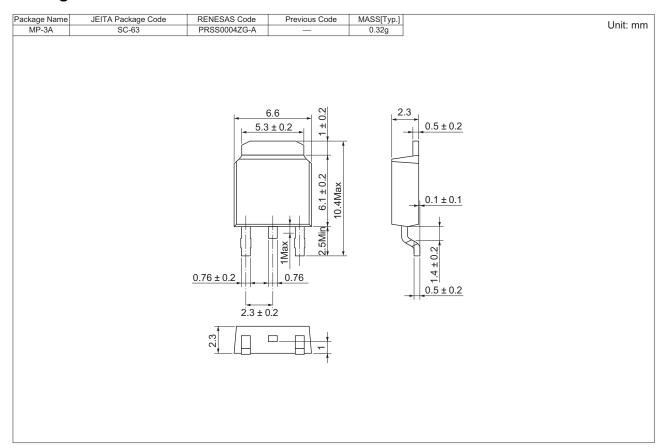
Performance Curves







Package Dimensions



Ordering Information

Orderable Part Number	Packing	Quantity	Remark
BCR5AS-14A#B00	Tube	75 pcs.	MP-3A package
BCR5AS-14A -T13#B00	Embossed Tape	3000 pcs.	MP-3A package, Taping direction "T1"

Note: Please confirm the specification about the shipping in detail.

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