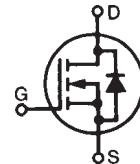


HiPerFET™ Power MOSFETs Q-Class

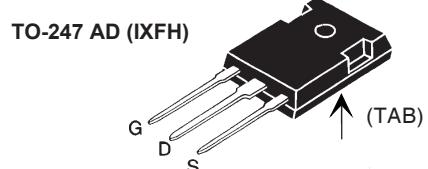
N-Channel Enhancement Mode
Avalanche Rated,
Low Q_g , High dv/dt

IXFH23N80Q
IXFT23N80Q

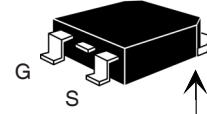
V_{DSS} = 800 V
 I_{D25} = 23 A
 $R_{DS(on)}$ = 0.42 Ω



$t_{rr} \leq 250$ ns



TO-247 AD (IXFH)



TO-268 (D3) (IXFT)

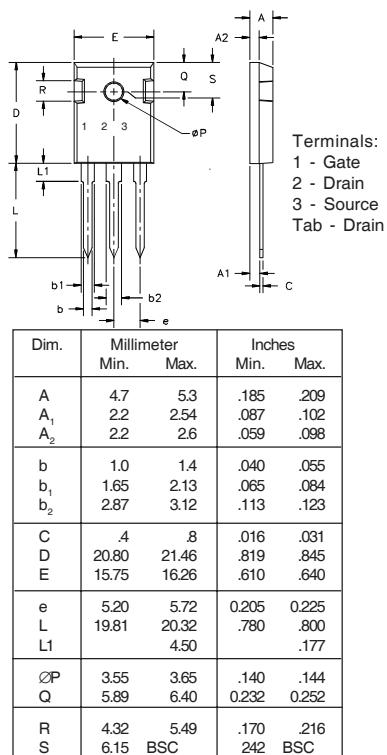
G = Gate
S = Source

TAB = Drain

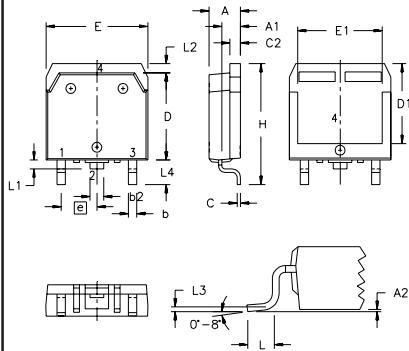
Symbol	Test Conditions	Maximum Ratings		
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	800	V	
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C ; $R_{GS} = 1 \text{ M}\Omega$	800	V	
V_{GS}	Continuous	± 30	V	
V_{GSM}	Transient	± 40	V	
I_{D25}	$T_c = 25^\circ\text{C}$	23	A	
I_{DM}	$T_c = 25^\circ\text{C}$, pulse width limited by T_{JM}	92	A	
I_{AR}	$T_c = 25^\circ\text{C}$	23	A	
E_{AR}	$T_c = 25^\circ\text{C}$	45	mJ	
E_{AS}	$T_c = 25^\circ\text{C}$	1.5	J	
dv/dt	$I_s \leq I_{DM}$, $di/dt \leq 100 \text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$, $R_G = 2 \Omega$	5	V/ns	
P_D	$T_c = 25^\circ\text{C}$	500	W	
T_J		-55 ... +150	$^\circ\text{C}$	
T_{JM}		150	$^\circ\text{C}$	
T_{stg}		-55 ... +150	$^\circ\text{C}$	
T_L	1.6 mm (0.063 in) from case for 10 s	300	$^\circ\text{C}$	
M_d	Mounting torque	TO-247	1.13/10 Nm/lb.in.	
F_c	Mounting Force	TO-268	20...120/4.5...27 N/lb	
Weight		TO-247 TO-268	6 4	g

Symbol	Test Conditions	Characteristic Values		
		($T_J = 25^\circ\text{C}$, unless otherwise specified)	min.	typ.
V_{DSS}	$V_{GS} = 0 \text{ V}$, $I_D = 250 \mu\text{A}$	800		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 3 \text{ mA}$	2.5		4.5 V
I_{GSS}	$V_{GS} = \pm 30 \text{ V}_{DC}$, $V_{DS} = 0$		± 100	nA
I_{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V}$	$T_J = 25^\circ\text{C}$ $T_J = 125^\circ\text{C}$	25 1	μA mA
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}$, $I_D = 0.5 \cdot I_{D25}$ Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2 \%$		0.42	Ω

Symbol	Test Conditions	Characteristic Values		
		min.	typ.	max.
g_{fs}	$V_{DS} = 10 \text{ V}; I_D = 0.5 \cdot I_{D25}$, pulse test	18	26	S
C_{iss} C_{oss} C_{rss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	4900	pF	
		500	pF	
		130	pF	
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 0.5 \cdot I_{D25}$ $R_G = 1.5 \Omega$ (External),	28	ns	
		27	ns	
		74	ns	
		14	ns	
$Q_{g(on)}$ Q_{gs} Q_{gd}	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 0.5 \cdot I_{D25}$	130	nC	
		26	nC	
		55	nC	
R_{thJC}			0.25	K/W
R_{thCK}	TO-247	0.25		K/W

TO-247 AD (IXFH) Outline

Source-Drain Diode
Characteristic Values
 $(T_J = 25^\circ\text{C}$, unless otherwise specified)

Symbol	Test Conditions	min.	typ.	max.
I_s	$V_{GS} = 0 \text{ V}$		23	A
I_{SM}	Repetitive; pulse width limited by T_{JM}		92	A
V_{SD}	$I_F = I_s, V_{GS} = 0 \text{ V}$, Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2 \%$		1.5	V
t_{rr} Q_{RM} I_{RM}	$I_F = I_s - di/dt = 100 \text{ A}/\mu\text{s}, V_R = 100 \text{ V}$	1	250	ns
		9		μC
				A

TO-268 (IXFT) Outline


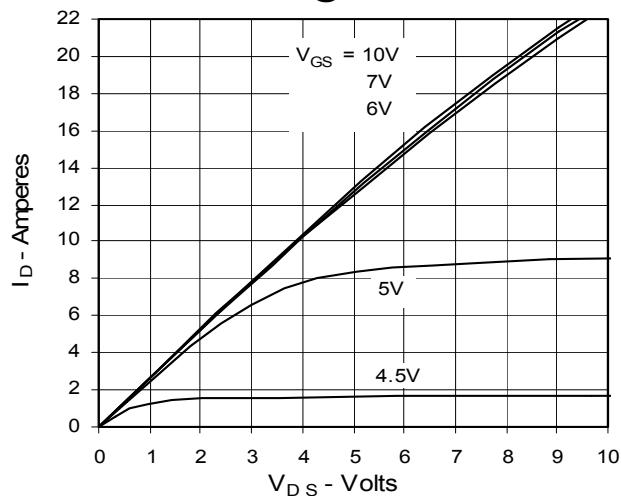
SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.193	.201	4.90	5.10
A1	.106	.114	2.70	2.90
A2	.001	.010	0.02	0.25
b	.045	.057	1.15	1.45
b2	.075	.083	1.90	2.10
C	.016	.026	0.40	0.65
C2	.057	.063	1.45	1.60
D	.543	.551	13.80	14.00
D1	.488	.500	12.40	12.70
E	.624	.632	15.85	16.05
E1	.524	.535	13.30	13.60
e	.215	BSC		5.45 BSC
H	.736	.752	18.70	19.10
L	.094	.106	2.40	2.70
L1	.047	.055	1.20	1.40
L2	.039	.045	1.00	1.15
L3	.010	BSC		0.25 BSC
L4	.150	.161	3.80	4.10

IXYS reserves the right to change limits, test conditions, and dimensions.

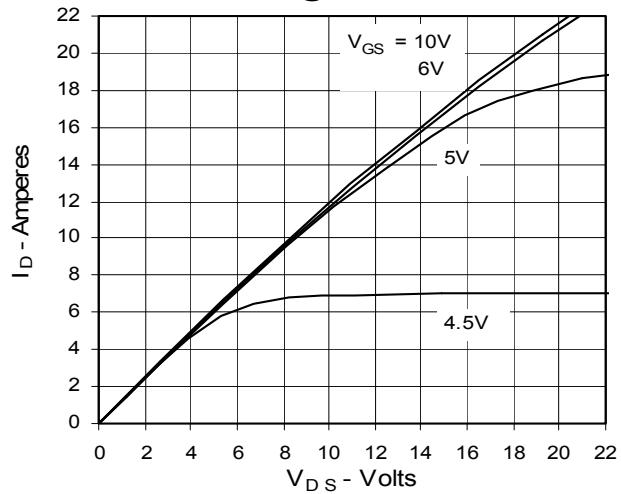
 IXYS MOSFETs and IGBTs are covered by one or more
of the following U.S. patents:

 4,835,592 4,881,106 5,017,508 5,049,961 5,187,117 5,486,715 6,306,728B1 6,259,123B1 6,306,728B1
 4,850,072 4,931,844 5,034,796 5,063,307 5,237,481 5,381,025 6,404,065B1 6,162,665 6,534,343

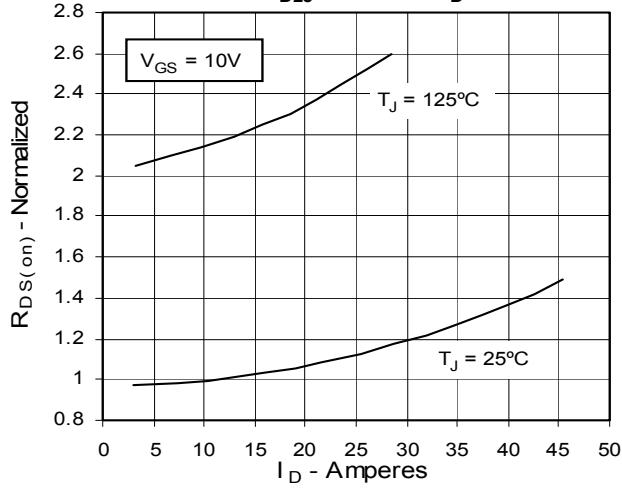
**Fig. 1. Output Characteristics
@ 25°C**



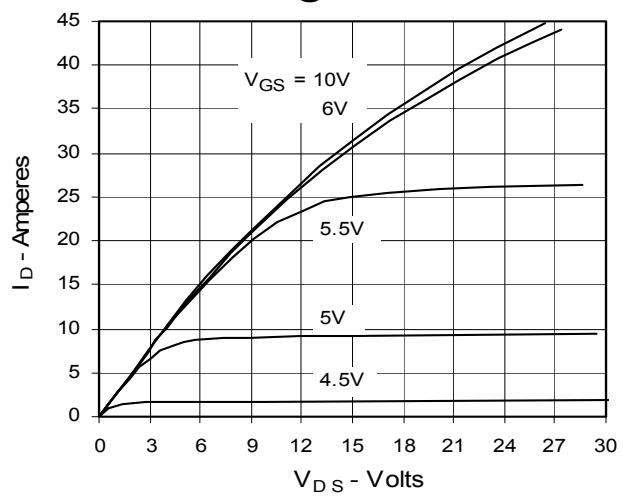
**Fig. 3. Output Characteristics
@ 125°C**



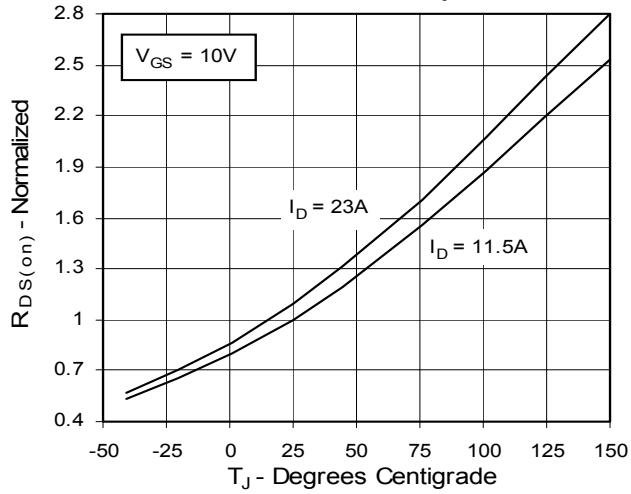
**Fig. 5. $R_{DS(on)}$ Normalized to
0.5 I_{D25} Value vs. I_D**



**Fig. 2. Extended Output Characteristics
@ 25°C**



**Fig. 4. $R_{DS(on)}$ Normalized to 0.5 I_{D25}
Value vs. Junction Temperature**



**Fig. 6. Drain Current vs. Case
Temperature**

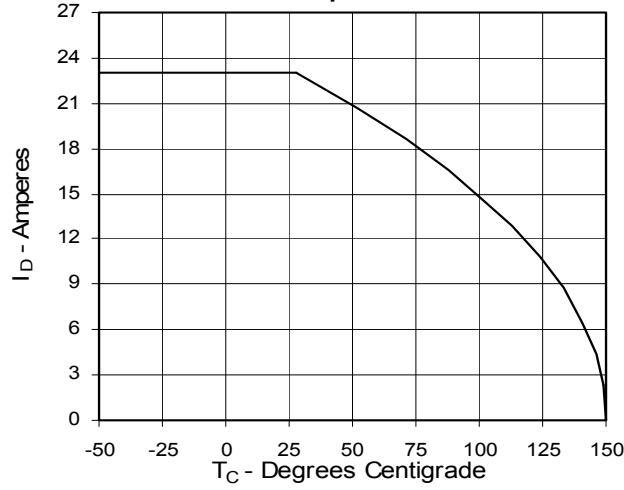
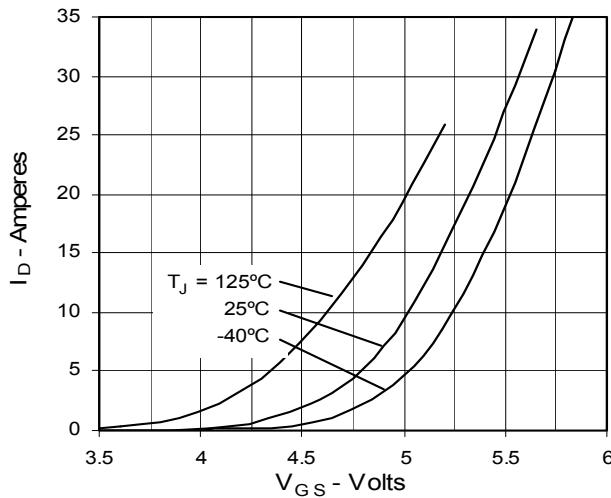
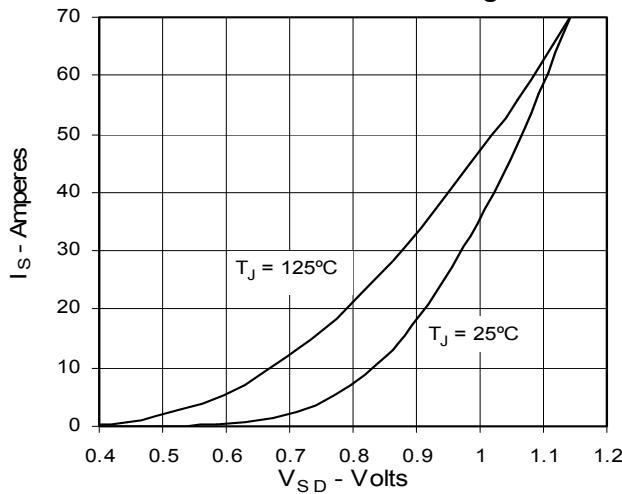
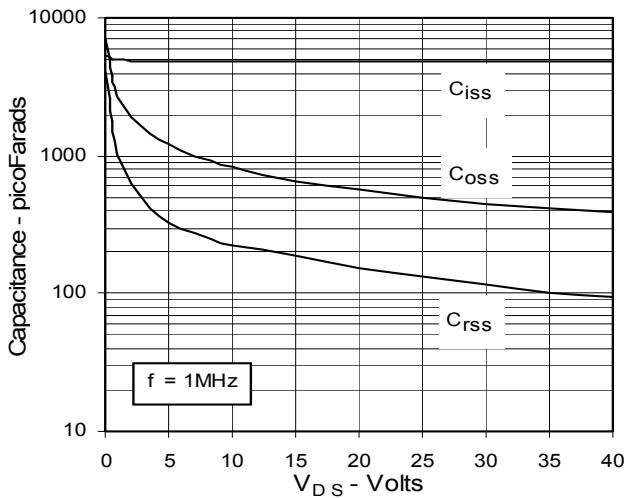
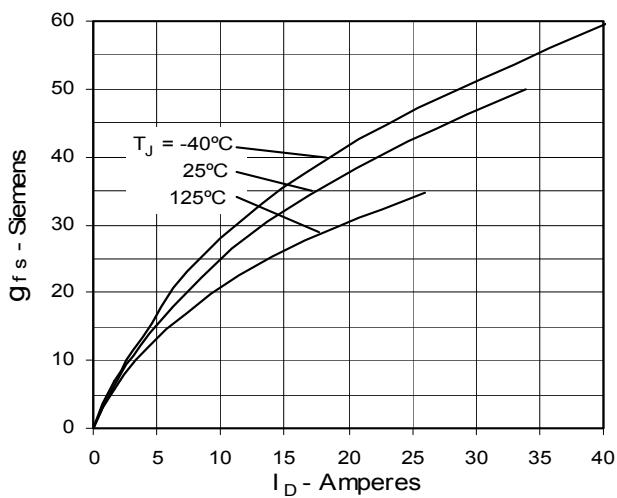
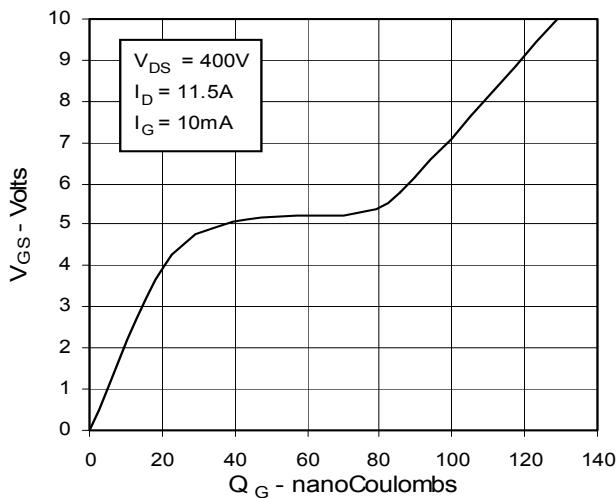


Fig. 7. Input Admittance

Fig. 9. Source Current vs. Source-To-Drain Voltage

Fig. 11. Capacitance

Fig. 8. Transconductance

Fig. 10. Gate Charge


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4,850,072	4,931,844	5,034,796	5,063,307	5,237,481	5,381,025	6,404,065B1	6,162,665	6,534,343

Fig. 12. Maximum Transient Thermal Resistance