



DMN601TK

#### N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C		
60V	2Ω @ V <sub>GS</sub> = 10V	0.3A		
000	3Ω @ V <sub>GS</sub> = 5V	0.2A		

### **Features**

- Low On-Resistance: RDS(ON)
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected Up To 2kV**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- **Power Management Functions**

### **Mechanical Data**

- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 🞉
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)

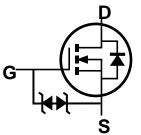
**SOT523** 



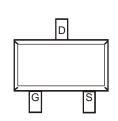




Top View



Gate Protection Diode



Top View Pin Out Configuration

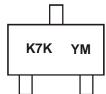
## **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN601TK-7	SOT523	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



K7K = Product Type Marking Code YM = Date Code Marking Y = Year (ex: S = 2005)M = Month (ex: 9 = September)

_	Date Code Key	,												
	Year	200	05	2006		2014	2015	2016	201	7 20	)18	2019	2020	2021
	Code	S	;	Т		В	С	D	Е		F	G	Н	I
	Month		Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
ſ	Code		1	2	3	4	5	6	7	8	9	0	N	D



# 

Characteristic		Symbol	Value	Units
Drain-Source Voltage		$V_{DSS}$	60	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Drain Current (Note 5)	Continuous Pulsed (Note 6)	I <sub>D</sub>	300 800	mA

# Thermal Characteristics (@TA = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P <sub>D</sub>	150	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	833	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

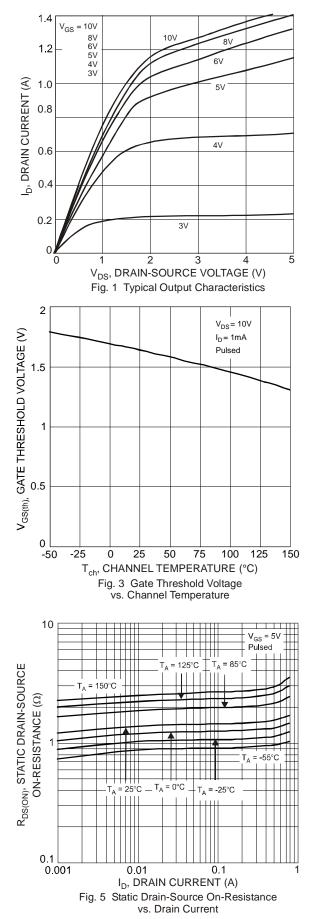
## Electrical Characteristics (@T<sub>A</sub> = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)		ı		ı		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.0	1.6	2.5	V	$V_{DS} = 10V$ , $I_D = 1mA$
Static Drain-Source On-Resistance			_	2.0	Ω	$V_{GS} = 10V, I_D = 0.5A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		_	3.0		$V_{GS} = 5V, I_D = 0.05A$
Forward Transfer Admittance	Y <sub>FS</sub>	80	_	_	ms	$V_{DS} = 10V, I_D = 0.2A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>ISS</sub>	_	_	50	pF	
Output Capacitance	Coss		_	25	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	C <sub>RSS</sub>		_	5.0	pF	1 = 1.000112
Turn-On Delay Time	t <sub>D(ON)</sub>		3.4		ns	
Turn-On Rise Time	t <sub>R</sub>		2.4		ns	VDD = 25V, VGS = 10V,
Turn-Off Delay Time	t <sub>D(OFF)</sub>		11.0		ns	$R_G = 25\Omega$ , $I_D = 500 \text{mA}$
Turn-Off Fall Time	t <sub>F</sub>	_	4.9	_	ns	

Notes:

- 5. Device mounted on FR-4 PCB.
- Delice modified of IN-4+ PC/S.
   Pulse width ≤10µS, Duty Cycle ≤1%.
   Short duration pulse test used to minimize self-heating effect.
   Guaranteed by design. Not subject to product testing.





1.00

V<sub>DS</sub> = 10V

Pulsed

T<sub>A</sub> = 125°C

T<sub>A</sub> = 75°C

T<sub>A</sub> = 25°C

0.01

1.5 2 2.5 3 3.5 4 4.5 5

V<sub>GS</sub>, GATE-SOURCE VOLTAGE (V)

Fig. 2 Typical Transfer Characteristics

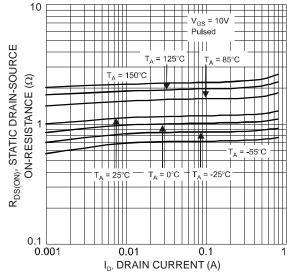
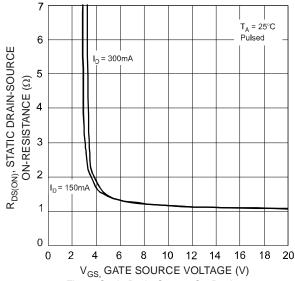


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current





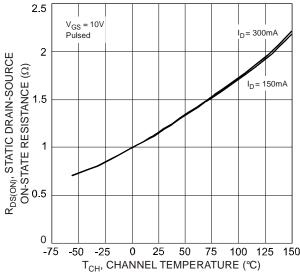
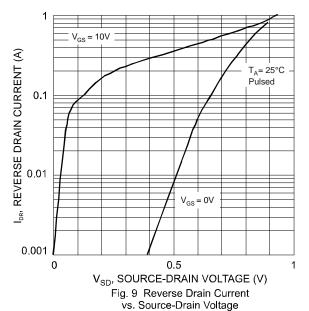
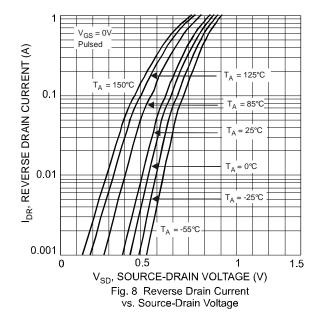


Fig. 7 Static Drain-Source On-State Resistance vs. Channel Temperature





0.01

V<sub>SS</sub> = 10V

Pulsed

T<sub>A</sub> = 25°C

T<sub>A</sub> = 150°C

T<sub>A</sub>

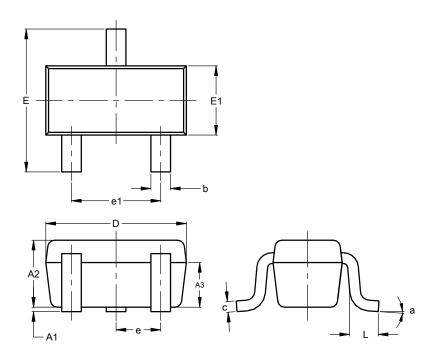
vs. Drain Current



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### **SOT523**

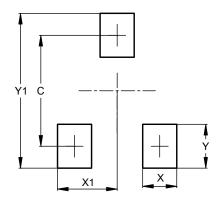


SOT523								
Dim	Min Max Typ							
Α	0.60	0.80	0.75					
A1	0.00	0.10	0.05					
A3	0.45	0.65	0.50					
b	0.15 0.30 0.22							
С	0.10 0.20 0.12							
D	1.50	1.70	1.60					
E	1.45	1.75	1.60					
E1	0.75	0.85	0.80					
е		0.50 BS	С					
e1	0.90 1.10 1.00							
L	0.20	0.33						
а	0°		8°					
All Dimensions in mm								

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT523**



Dimensions	Value
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80



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