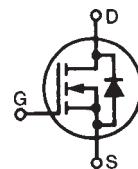


# PolarHV™ Power MOSFET

N-Channel Enhancement Mode  
Avalanche Rated

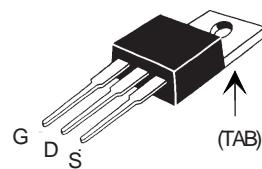
**IXTP 1R6N50P**  
**IXTY 1R6N50P**

**V<sub>DSS</sub>** = 500 V  
**I<sub>D25</sub>** = 1.6 A  
**R<sub>DS(on)</sub>** ≤ 6.5 Ω

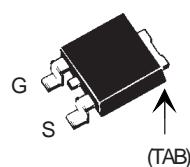


Symbol	Test Conditions	Maximum Ratings		
<b>V<sub>DSS</sub></b>	T <sub>J</sub> = 25°C to 150°C	500	V	
<b>V<sub>DGR</sub></b>	T <sub>J</sub> = 25°C to 150°C; R <sub>GS</sub> = 1 MΩ	500	V	
<b>V<sub>GS</sub></b>	Continuous	±30	V	
<b>V<sub>GSM</sub></b>	Transient	±40	V	
<b>I<sub>D25</sub></b>	T <sub>C</sub> = 25°C	1.6	A	
<b>I<sub>DM</sub></b>	T <sub>C</sub> = 25°C, pulse width limited by T <sub>JM</sub>	2.5	A	
<b>I<sub>AR</sub></b>	T <sub>C</sub> = 25°C	1.6	A	
<b>E<sub>AR</sub></b>	T <sub>C</sub> = 25°C	5	mJ	
<b>E<sub>AS</sub></b>	T <sub>C</sub> = 25°C	75	mJ	
<b>dv/dt</b>	I <sub>S</sub> ≤ I <sub>DM</sub> , di/dt ≤ 100 A/μs, V <sub>DD</sub> ≤ V <sub>DSS</sub> , T <sub>J</sub> ≤ 150°C, R <sub>G</sub> = 50 Ω	10	V/ns	
<b>P<sub>D</sub></b>	T <sub>C</sub> = 25°C	43	W	
<b>T<sub>J</sub></b>		-55 ... +150	°C	
<b>T<sub>JM</sub></b>		150	°C	
<b>T<sub>stg</sub></b>		-55 ... +150	°C	
<b>T<sub>L</sub></b>	1.6 mm (0.062 in.) from case for 10s	300	°C	
<b>T<sub>SOLD</sub></b>	Plastic body for 10 s	260	°C	
<b>M<sub>d</sub></b>	Mounting torque (TO-220)	1.13/10	Nm/lb.in.	
<b>Weight</b>	TO-252	0.8	g	
	TO-220	4	g	

## TO-220 (IXTP)



## TO-252 (IXTY)



G = Gate  
S = Source

D = Drain  
TAB = Drain

Symbol	Test Conditions (T <sub>J</sub> = 25°C, unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
<b>BV<sub>DSS</sub></b>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	500		V
<b>V<sub>GS(th)</sub></b>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 25 μA	3.0		V
<b>I<sub>GSS</sub></b>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V		±100	nA
<b>I<sub>DSS</sub></b>	V <sub>DS</sub> = V <sub>DSS</sub> V <sub>GS</sub> = 0 V	T <sub>J</sub> = 125°C	1 50	μA
<b>R<sub>DS(on)</sub></b>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 I <sub>D25</sub> Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %		6.5	Ω

## Features

- International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
  - easy to drive and to protect

## Advantages

- Easy to mount
- Space savings
- High power density

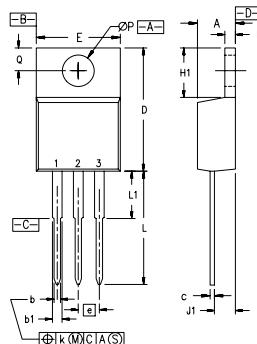
Symbol	Test Conditions	Characteristic Values			
		(T <sub>j</sub> = 25°C, unless otherwise specified)	Min.	Typ.	Max.
<b>g<sub>fs</sub></b>	V <sub>DS</sub> = 20 V; I <sub>D</sub> = 0.5 I <sub>D25</sub> , pulse test	0.7	1.3	S	
<b>C<sub>iss</sub></b> <b>C<sub>oss</sub></b> <b>C<sub>rss</sub></b>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz	140	pF		
		20	pF		
		2.6	pF		
<b>t<sub>d(on)</sub></b> <b>t<sub>r</sub></b> <b>t<sub>d(off)</sub></b> <b>t<sub>f</sub></b>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 0.5 V <sub>DSS</sub> , I <sub>D</sub> = 0.5 I <sub>D25</sub> R <sub>G</sub> = 50 Ω (External)	20	ns		
		26	ns		
		45	ns		
		23	ns		
<b>Q<sub>g(on)</sub></b> <b>Q<sub>gs</sub></b> <b>Q<sub>gd</sub></b>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 0.5 V <sub>DSS</sub> , I <sub>D</sub> = 0.5 I <sub>D25</sub>	3.9	nC		
		1.4	nC		
		1.3	nC		
<b>R<sub>thJC</sub></b> <b>R<sub>thJS</sub></b>	(TO-220)		2.9 °C/W		
		0.25	°C/W		

### Source-Drain Diode

**Characteristic Values**  
(T<sub>j</sub> = 25°C, unless otherwise specified)

Symbol	Test Conditions	Min.	Typ.	Max.
I <sub>s</sub>	V <sub>GS</sub> = 0 V		1.6	A
I <sub>SM</sub>	Repetitive		5.0	A
V <sub>SD</sub>	I <sub>F</sub> = I <sub>S</sub> , V <sub>GS</sub> = 0 V, Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %		1.5	V
t <sub>rr</sub>	I <sub>F</sub> = 1.6 A, -di/dt = 100 A/μs V <sub>R</sub> = 100V	400		ns

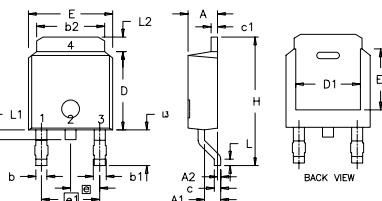
### TO-220 (IXTP) Outline



Pins: 1 - Gate      2 - Drain  
3 - Source      4 - Drain

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.170	.190	4.32	4.83
b	.025	.040	0.64	1.02
b1	.045	.065	1.15	1.65
c	.014	.022	0.35	0.56
D	.580	.630	14.73	16.00
E	.390	.420	9.91	10.66
e	.100 BSC		2.54 BSC	
F	.045	.055	1.14	1.40
H1	.230	.270	5.85	6.85
J1	.090	.110	2.29	2.79
K	0	.015	0	0.38
L	.500	.550	12.70	13.97
L1	.110	.230	2.79	5.84
ØP	.139	.161	3.53	4.08
Q	.100	.125	2.54	3.18

### TO-252 AA (IXTY) Outline



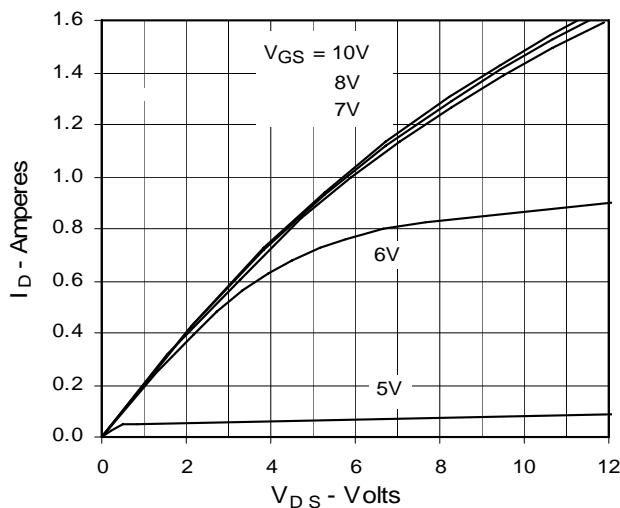
Pins: 1 - Gate      2 - Drain  
3 - Source      4 - Drain

Dim.	Millimeter Min.	Max.	Inches Min.	Max.
A	2.19	2.38	0.086	0.094
A1	0.89	1.14	0.035	0.045
A2	0	0.13	0	0.005
b	0.64	0.89	0.025	0.035
b1	0.76	1.14	0.030	0.045
b2	5.21	5.46	0.205	0.215
c	0.46	0.58	0.018	0.023
c1	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
D1	4.32	5.21	0.170	0.205
E	6.35	6.73	0.250	0.265
E1	4.32	5.21	0.170	0.205
e	2.28 BSC		0.090 BSC	
e1	4.57 BSC		0.180 BSC	
H	9.40	10.42	0.370	0.410
L	0.51	1.02	0.020	0.040
L1	0.64	1.02	0.025	0.040
L2	0.89	1.27	0.035	0.050
L3	2.54	2.92	0.100	0.115

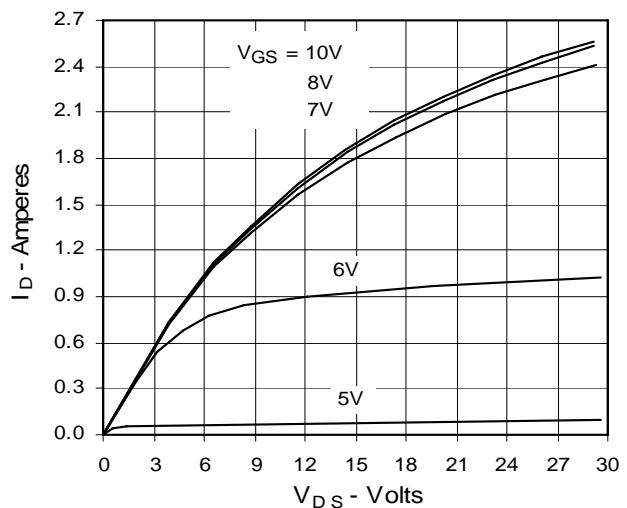
IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by 4,835,592 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065 B1 6,683,344 6,727,585 one or more of the following U.S. patents: 4,850,072 5,017,508 5,063,307 5,381,025 6,259,123 B1 6,534,343 6,710,405B2 6,759,692 4,881,106 5,034,796 5,187,117 5,486,715 6,306,728 B1 6,583,505 6,710,463 6,771,478 B2

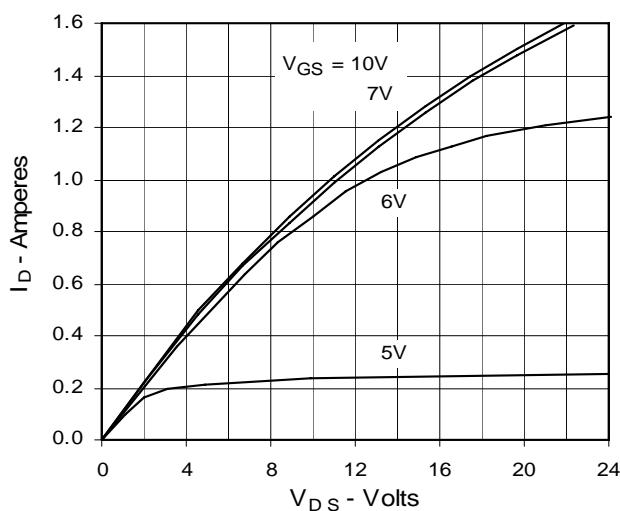
**Fig. 1. Output Characteristics  
@ 25°C**



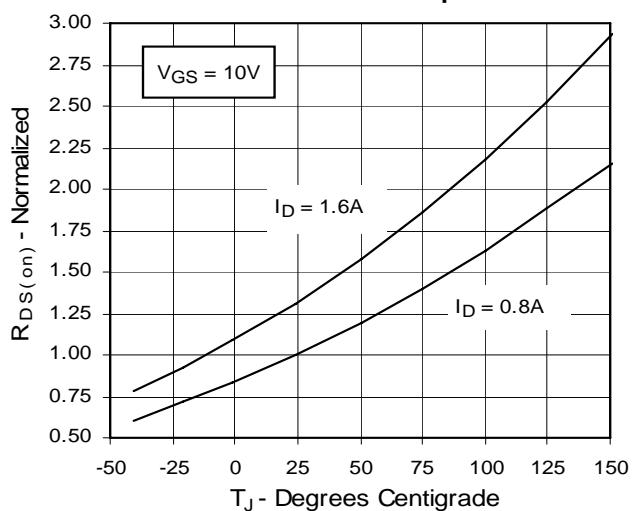
**Fig. 2. Extended Output Characteristics  
@ 25°C**



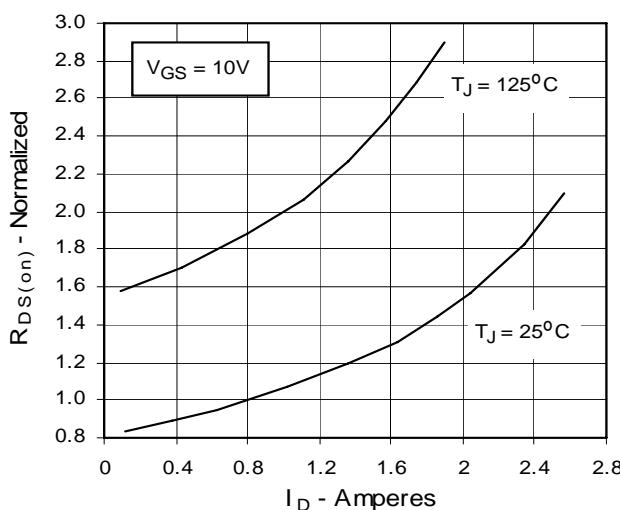
**Fig. 3. Output Characteristics  
@ 125°C**



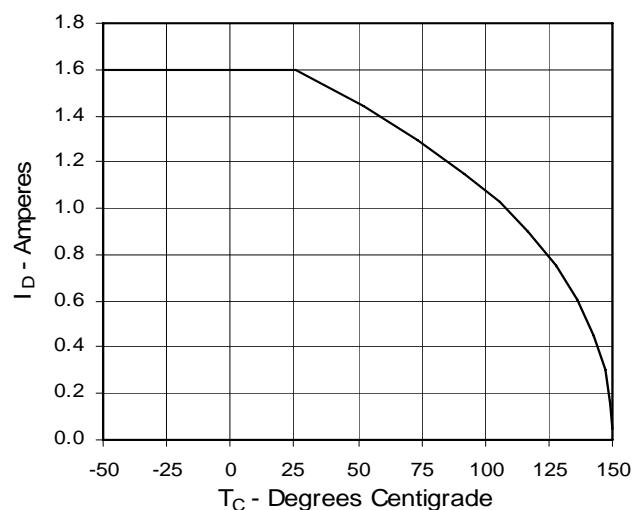
**Fig. 4.  $R_{DS(on)}$  Normalized to 0.5  $I_{D25}$  Value vs. Junction Temperature**

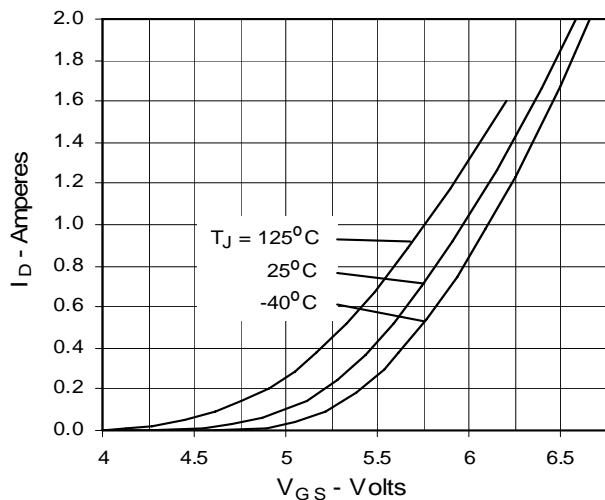
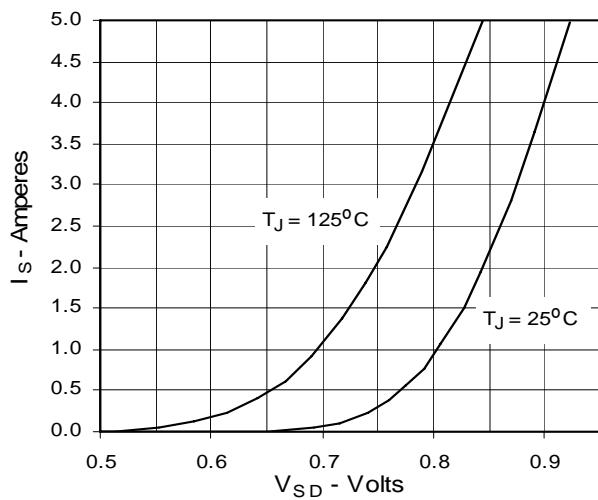
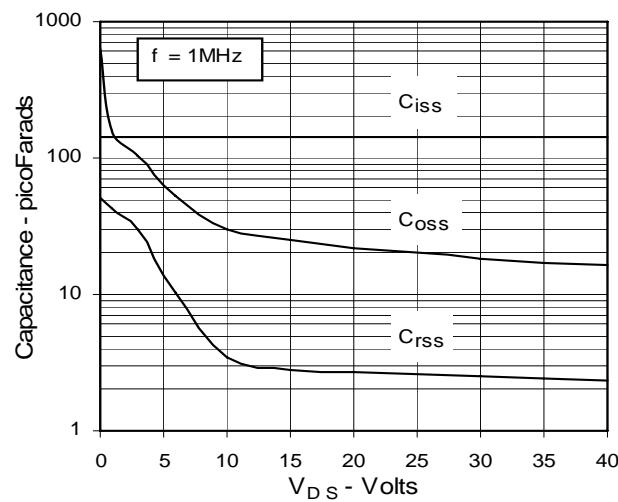
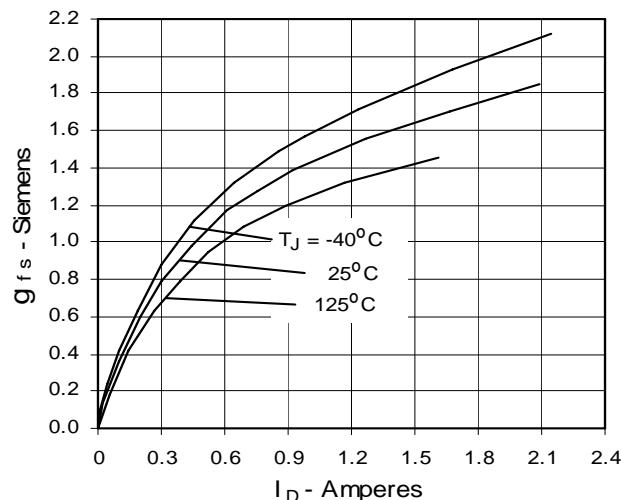
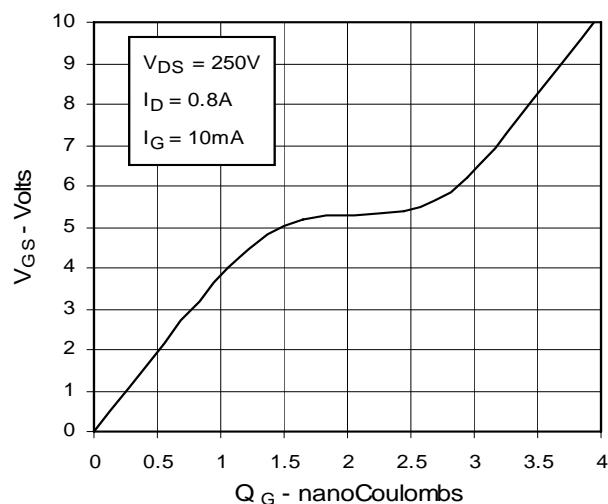
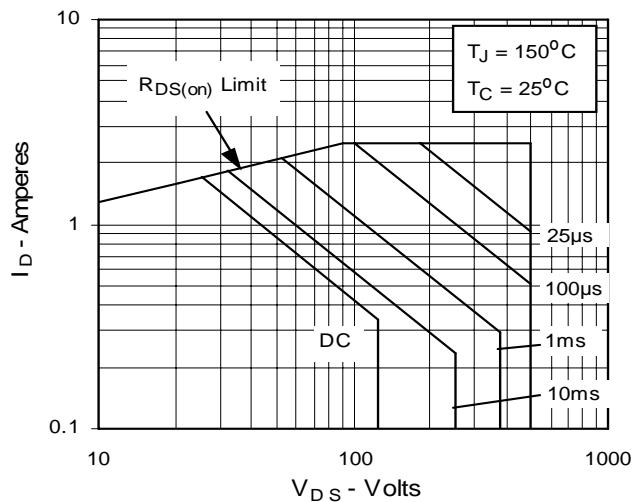


**Fig. 5.  $R_{DS(on)}$  Normalized to  
0.5  $I_{D25}$  Value vs.  $I_D$**



**Fig. 6. Drain Current vs. Case Temperature**



**Fig. 7. Input Admittance**

**Fig. 9. Source Current vs. Source-To-Drain Voltage**

**Fig. 11. Capacitance**

**Fig. 8. Transconductance**

**Fig. 10. Gate Charge**

**Fig. 12. Forward-Bias Safe Operating Area**


**Fig. 13. Maximum Transient Thermal Resistance**