

S1D13A05

S1D13A05 QVGA LCD Controller

The S1D13A05 is an LCD solution designed for seamless connection to a wide variety of microprocessors. The S1D13A05 integrates an LCD graphics controller with an embedded 256K byte SRAM display buffer. The LCD controller supports TFT and passive panel types and includes a Hardware Acceleration Engine to greatly improve screen drawing functions.

The S1D13A05 utilizes a guaranteed low-latency CPU architecture that provides support for microprocessors without READY/WAIT# handshaking signals. The 32-bit internal data path, write buffer and the Hardware Acceleration Engine provide high performance bandwidth into display memory allowing for fast display updates.

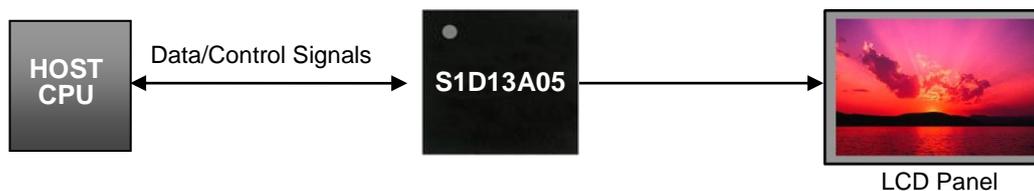
Additionally, products requiring a rotated display can take advantage of the SwivelView™ feature which provides hardware rotation of the display memory transparent to the software application. The S1D13A05 also provides support for Picture-in-Picture (a variable size Overlay window).

The S1D13A05's impartiality to CPU type or operating system makes it an ideal display solution for a wide variety of applications in embedded markets.

FEATURES

- Embedded 256KB Display Buffer
- Low Operating Voltage
- Low-latency CPU interface
- Direct support for multiple CPU types
- TFT panel support
- Passive LCD panel support
- Programmable resolutions and color depths
- USB Client, Revision 1.1 compliant
- Picture-in-Picture
- SwivelView™ (90°, 180°, 270° hardware rotation of displayed image)
- Pixel Doubling
- Hardware Acceleration Engine
- Software Initiated Power Save Mode
- Software Video Invert
- 121-pin PFBGA package

SYSTEM BLOCK DIAGRAM



S1D13A05 Features

- 256 KB SRAM
- Hardware Acceleration Engine
- SwivelView Hardware Rotation
- Picture-in-Picture Overlay Window
- Double Buffering
- Pixel Doubling



DESCRIPTION

Display Buffer

- Embedded 256K byte SRAM display buffer

CPU Interface

- Fixed low-latency CPU access times
- Direct support for a variety of popular interfaces

Display Support

- 1/2/4/8/16 bit-per-pixel (bpp) support
- Up to 64 gray shades on monochrome passive panels
- 9/12/18-bit TFT interface
- Single-panel, single-drive passive displays
 - 4/8-bit monochrome LCD interface
 - 4/8/16-bit color passive LCD interface
- Typical resolutions supported:
 - 320x320 @ 8 bpp
 - 320x240 @ 16 bpp
 - 160x160 @ 16 bpp (2 pages)
 - 160x240 @ 16 bpp

Display Features

- Picture-in-Picture: displays a variable size window overlaid over background image
- SwivelView™: hardware rotation of 90°, 180°, 270°
- Pixel Doubling: horizontal and vertical resolutions can be doubled without any additional memory
- Software video invert
- 2D BitBLT Engine:
 - Write BLT
 - Move BLT
 - Solid Fill BLT
 - Pattern Fill
 - Move BLT with Color Expansion
 - Transparent Write BLT
 - Transparent Move BLT
 - Read BLT
 - Color Expansion BLT

Miscellaneous

- USB Client, Revision 1.1 compliant
- Three independent clock inputs
- Flexible clock source selection with dividers
- Software Initiated Power Save Mode
- COREVDD 2.0 ± 10% or 2.5 ± 10% volts
- IOVDD 3.0 ± 10% volts
- 121-pin PFBGA package

JAPAN
SEIKO EPSON CORPORATION
IC Sales & Marketing Department
421-8 Hino, Hino-shi, Tokyo 191-8501, JAPAN
Phone: +81-42-587-5816 FAX: +81-42-587-5117

CHINA
EPSON (CHINA) CO., LTD
4th Floor, Tower 1 of China Central Place, 81 Jianguo Road,
Chaoyang District, Beijing 100025, CHINA
Phone: +86-10-8522-1199 FAX: +86-10-8522-1120

TAIWAN
EPSON TAIWAN TECHNOLOGY & TRADING LTD.
14F, No. 7, Song Ren Road,
Taipei 110, TAIWAN
Phone: +886-2-8786-6688 FAX: +886-2-8786-6660

AMERICA
EPSON ELECTRONICS AMERICA, INC.
214 Devcon Drive, San Jose, CA 95112,
USA
Phone: +1-800-228-3964 FAX: +1-408-922-0238

CHINA
SHANGHAI Branch
Room 01 & 04 17F, Greenland Center II, 562 Dong An Road,
Shanghai 200032, CHINA
Phone: +86-21-5423-5577 FAX: +86-21-5423-4677

SINGAPORE
EPSON SINGAPORE PTE., LTD.
1 HarbourFront Place,
#03-02 HarbourFront Tower One, SINGAPORE 098633
Phone: +65-6586-5500 FAX: +65-6271-3182

EUROPE
EPSON EUROPE ELECTRONICS GmbH
Riesstrasse 15, 80992 Munich,
GERMANY
Phone: +49-89-14005-0 FAX: +49-89-14005-110

CHINA
SHENZHEN Branch
Room 804-805, 8th Floor, Tower 2, Ali Center,
No.3331 Keyuan South RD (Shenzhen bay), Nanshan District,
Shenzhen 518054, CHINA
Phone: +86-10-3299-0588 FAX: +86-10-3299-0560

KOREA
SEIKO EPSON CORP.
19F, KLI 63 Bldg., 60 Yoido-dong,
Yongdeungpo-Ku, Seoul 150-763, KOREA
Phone: +82-2-784-6027 FAX: +82-2-767-3677

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Document code: X40A-C-001-04.1

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