TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIV)

TPC6107

Notebook PC Applications Portable Equipment Applications

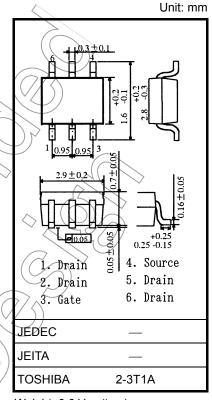
- Low drain-source ON resistance: $R_{DS (ON)} = 40 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: |Y_{fs}| = 9.6 S (typ.)

Small footprint due to small and thin package

- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -20 \text{ V)}$
- Enhancement model: V_{th} = -0.5 to -1.2 V (V_{DS} = -10 V, I_{D} = -200 μA)

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit |
|--|----------------|------------------|------------|------|
| Drain-source voltage | | V _{DSS} | -20 | (V) |
| Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$) | | V_{DGR} | -20 | У |
| Gate-source voltage | | V _{GSS} | ±12 | > V |
| Drain current | DC (Note 1) | ΙD | 4.5 | Α |
| | Pulse (Note 1) | I _{DP} | -18 | A |
| Drain power dissipation (t = 5 s) (Note 2a) | | P _D | 2.2 | XW |
| Drain power dissipation (t = 5 s) (Note 2b) | | PD | 0.7 | W |
| Single pulse avalanche energy (Note 3) | | EAS | 1.3 | mJ |
| Avalanche current | | (IAR)) | -2.25 | A |
| Repetitive avalanche energy (Note 4) | | EAR | 0.22 | mJ |
| Channel temperature | | ch | 150 | °C |
| Storage temperature range | | T _{stg} | -55 to 150 | °C |



Weight: 0.011 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data/(i.e. reliability test report and estimated failure rate, etc).

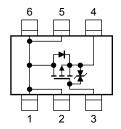
Thermal Characteristics

| Characteristics | Symbol | Max | Unit |
|--|------------------------|-------|------|
| Thermal resistance, channel to ambient (t = 5 s) (Note 2a) | R _{th (ch-a)} | 56.8 | °C/W |
| Thermal resistance, channel to ambient (t = 5 s) (Note 2b) | R _{th (ch-a)} | 178.5 | °C/W |

Note: For (Note 1), (Note 2), (Note 3) and (Note 4), see the third page.

This transistor is an electrostatic-sensitive device. Please handle with caution.

Circuit Configuration



Electrical Characteristics (Ta = 25°C)

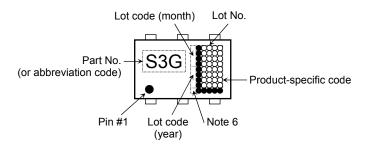
| Ch | aracteristics | Symbol | Test Condition | Min | Тур. | Max | Unit | |
|-------------------------------------|---------------|----------------------|---|---------------|------|---------------|-----------|--|
| Gate leakage cui | rent | I _{GSS} | $V_{GS} = \pm 10 \text{ V}, V_{DS} = 0 \text{ V}$ | _ | _ | ±10 | μА | |
| Drain cut-OFF cu | ırrent | I _{DSS} | $V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$ | _ | _ | -10 | μА | |
| Drain-source breakdown voltage | | V _{(BR)DSS} | $I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$ | -20 | _ | | V | |
| | | V _{(BR)DSX} | $I_D = -10 \text{ mA}, V_{GS} = 12 \text{ V}$ | 8 | _ | | V | |
| Gate threshold ve | oltage | V _{th} | $V_{DS} = -10 \text{ V}, I_D = -200 \mu\text{A}$ | 0.5 |) > | -1.2 | > | |
| Drain-source ON resistance | | R _{DS} (ON) | $V_{GS} = -2 \text{ V}, I_D = -2.2 \text{ A}$ | <u> </u> | 110 | 180 | | |
| | | R _{DS} (ON) | $V_{GS} = -2.5 \text{ V}, I_D = -2.2 \text{ A}$ | \rightarrow | 70 | 100 | $m\Omega$ | |
| | | R _{DS (ON)} | $V_{GS} = -4.5 \text{ V}, I_D = -2.2 \text{ A}$ | \ | 40 | 55 | | |
| Forward transfer | admittance | Y _{fs} | $V_{DS} = -10 \text{ V}, I_D = -2.2 \text{ A}$ | 4.8 | 9.6 | _ | S | |
| Input capacitance | е | C _{iss} | | _ | 680 | _ | | |
| Reverse transfer | capacitance | C _{rss} | $V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ | | (130 | \rightarrow | pF | |
| Output capacitan | ce | Coss | | - | 140 | > — | | |
| Switching time | Rise time | t _r | | | 6 |) — | | |
| | Turn-ON time | t _{on} | V _{GS} OV I _D = -2.2 A OVOUT | /((| 16 | | | |
| | Fall time | t _f | 74.4 W W W W W W W W W W W W W W W W W W | | 38 | | ns | |
| | Turn-OFF time | t _{off} | Duty ≤ 1%, t _w ≠ 10 μs | _ | 85 | | | |
| Total gate charge (gate-source plus | | (Q _g | $V_{DD} \simeq -16 \text{ V}, V_{GS} = -5 \text{ V},$ | | 9.8 | | _ | |
| Gate-source cha | rge | Qgs | $I_D = -4.5 \text{ A}$ | _ | 2 | _ | nC | |
| Gate-drain ("mille | er") charge | Q _{gd} | | _ | 3 | _ | | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

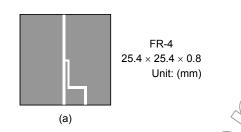
| Characteristics | Symbol Test Condition | Min | Тур. | Max | Unit |
|--------------------------------------|---|-----|------|-----|------|
| Pulse drain reverse current (Note 1) | IDRP - | _ | _ | -18 | Α |
| Forward voltage (diode) | V_{DSF} $I_{DR} = -4.5 \text{ A}, V_{GS} = 0 \text{ V}$ | _ | _ | 1.2 | V |

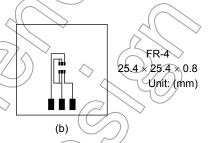


Marking (Note 5)



- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: (a) Device mounted on a glass-epoxy board (a) (t = 5 s)
 - (b) Device mounted on a glass-epoxy board (b) (t = 5 s)





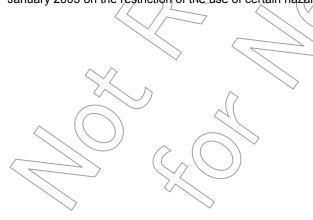
- Note 3: $V_{DD} = 16 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.2 mH, $R_G = 25 \Omega$, $I_{AR} = -2.25 \Omega$
- Note 4: Repetitive rating: pulse width limited by maximum channel temperature
- Note 5: on lower left of the marking indicates Rin 1.
- Note 6 A dot marking identifies the indication of product Labels.

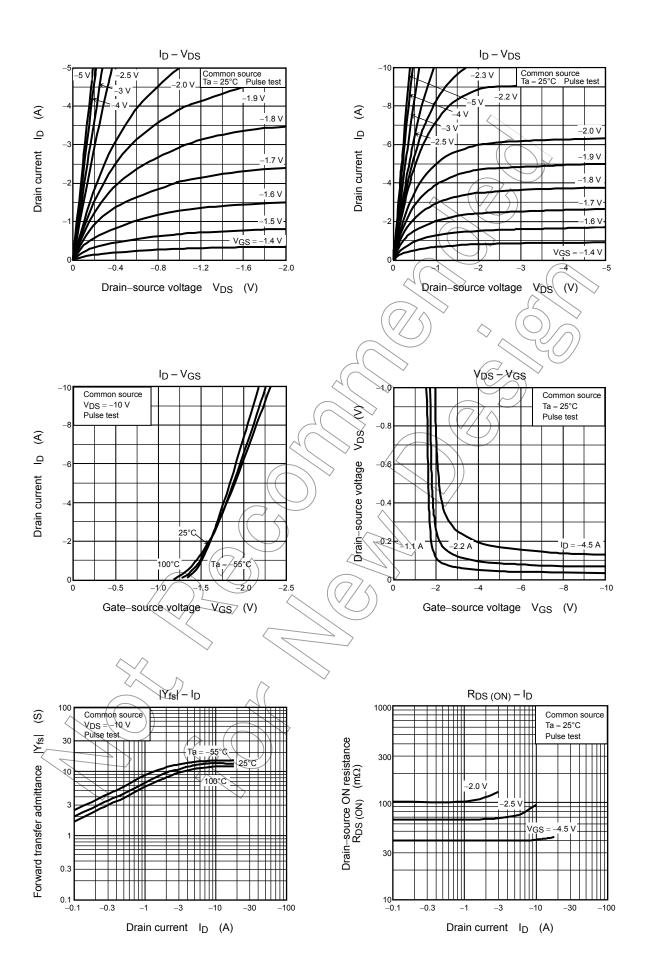
Without a dot: [[Pb]]/INCLUDES > MCV

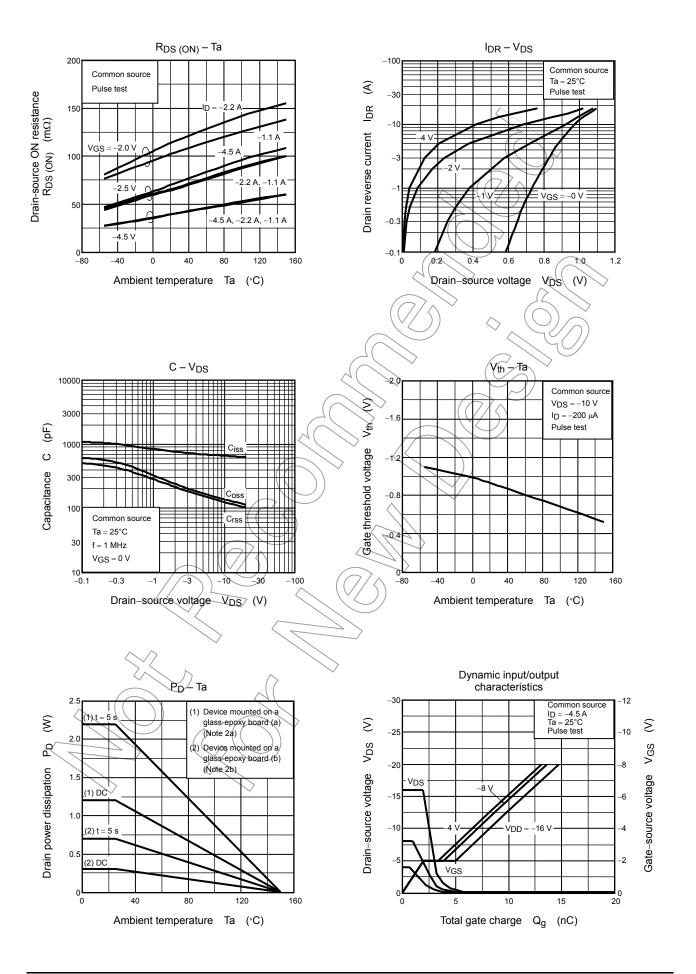
With a dot: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

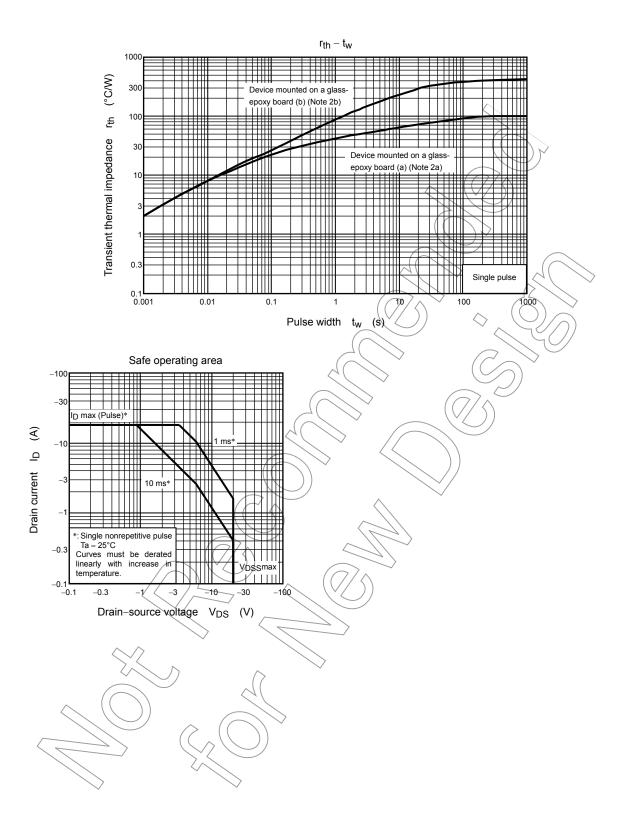
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