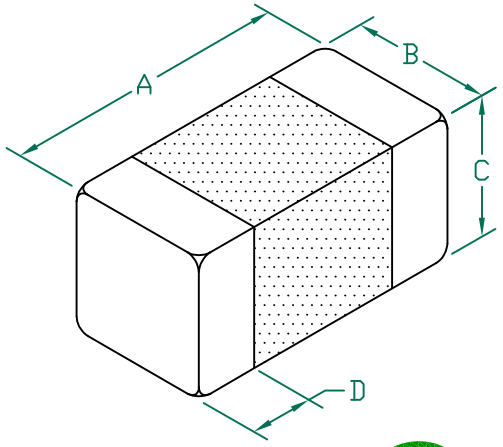
IC0805A472R-10

UNCONTROLLED DOCUMENT

PHYSICAL DIMENSIONS:

A 2.00 [.079] ± 0.20[.008]
B 1.25 [.049] ± 0.20[.008]
C 1.25 [.049] ± 0.20[.008]
D 0.50 [.020] ± 0.30[.012]



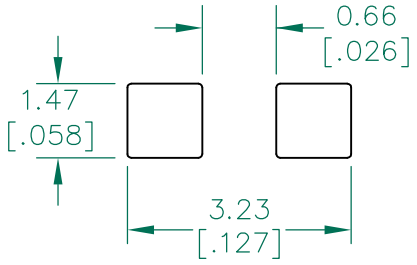
Pb

NOTES: UNLESS OTHERWISE SPECIFIED
1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 2000 PCS/REEL, EMBOSSED PLASTIC TAPE.
2. TERMINATION FINISH IS 100% MATTE Sn OVER Ni.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. I (MAX.) IS BASED ON THE MAXIMUM SUSTAINED CURRENT APPLIED WHILE MAINTAINING A MAXIMUM TEMPERATURE RISE OF 40° C OVER AMBIENT.
5. I (OPERATING.) IS BASED ON THE MAXIMUM SUSTAINED CURRENT APPLIED WHILE MAINTAINING A MINIMUM INDUCTANCE (L).
6. OPERATING TEMP. RANGE: −40°C~+125°C. (INCLUDING SELF−HEATING)

ELECTRICAL CHARACTERISTICS:

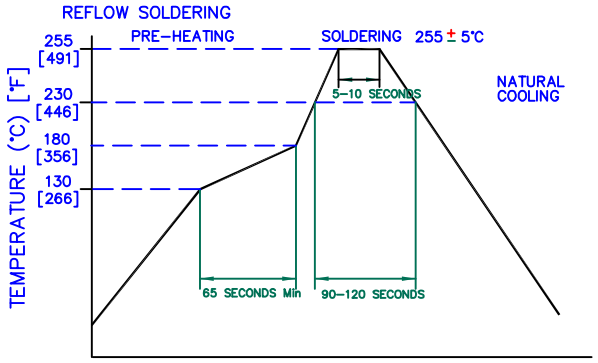
L (nH) ± 10%	5,170	Max
	4,700	Nom
	4,230	Min@ 30Ma
Q (Min)	45	
Freq. (MHz)	10	
Self−Resonant Freq (MHz)	35	
DCR(Max) Ω	1.00	
I (Max)	150mA	
I (Operating)	30mA	

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762[.030] to this dimension)

RECOMMENDED SOLDERING CONDITIONS



RoHS 2002/95/EC

REV	DESCRIPTION	DATE	INT
D	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU
C	UPDATE COMPANY LOGO	06/20/08	JRK
B	REMOVE KO FROM P/N CHG SRF VALUE UPDATE NOTES, REV LAND PATTERN DIMS, ADD OPERATING TEMP TO ELECTRICAL	01/17/07	JRK
A	ORIGINAL DRAFT	05/06/04	JRK

DIMENSIONS ARE IN mm [INCHES].

Laird

PROJECT/PART NUMBER:	REV	PART TYPE:	DRAWN BY:
IC0805A472R−10	D	CO−FIRE	JRK
DATE: 05/06/04	SCALE: NTS	SHEET: 2 of 2	
CAD # IC0805A472R−10−D	TOOL # −		