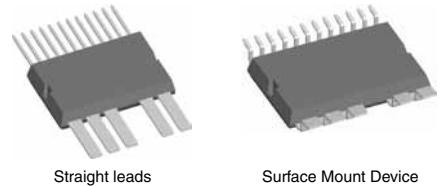
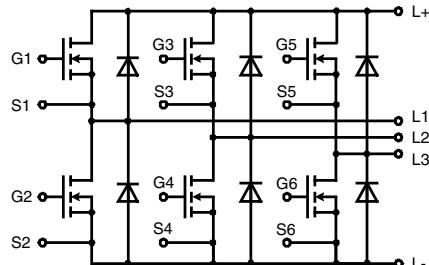


Three phase full Bridge
with Trench MOSFETs
in DCB isolated high current package

V_{DSS} = 75 V
I_{D25} = 110 A
R_{DSon typ.} = 4.0 mΩ



MOSFETs

Symbol	Conditions	Maximum Ratings		
V_{DSS}	T _{VJ} = 25°C to 150°C		75	V
V_{GS}			± 20	V
I_{D25}	T _C = 25°C		110	A
I_{D90}	T _C = 90°C		85	A
I_{F25}	T _C = 25°C (diode)		110	A
I_{F90}	T _C = 90°C (diode)		80	A

Symbol Conditions

Symbol	Conditions	Characteristic Values		
		(T _{VJ} = 25°C, unless otherwise specified)	min.	typ.
R_{DSon}	on chip level at V _{GS} = 10 V; I _D = 60 A	T _{VJ} = 25°C		4.0
		T _{VJ} = 125°C		7.2
V_{GS(th)}	V _{DS} = 20 V; I _D = 1 mA		2	4
				V
I_{DSS}	V _{DS} = V _{DSS} ; V _{GS} = 0 V	T _{VJ} = 25°C		1
		T _{VJ} = 125°C		μA
I_{GSS}	V _{GS} = ± 20 V; V _{DS} = 0 V			0.1
				mA
Q_g Q_{gs} Q_{gd}	V _{GS} = 10 V; V _{DS} = 36 V; I _D = 25 A		115	nC
			30	nC
			30	nC
t_{d(on)} t_r t_{d(off)} t_f E_{on} E_{off} E_{recoff}	V _{GS} = 10 V; V _{DS} = 30 V I _D = 80 A; R _G = 39 Ω inductive load	T _{VJ} = 125°C	130	ns
			100	ns
			500	ns
			100	ns
			0.20	mJ
			0.50	mJ
R_{thJC} R_{thJH}	with heat transfer paste (IXYS test setup)		0.01	mJ
			1.0	K/W
			1.3	K/W
			1.6	K/W

Applications

- AC drives
 - in automobiles
 - electric power steering
 - starter generator
- in industrial vehicles
 - propulsion drives
 - fork lift drives
- in battery supplied equipment

Features

- MOSFETs in trench technology:
 - low RDSon
 - optimized intrinsic reverse diode
- package:
 - high level of integration
 - high current capability 300 A max.
 - aux. terminals for MOSFET control
 - terminals for soldering or welding connections
 - isolated DCB ceramic base plate with optimized heat transfer
- Space and weight savings

Package options

- 2 lead forms available
 - straight leads (SL)
 - SMD lead version (SMD)

Recommended replacement: MTI 90W75GA / MTI 90W75GC

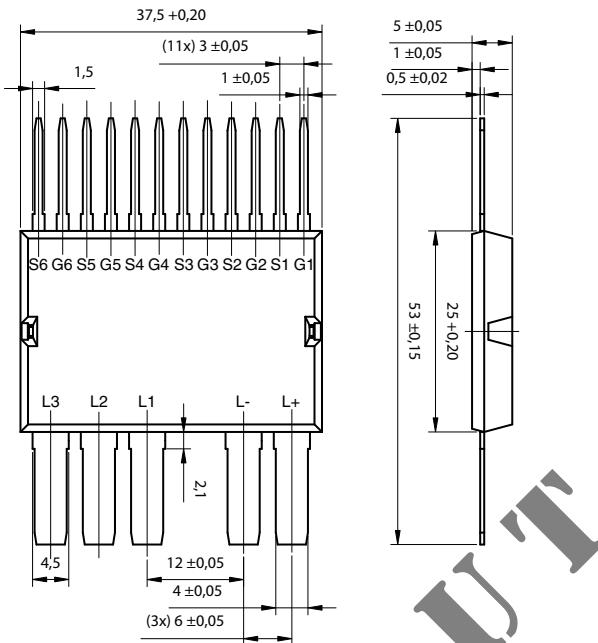
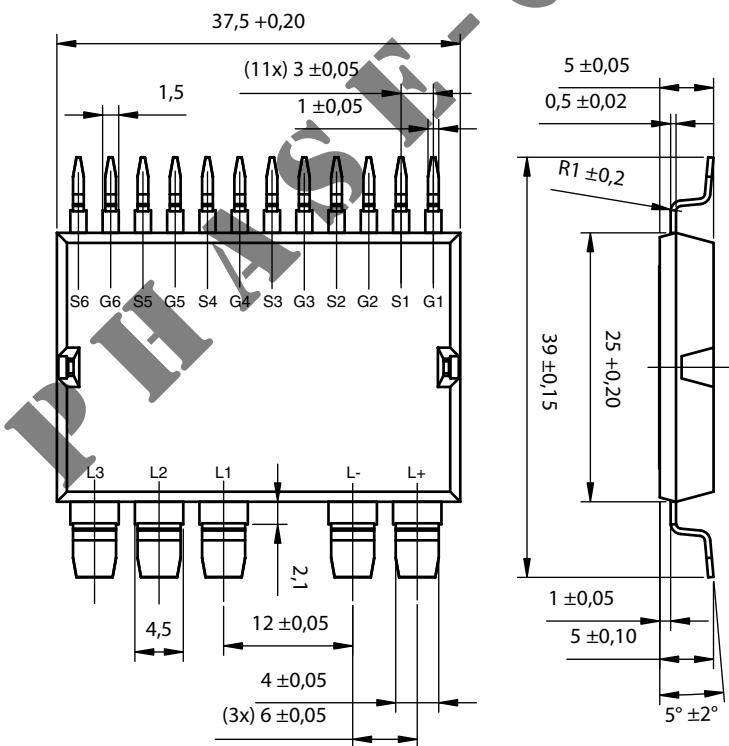
Source-Drain Diode

Symbol	Conditions	Characteristic Values		
		(T _J = 25°C, unless otherwise specified)		
		min.	typ.	max.
V _{SD}	(diode) I _F = 80 A; V _{GS} = 0 V	0.9	1.2	V
t _{rr} Q _{RM} I _{RM}	I _F = 80 A; -di _F /dt = 800 A/μs V _R = 30 V; T _J = 125°C	55 0.9 30		ns μC A

Component

Symbol	Conditions	Maximum Ratings		
I _{RMS}	per pin in main current paths (P+, N-, L1, L2, L3) may be additionally limited by external connections	300		A
T _{VJ}		-55...+175		°C
T _{stg}		-55...+125		°C
V _{ISOL}	I _{ISOL} ≤ 1 mA, 50/60 Hz, f = 1 minute	1000		V~
F _c	mounting force with clip	50 - 250		N

Symbol	Conditions	Characteristic Values		
		min.	typ.	max.
R _{pin to chip}	with heatsink compound		0.6	mΩ
C _P	coupling capacity between shorted pins and mounting tab in the case		160	pF
Weight	typ.		25	g

Straight Leads GWM 120-0075X1-SL**Surface Mount Device GWM 120-0075X1-SMD**

Leads	Ordering	Part Name & Packing Unit Marking	Part Marking	Delivering Mode	Base Qty.	Ordering Code
Straight	Standard	GWM 120-0075X1 - SL	GWM 120-0075X1	Blister	28	505 960
SMD	Standard	GWM 120-0075X1 - SMD	GWM 120-0075X1	Blister	28	505 581

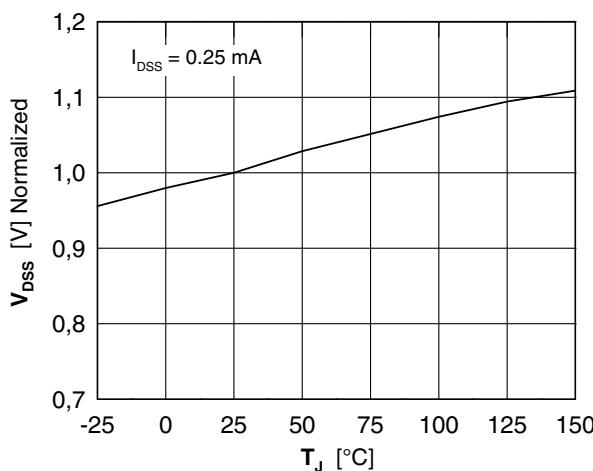


Fig. 1 Drain source breakdown voltage V_{DSS} vs. junction temperature T_J

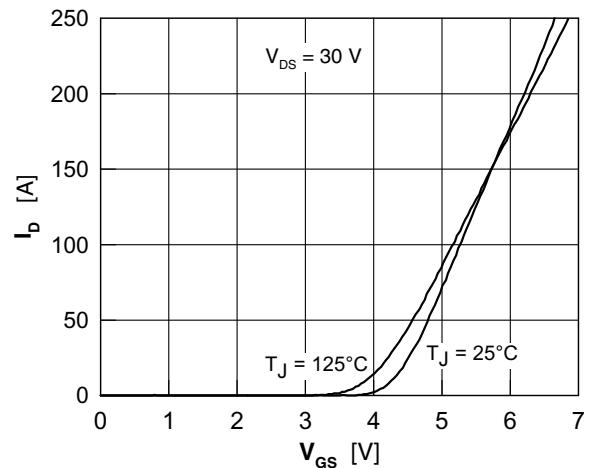


Fig. 2 Typical transfer characteristic

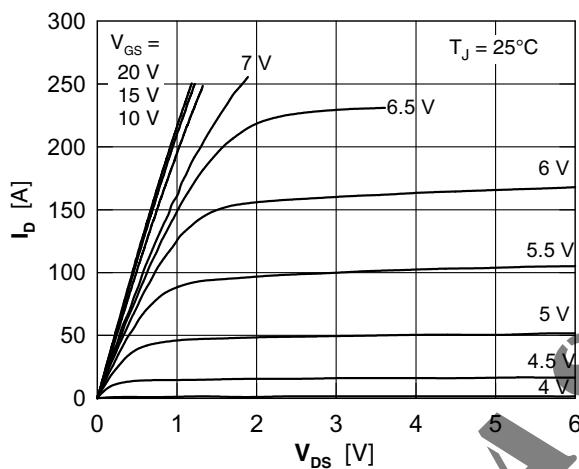


Fig. 3 Typical output characteristic

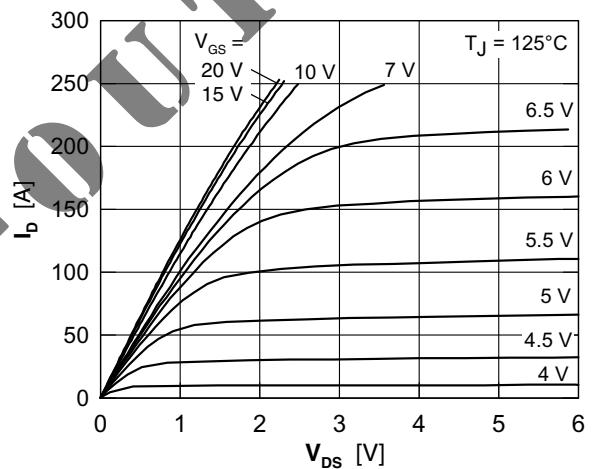


Fig. 4 Typical output characteristic

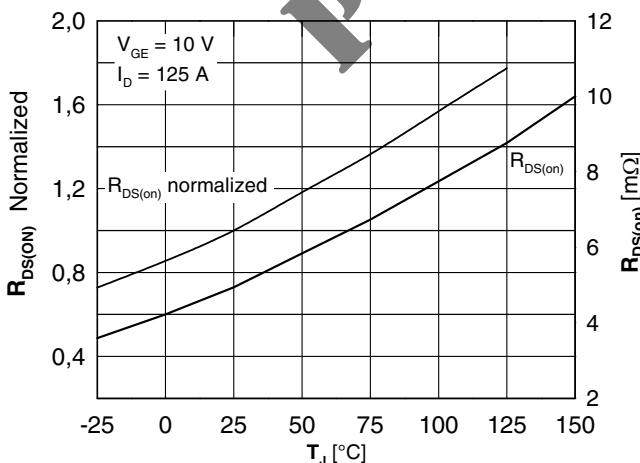


Fig. 5 Drain source on-state resistance $R_{DS(on)}$ versus junction temperature T_J

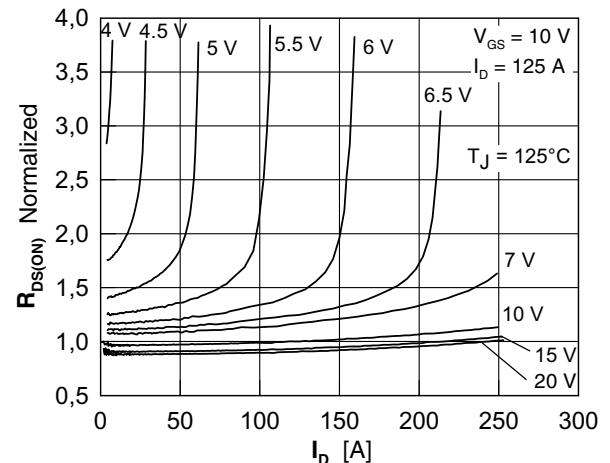


Fig. 6 Drain source on-state resistance $R_{DS(on)}$ versus I_D

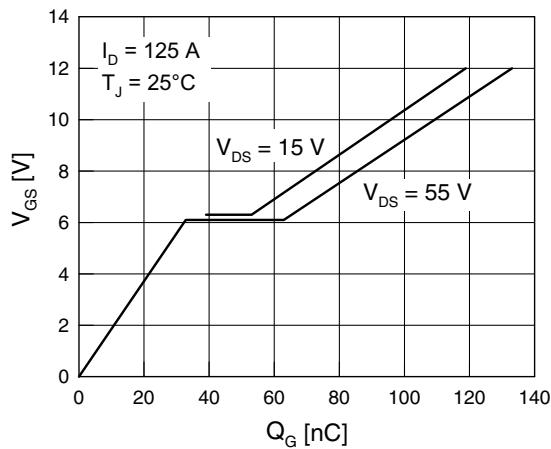


Fig. 7 Gate charge characteristic

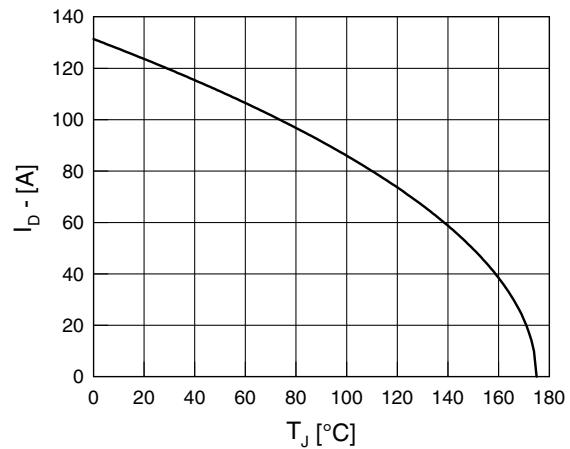
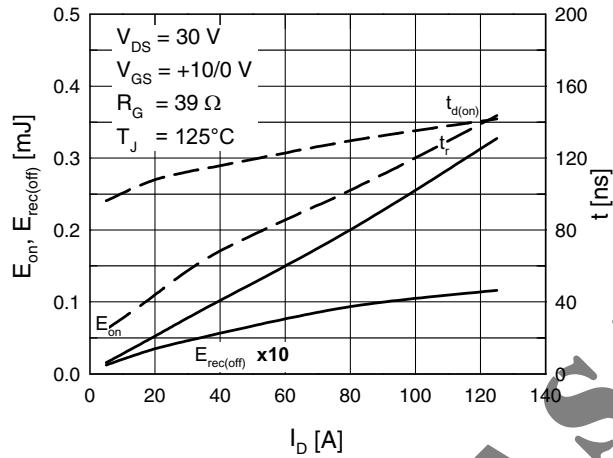
Fig. 8 Drain current I_D vs. case temperature T_c 

Fig. 9 Typ. turn-on energy & switching times vs. collector current, inductive switching

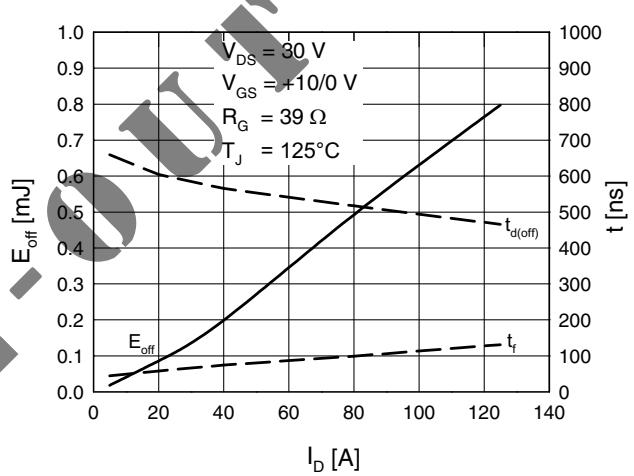


Fig. 10 Typ. turn-off energy & switching times vs. collector current, inductive switching

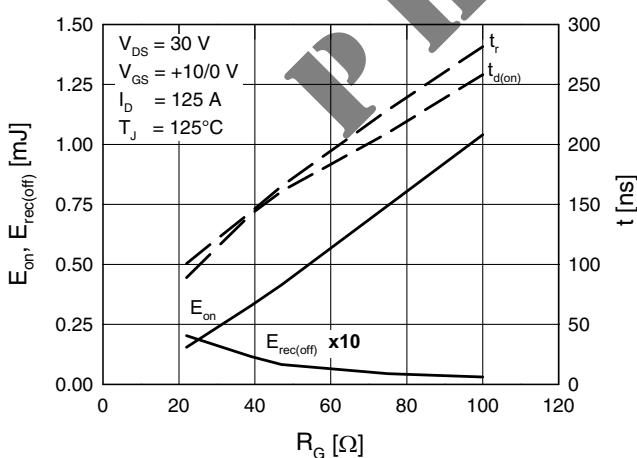


Fig. 11 Typ. turn-on energy & switching times vs. gate resistor, inductive switching

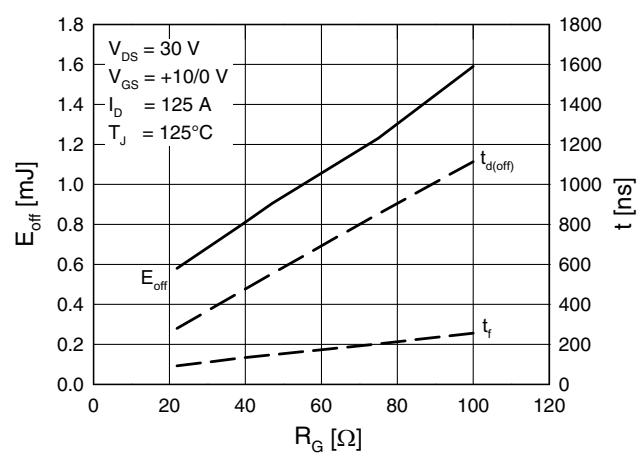


Fig. 12 Typ. turn-off energy & switching times vs. gate resistor, inductive switching

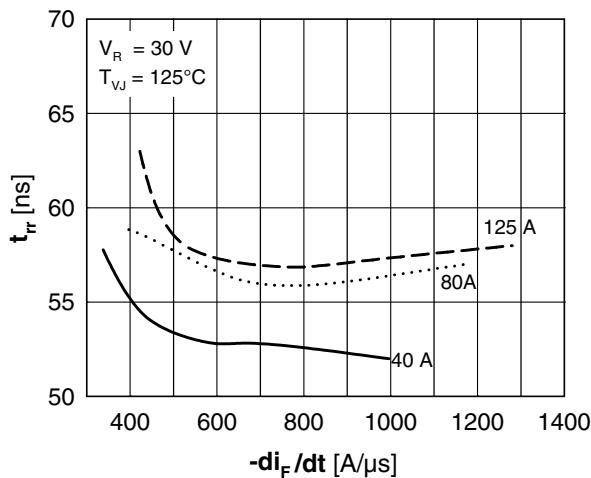


Fig. 13 Reverse recovery time t_{rr} of the body diode vs. di/dt

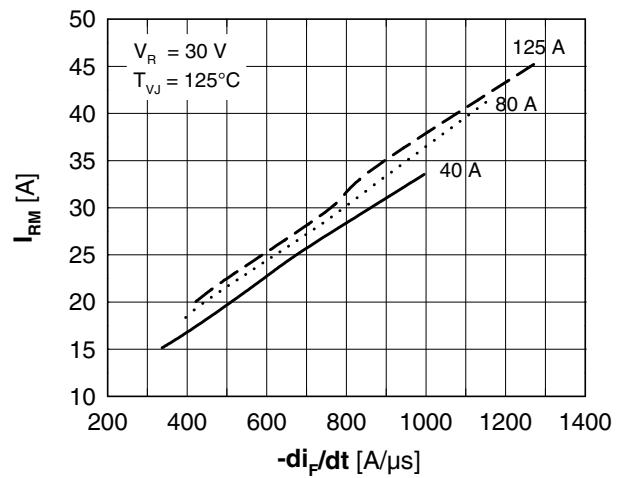


Fig. 14 Reverse recovery current I_{RM} of the body diode vs. di/dt

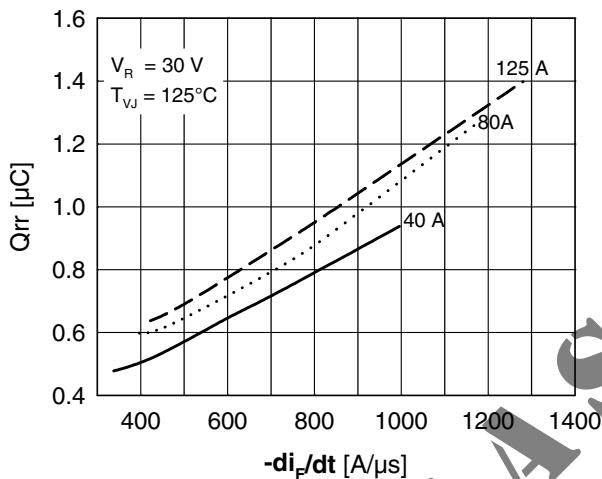


Fig. 15 Reverse recovery charge Q_{rr} of the body diode vs. di/dt

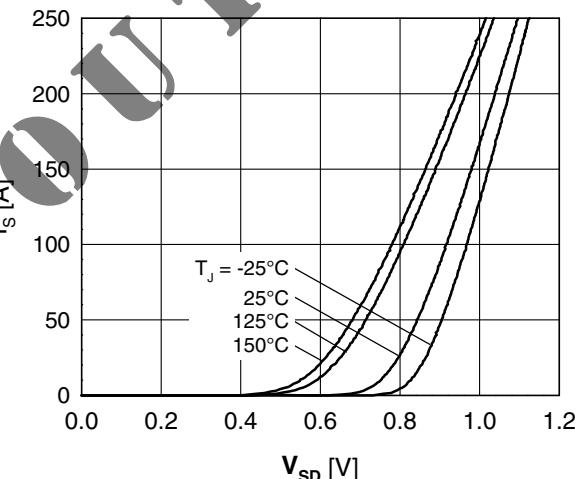


Fig. 16 Source current I_S vs. source drain voltage V_{SD} (body diode)

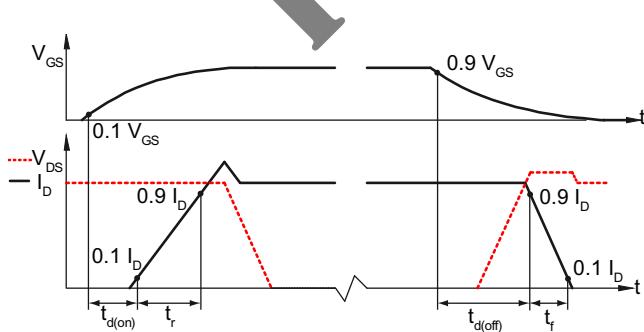


Fig. 17 Definition of switching times

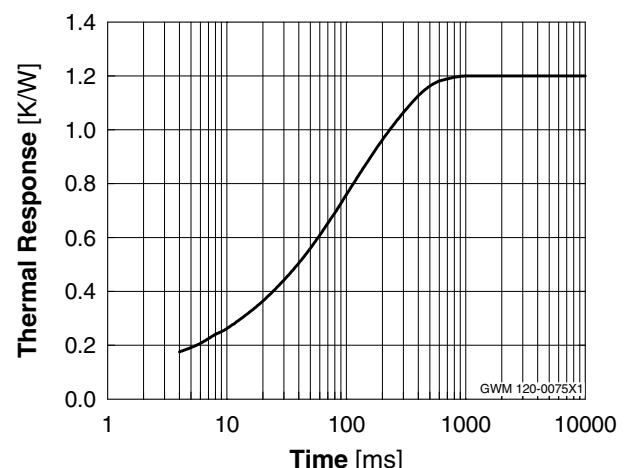


Fig. 18 Typ. therm. impedance junction to heatsink Z_{thJC}