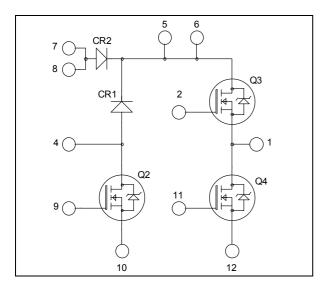
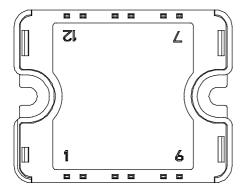


Boost chopper & Phase Leg Super Junction MOSFET Power Module





Pins 7/8; 5/6 must be shorted together

Boost chopper: $V_{DSS} = 600V$ $R_{DSon} = 45 m\Omega$ max @ $Tj = 25^{\circ}C$ $I_D = 49A$ @ $Tc = 25^{\circ}C$

$$\begin{split} Phase \ leg: \ &V_{DSS} = 600V \\ &R_{DSon} = 83m\Omega \ max \ @ \ Tj = 25^{\circ}C \\ &I_D = 36A \ @ \ Tc = 25^{\circ}C \end{split}$$

Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Solar converter

Features

- CoolMOSTM
 - Ultra low R_{DSon}
 - Low Miller capacitance
 - Ultra low gate charge
 - Avalanche energy rated
 - Very rugged
- SiC Schottky Diode (CR1)
 - Zero reverse recovery
 - Zero forward recovery
 - Temperature Independent switching behavior
 - Positive temperature coefficient on VF
- By pass FRED diode (CR2)

Benefits

- Very low stray inductance
- High level of integration
- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

All ratings @ $T_i = 25^{\circ}C$ unless otherwise specified

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



1. Phase leg (Q3 & Q4)

Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		600	V
т	Continuous Drain Current	$T_c = 25^{\circ}C$	36	
I _D Continuous Drain Current	$T_c = 80$ °C	27	Α	
I_{DM}	Pulsed Drain current		115	
V_{GS}	Gate - Source Voltage		±20	V
R_{DSon}	Drain - Source ON Resistance		83	mΩ
P_{D}	Maximum Power Dissipation	$T_c = 25^{\circ}C$	250	W
I_{AR}	Avalanche current (repetitive and non repetitive)		20	Α
E_{AR}	Repetitive Avalanche Energy		1	mI
E_{AS}	Single Pulse Avalanche Energy		1800	mJ

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$	$T_j = 25^{\circ}C$			100	^
		$V_{GS} = 0V, V_{DS} = 600V$	$T_{j} = 125^{\circ}C$			5000	μΑ
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 24.5A$				83	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 3mA$		3	4	5	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$	V			100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$; $V_{DS} = 25V$		7.2		nF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		0.041		111
Q_{g}	Total gate Charge	$V_{GS} = 10V$		250		
Q_{gs}	Gate – Source Charge	$V_{Bus} = 300V$		43		nC
Q_{gd}	Gate – Drain Charge	$I_{D} = 36A$		135]
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (125°C)		21		
T_{r}	Rise Time	$V_{GS} = 10V$		30		
$T_{d(off)}$	Turn-off Delay Time	$V_{\text{Bus}} = 400V$ $I_{\text{D}} = 36A$		240		ns
T_{f}	Fall Time	$R_G = 5\Omega$		52	·	
R_{thJC}	Junction to Case Thermal resistance				0.5	°C/W

Source - Drain diode ratings and characteristics

Symbol	Characteristic Characteristic	Test Conditions		Min	Тур	Max	Unit
I_S	Continuous Source current (Body diode)		$Tc = 25^{\circ}C$ $Tc = 80^{\circ}C$			36 27	A
V_{SD}	Diode Forward Voltage	$V_{GS} = 0V, I_S = -36A$				1.2	V
dv/dt	Peak Diode Recovery 1					40	V/ns
+	Reverse Recovery Time	7 264	$T_j = 25^{\circ}C$		210		ns
t_{rr}		$I_S = -36A$ $V_R = 350V$	$T_{j} = 125^{\circ}C$		350		115
	Reverse Recovery Charge	$di_{S}/dt = 100A/\mu s$	$T_j = 25$ °C		2		
Q_{rr}		αις/αι 100/1/μ3	$T_j = 125$ °C		5.4		μC

 $oldsymbol{0}$ dv/dt numbers reflect the limitations of the circuit rather than the device itself. $I_S \le$ - 36A $\,$ di/dt \le 100A/ μs $\,$ $V_R \le V_{DSS}$ $\,$ $T_j \le$ 150°C



2. Boost chopper (CR1 & Q2)

Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		600	V
Ţ	Continuous Drain Current	$T_c = 25$ °C	49	
I_D	Continuous Diam Current	$T_c = 80$ °C	38	Α
I_{DM}	Pulsed Drain current		130	
V_{GS}	Gate - Source Voltage		±20	V
R_{DSon}	Drain - Source ON Resistance		45	$m\Omega$
P_{D}	Maximum Power Dissipation	$T_c = 25^{\circ}C$	250	W
I_{AR}	Avalanche current (repetitive and non repetitive)		15	A
E _{AR}	Repetitive Avalanche Energy		3	mJ
E_{AS}	Single Pulse Avalanche Energy		1900	1113

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Typ	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 25^{\circ}$			250	^
		$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 125$	°C		500	μΑ
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 24.5A$		40	45	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 3mA$		3	3.9	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$; $V_{DS} = 25V$		7.2		nF
C_{oss}	Output Capacitance	f = 1MHz		8.5		111
Q_{g}	Total gate Charge	$V_{GS} = 10V$		150		
Q_{gs}	Gate – Source Charge	$V_{\text{Bus}} = 300V$		34		nC
Q_{gd}	Gate – Drain Charge	$I_D = 49A$		51		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (125°C)		21		
T_{r}	Rise Time	$V_{GS} = 10V$		30		
$T_{d(off)} \\$	Turn-off Delay Time	$V_{\text{Bus}} = 400V$ $I_{\text{D}} = 49A$		100		ns
T_{f}	Fall Time	$R_G = 5\Omega$		45		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C V _{GS} = 10V; V _{Bus} = 400V		405		цŢ
E_{off}	Turn-off Switching Energy	$I_{D} = 49A ; R_{G} = 5\Omega$		520		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C V _{GS} = 10V; V _{Bus} = 400V		660		T
E_{off}	Turn-off Switching Energy	$I_{\rm D} = 49 \text{A} \; ; \; R_{\rm G} = 5 \Omega$		635	·	μJ
R_{thJC}	Junction to Case Thermal resistance				0.5	°C/W



SiC schottky diode ratings and characteristics (CR1)

Symbol	bol Characteristic Test Conditions		Test Conditions		Typ	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V
I_{RM}	Maximum Reverse Leakage Current	V _R =600V	$T_j = 25$ °C		10	60	μA
1RM	Wiaximum Reverse Leakage Current	V R 000 V	$T_j = 175$ °C		20	300	μπ
$I_{F(AV)}$	Maximum Average Forward Current	50% duty cycle	Tc = 100°C		10		A
$V_{\rm F}$	Diode Forward Voltage	$I_F = 10A$	$T_j = 25^{\circ}C$		1.6	1.8	V
V F		$T_{i} = 175^{\circ}C$			2	2.4	Ť
Qc	Total Capacitive Charge	$I_F = 10A, V_R = 300V$ di/dt = 500A/ μ s			14		nC
С	Total Canacitance	$f = 1MHz, V_R =$	200V		65		рF
	Total Capacitance	$f = 1MHz, V_R =$	400V		50		þΓ
R_{thJC}	Junction to Case Thermal resistance		_			2.5	°C/W

3. By pass FRED diode (CR2)

Diode ratings and characteristics

Symbol	Characteristic	Test Conditions	Test Conditions		Тур	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V
I_{RM}	Maximum Reverse Leakage Current	V _R =600V	$T_i = 25^{\circ}C$ $T_i = 150^{\circ}C$			100 350	μА
I_{F}	DC Forward Current		$T_c = 80^{\circ}C$		30	330	A
V		$I_F = 30A$	$T_i = 25^{\circ}C$		1.6	2	V
v _F		$V_{GE} = 0V$	$T_{i} = 150^{\circ}C$		1.5		v
t_{rr}	Reverse Recovery Time		$T_j = 25$ °C		100		ns
·rr			$T_{i} = 150^{\circ}C$		150		115
Q _{rr}	Reverse Recovery Charge	$I_F = 30A$ $V_R = 300V$	$T_j = 25$ °C		1.5		μС
Vп	Reverse Recovery Charge	$\frac{V_R - 300 V}{\text{di/dt} = 1800 \text{A/}\mu\text{s}} = \frac{T_j}{T_j} = \frac{T_j}{T_j}$	$T_{i} = 150^{\circ}C$		3.1		μС
Б	Daviana Dagaviani Engineri	αν αν 100012/μο	$T_j = 25$ °C		0.34		m I
E _{rr}	Reverse Recovery Energy		$T_j = 150$ °C		0.75		mJ
R_{thJC}	Junction to Case Thermal Resistance	_				2.45	°C/W

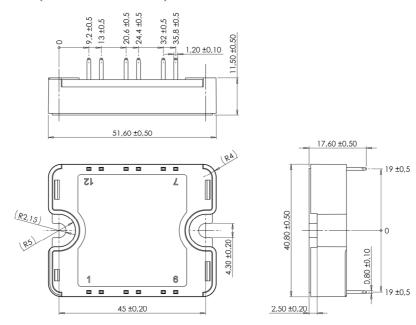
4. Thermal & Package characteristics

	" I not mut & I dekage characteristics						
Symbol	Characteristic			Min	Typ	Max	Unit
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T_{J}	Operating junction temperature range			-40		150*	
T_{STG}	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature					100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					80	g

^{*} $T_{jmax} \! = \! 175^{\circ} C$ for by pass and SiC diode

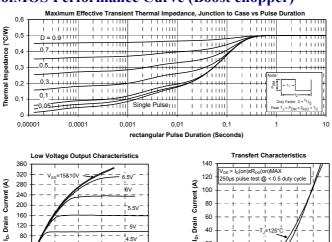


SP1 Package outline (dimensions in mm)



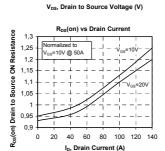
See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

5. Typical CoolMOS Performance Curve (Boost chopper)



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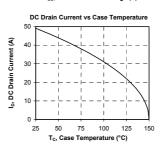


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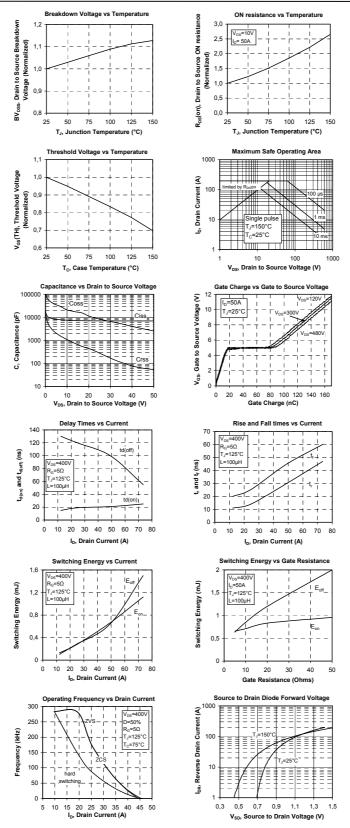
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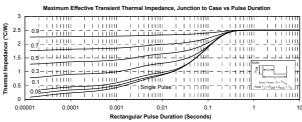


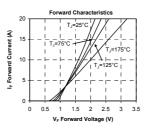


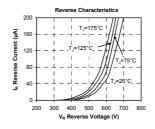
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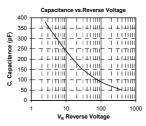


6. SiC Typical Performance Curve (CR1)

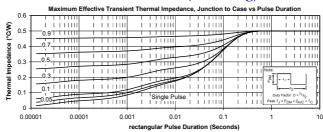


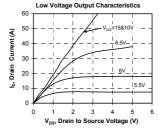


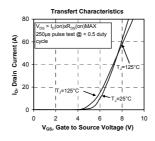


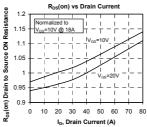


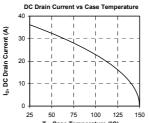
7. Typical CoolMOS Performance Curve (Phase leg)

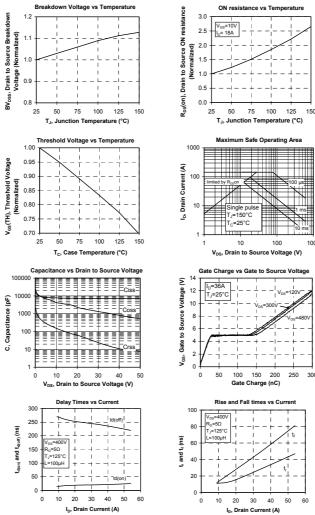




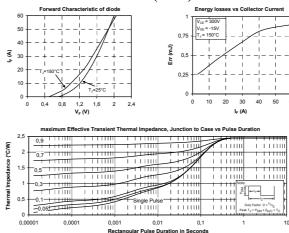








8. Typical By pass Performance Curve (CR2)



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