

TOSHIBA Field Effect Transistor with Built-in Schottky Barrier Diode  
Silicon N-Channel MOS Type (U-MOS V-H)

# TPCC8A01-H

High Efficiency DC-DC Converter Applications

Notebook PC Applications

Portable Equipment Applications

- Built-in a Schottky barrier diode  
Low forward voltage:  $V_{DSF} = -0.6$  V (max)
- High-speed switching
- Small gate charge:  $Q_{SW} = 4.1$  nC (typ.)
- Low drain-source ON-resistance:  
 $R_{DS(ON)} = 9.0$  m $\Omega$  (typ.) ( $V_{GS} = 4.5$  V)
- High forward transfer admittance:  $|Y_{fs}| = 52$  S (typ.)
- Low leakage current:  $I_{DSS} = 100$   $\mu$ A (max) ( $V_{DS} = 30$  V)
- Enhancement mode:  $V_{th} = 1.3$  to  $2.3$  V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

## Absolute Maximum Ratings (Ta = 25°C)

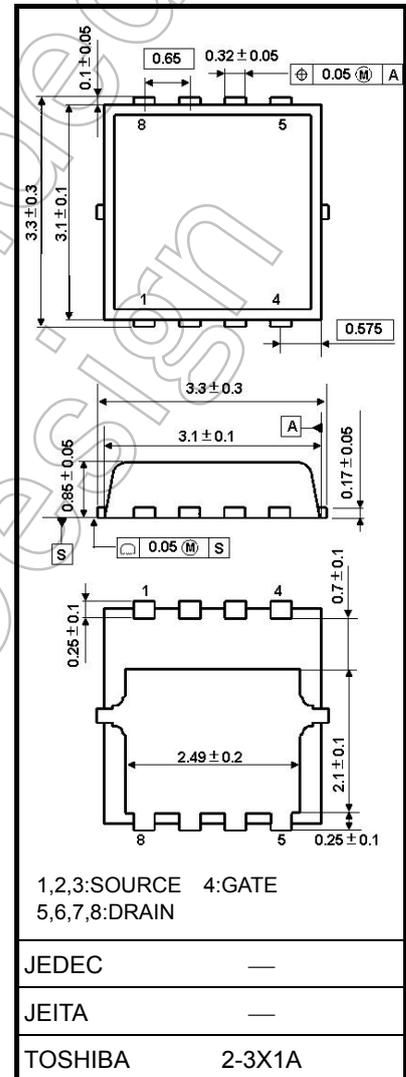
Characteristic	Symbol	Rating	Unit
Drain-source voltage	$V_{DSS}$	30	V
Drain-gate voltage ( $R_{GS} = 20$ k $\Omega$ )	$V_{DGR}$	30	V
Gate-source voltage	$V_{GSS}$	$\pm 20$	V
Drain current	DC (Note 1)	$I_D$	21
	Pulsed (Note 1)	$I_{DP}$	63
Drain power dissipation ( $T_c = 25^\circ\text{C}$ )	$P_D$	30	W
Drain power dissipation ( $t = 10$ s) (Note 2a)	$P_D$	1.9	W
Drain power dissipation ( $t = 10$ s) (Note 2b)	$P_D$	0.7	W
Single-pulse avalanche energy (Note 3)	$E_{AS}$	115	mJ
Avalanche current	$I_{AR}$	21	A
Repetitive avalanche energy ( $T_c = 25^\circ\text{C}$ ) (Note 4)	$E_{AR}$	1.84	mJ
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature range	$T_{stg}$	-55 to 150	$^\circ\text{C}$

Note: For Notes 1 to 4, refer to the next page.

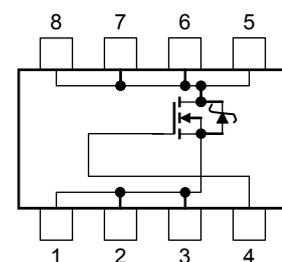
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



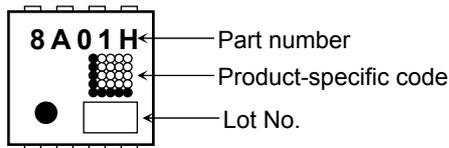
## Circuit Configuration



## Thermal Characteristics

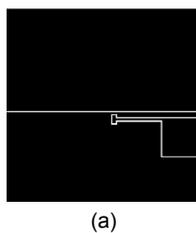
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case ( $T_c = 25^\circ\text{C}$ )	$R_{th(ch-c)}$	4.2	$^\circ\text{C/W}$
Thermal resistance, channel to ambient ( $t = 10\text{ s}$ ) (Note 2a)	$R_{th(ch-a)}$	66	$^\circ\text{C/W}$
Thermal resistance, channel to ambient ( $t = 10\text{ s}$ ) (Note 2b)	$R_{th(ch-a)}$	180	$^\circ\text{C/W}$

## Marking (Note 5)

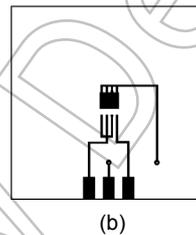


Note 1: Ensure that the channel temperature does not exceed  $150^\circ\text{C}$ .

Note 2: (a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)



FR-4  
 $25.4 \times 25.4 \times 0.8$   
 (Unit: mm)

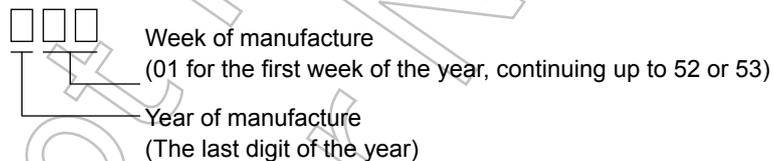


FR-4  
 $25.4 \times 25.4 \times 0.8$   
 (Unit: mm)

Note 3:  $V_{DD} = 24\text{ V}$ ,  $T_{ch} = 25^\circ\text{C}$  (initial),  $L = 200\ \mu\text{H}$ ,  $R_G = 25\ \Omega$ ,  $I_{AR} = 21\text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: \* Weekly code: (Three digits)

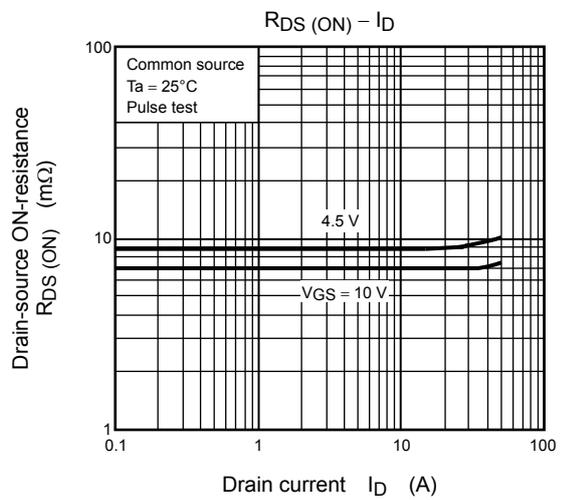
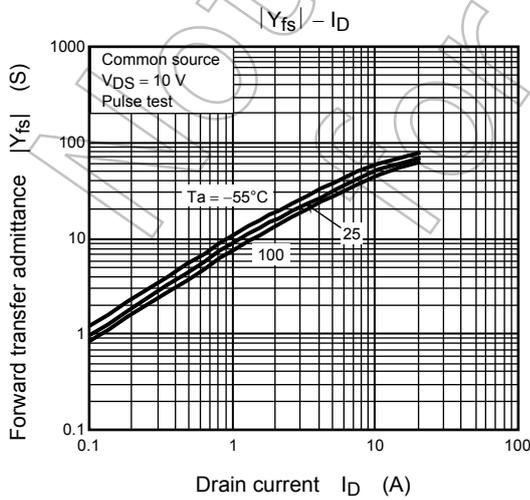
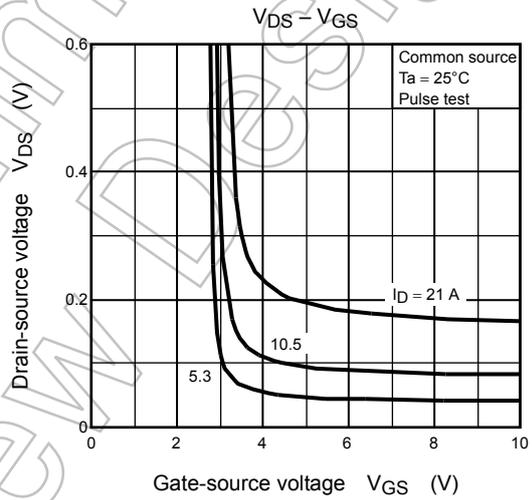
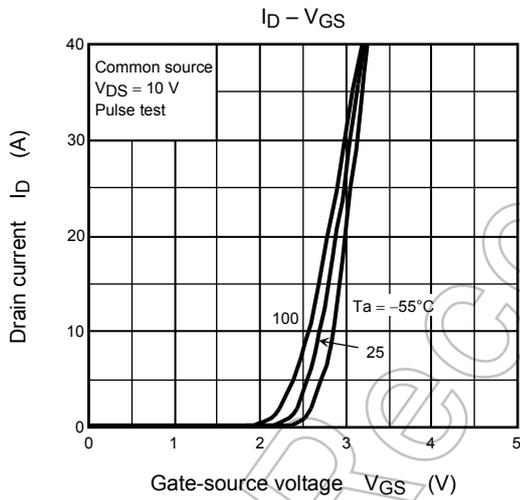
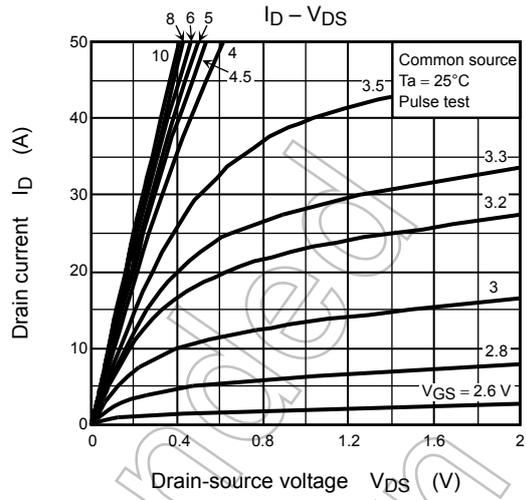
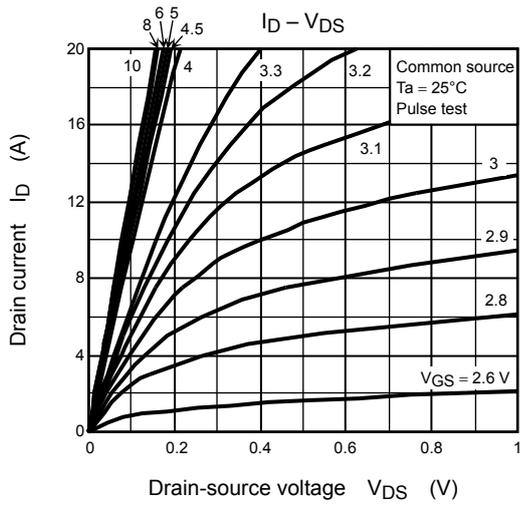


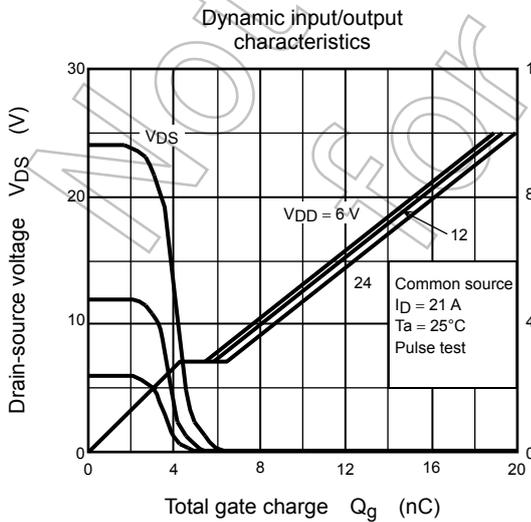
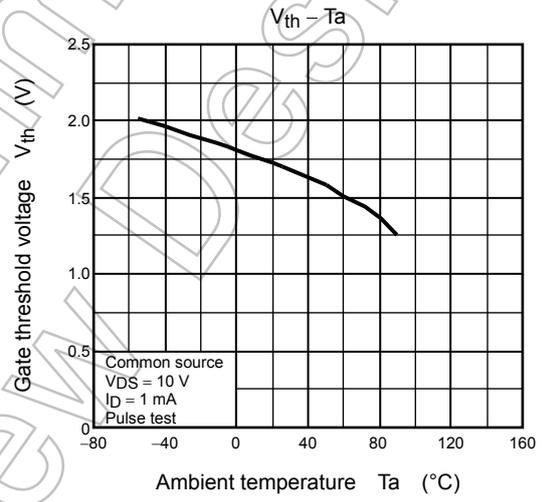
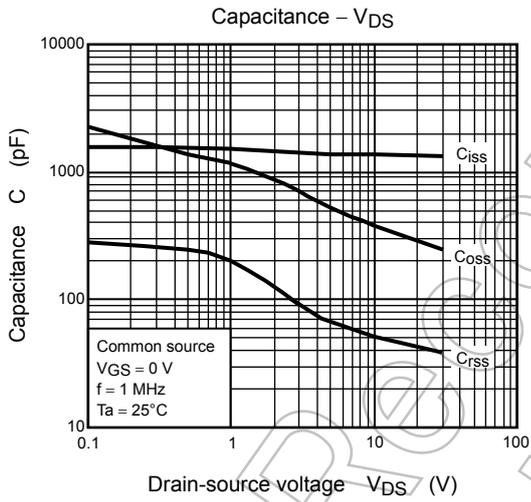
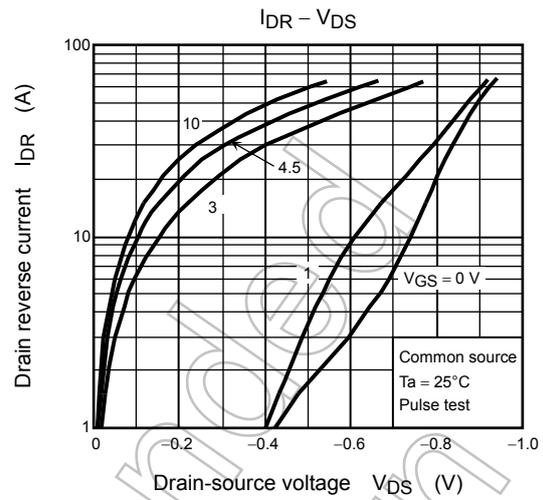
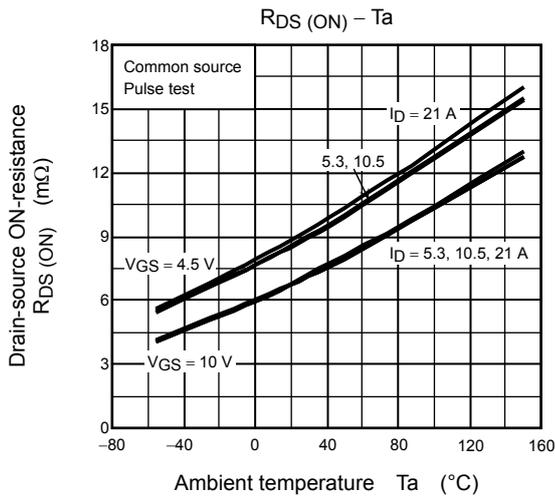
## Electrical Characteristics (Ta = 25°C)

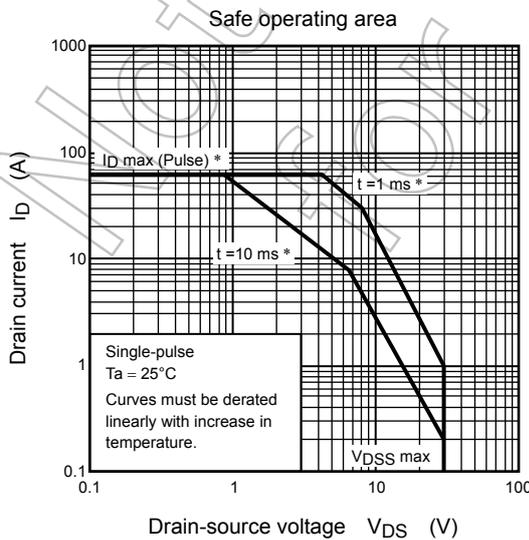
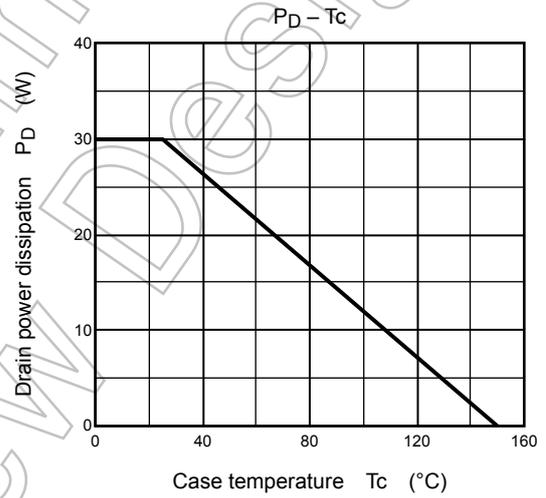
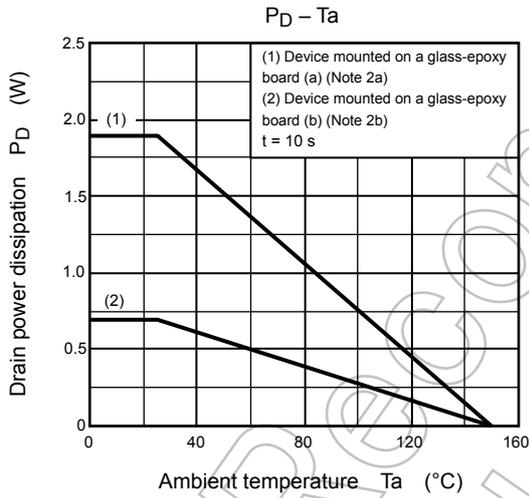
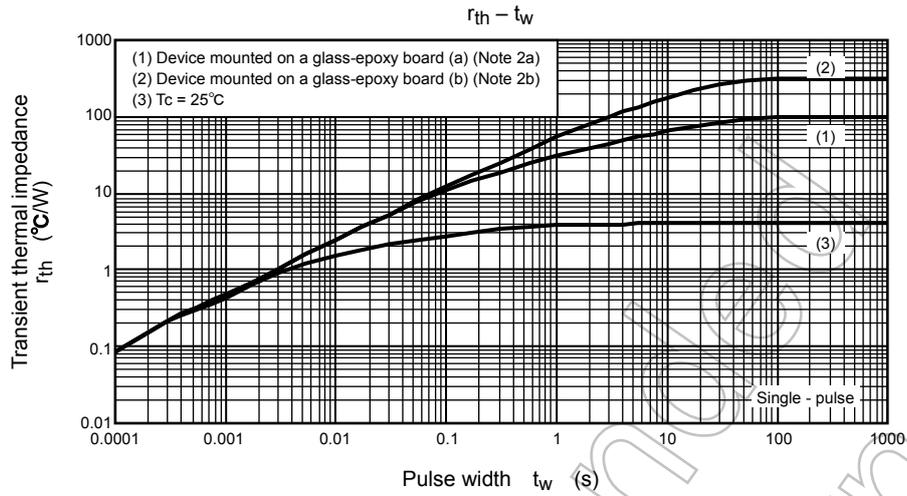
Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		$I_{GSS}$	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$	—	—	$\pm 100$	nA
Drain cut-off current		$I_{DSS}$	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$	—	—	100	$\mu\text{A}$
Drain-source breakdown voltage		$V_{(BR)DSS}$	$I_D = 10\text{ mA}, V_{GS} = 0\text{ V}$	30	—	—	V
		$V_{(BR)DSX}$	$I_D = 10\text{ mA}, V_{GS} = -20\text{ V}$	15	—	—	
Gate threshold voltage		$V_{th}$	$V_{DS} = 10\text{ V}, I_D = 1\text{ mA}$	1.3	—	2.3	V
Drain-source ON-resistance		$R_{DS(ON)}$	$V_{GS} = 4.5\text{ V}, I_D = 10.5\text{ A}$	—	9.0	12.6	m $\Omega$
			$V_{GS} = 10\text{ V}, I_D = 10.5\text{ A}$	—	7.1	9.9	
Forward transfer admittance		$ Y_{fs} $	$V_{DS} = 10\text{ V}, I_D = 10.5\text{ A}$	26	52	—	S
Input capacitance		$C_{iss}$	$V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	—	1430	1900	pF
Reverse transfer capacitance		$C_{rss}$		—	54	81	
Output capacitance		$C_{oss}$		—	380	—	
Gate resistance		$r_g$	$V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}, f = 5\text{ MHz}$	—	1.5	2.3	$\Omega$
Switching time	Rise time	$t_r$	<p><math>V_{GS} = 10\text{ V}</math> <math>0\text{ V}</math> <math>I_D = 10.5\text{ A}</math> <math>V_{OUT}</math> <math>47\text{ nF}</math> <math>1.43\ \Omega</math> <math>R_L = 1.43\ \Omega</math> <math>V_{DD} \approx 15\text{ V}</math></p>	—	2.2	—	ns
	Turn-on time	$t_{on}$		—	7.7	—	
	Fall time	$t_f$		—	3.4	—	
	Turn-off time	$t_{off}$		Duty $\leq 1\%$ , $t_w = 10\ \mu\text{s}$	—	22	
Total gate charge (gate-source plus gate-drain)		$Q_g$	$V_{DD} \approx 24\text{ V}, V_{GS} \approx 10\text{ V}, I_D = 21\text{ A}$	—	20	—	nC
			$V_{DD} \approx 24\text{ V}, V_{GS} = 5\text{ V}, I_D = 21\text{ A}$	—	10.1	—	
Gate-source charge 1		$Q_{gs1}$	$V_{DD} \approx 24\text{ V}, V_{GS} = 10\text{ V}, I_D = 21\text{ A}$	—	4.3	—	
Gate-drain ("Miller") charge		$Q_{gd}$		—	2.1	—	
Gate switch charge		$Q_{sw}$		—	4.1	—	

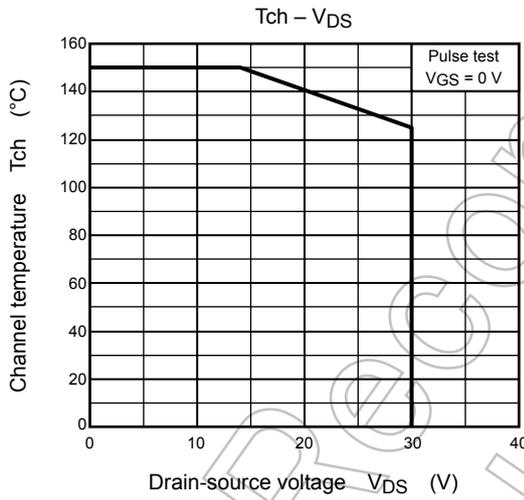
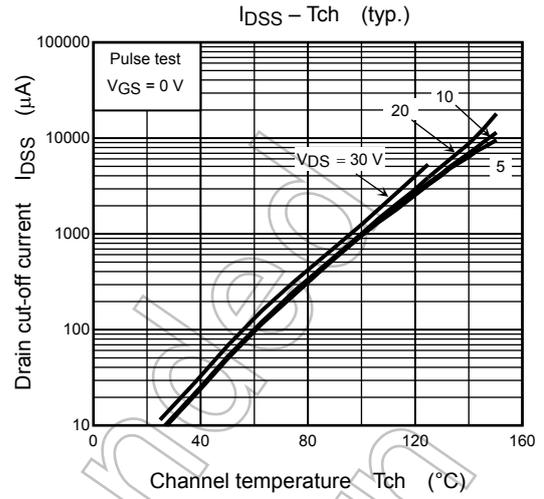
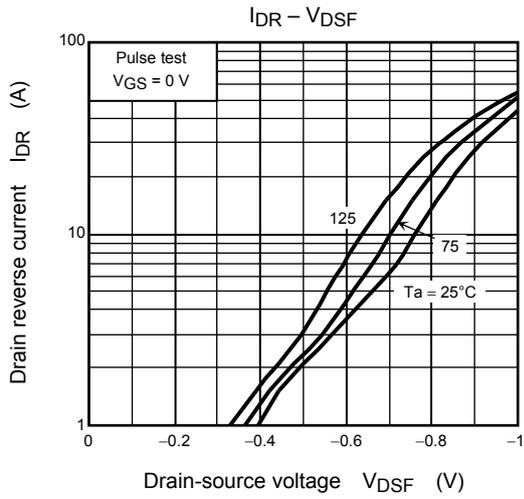
## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
Drain reverse current	Pulse (Note 1)	$I_{DRP}$	—	—	—	63	A
Forward voltage (diode)		$V_{DSF}$	$I_{DR} = 1\text{ A}, V_{GS} = 0\text{ V}$	—	-0.4	-0.6	V
			$I_{DR} = 21\text{ A}, V_{GS} = 0\text{ V}$	—	—	-1.2	V









Not Recommended for New Design

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