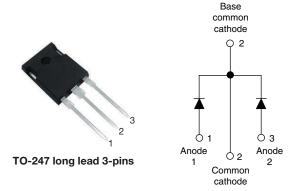
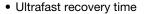


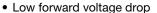
Ultrafast Rectifier, 2 x 30 A FRED Pt®

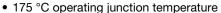


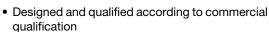
| PRODUCT SUMMARY | | | | | |
|----------------------------------|-------------------------|--|--|--|--|
| Package | TO-247 long lead 3-pins | | | | |
| I _{F(AV)} | 2 x 30 A | | | | |
| V_{R} | 600 V | | | | |
| V _F at I _F | 1.75 V | | | | |
| t _{rr} typ. | 26 ns | | | | |
| T _J max. | 175 °C | | | | |
| Diode variation | Common cathode | | | | |

FEATURES









 Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE

DESCRIPTIONS/APPLICATIONS

VS-CPU60... series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

| ABSOLUTE MAXIMUM RATINGS | | | | | | |
|---------------------------------------------|-----------------------------------|-------------------------|-------------|-------|--|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | MAX. | UNITS | | |
| Repetitive peak reverse voltage | V_{RRM} | | 600 | V | | |
| Average rectified forward current | I _{F(AV)} | T _C = 131 °C | 60 | | | |
| Non-repetitive peak surge current per leg | I _{FSM} | T _J = 25 °C | 250 | _ A | | |
| Operating junction and storage temperatures | T _J , T _{Stg} | | -65 to +175 | °C | | |

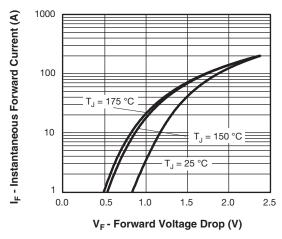
| ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified) | | | | | | | |
|--------------------------------------------------------------------------------------|-------------------------------------|----------------------------------------------------------------|------|------|------|-------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS | |
| Breakdown voltage, blocking voltage | V _{BR} , V _R | Ι _R = 100 μΑ | 600 | - | - | | |
| Forward voltage | V _F | I _F = 30 A | - | 1.4 | 1.75 |] V | |
| | | I _F = 30 A, T _J = 150 °C | - | 1.1 | 1.4 | | |
| Reverse leakage current | I _R | $V_R = V_R$ rated | - | 0.02 | 30 | | |
| | | T _J = 150 °C, V _R = V _R rated | - | 30 | 200 | μΑ | |
| Junction capacitance | C _T | V _R = 600 V | - | 20 | - | pF | |

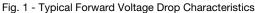


| DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified) | | | | | | | | |
|---------------------------------------------------------------------------------------------|------------------|--------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------|------|------|-------|-----|--|
| PARAMETER | SYMBOL | TEST C | MIN. | TYP. | MAX. | UNITS | | |
| | | $I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$ | | - | 26 | - | | |
| Reverse recovery time | t _{rr} | T _J = 25 °C | | - | 42 | - | ns | |
| | | T _J = 125 °C | | - | 100 | - | | |
| Peak recovery current | I _{RRM} | T _J = 25 °C | $I_F = 30 \text{ A}$ $dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$ | - | 5 | = | Α | |
| | | T _J = 125 °C | | - | 10 | - | | |
| Reverse recovery charge | Q _{rr} | T _J = 25 °C | | - | 125 | - | nC | |
| | | T _J = 125 °C | | - | 580 | = | IIC | |

| THERMAL - MECHANICAL SPECIFICATIONS | | | | | | | |
|-------------------------------------------------|-----------------------------------|--------------------------------------------|--------------|------|------------|------------------------|--|
| PARAMETER | SYMBOL | L TEST CONDITIONS | | TYP. | MAX. | UNITS | |
| Maximum junction and storage temperature range | T _J , T _{Stg} | | -65 | - | 175 | °C | |
| Thermal resistance, junction to case per leg | R _{thJC} | | - | 0.7 | 1 | | |
| Thermal resistance, junction to ambient per leg | R _{thJA} | R _{thJA} Typical socket mount | | - | 70 | °C/W | |
| Thermal resistance, case to heatsink | R _{thCS} | Mounting surface, flat, smooth and greased | - | 0.5 | - | | |
| Weight | | | - | 6.0 | - | g | |
| vveignt | | | - | 0.21 | - | OZ. | |
| Mounting torque | | | 6.0 (5.0) | - | 12 (10) | kgf · cm (lbf · in) | |
| Marking device | | Case style TO-247 long lead 3-pins | CPU6006L | | | | |







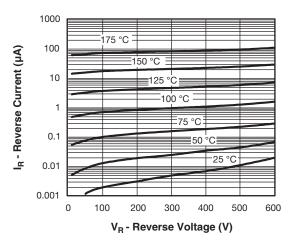


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

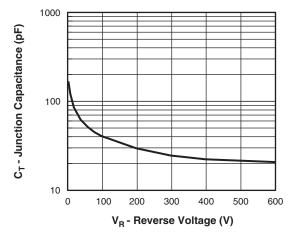


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

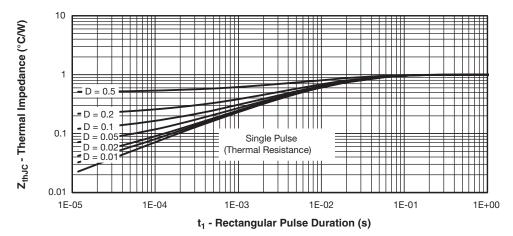


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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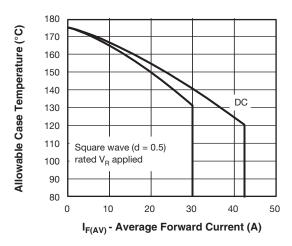


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

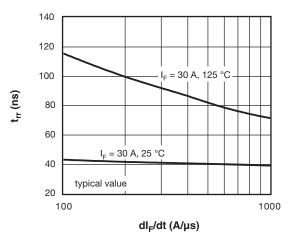


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

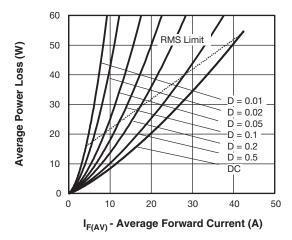


Fig. 6 - Forward Power Loss Characteristics

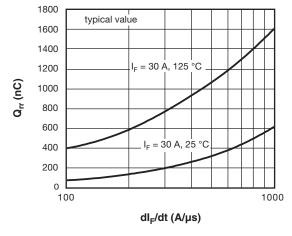
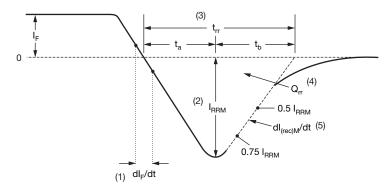


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

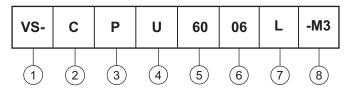
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Circuit configuration:

C = common cathode

- **3** P = TO-247
- U = ultrafast recovery time
- Current code (60 = 2 x 30 A)
- 6 Voltage code (06 = 600 V)
- 7 L = long lead
- 8 Environmental digit:

-M3 = halogen-free, RoHS-compliant and termination lead (Pb)-free

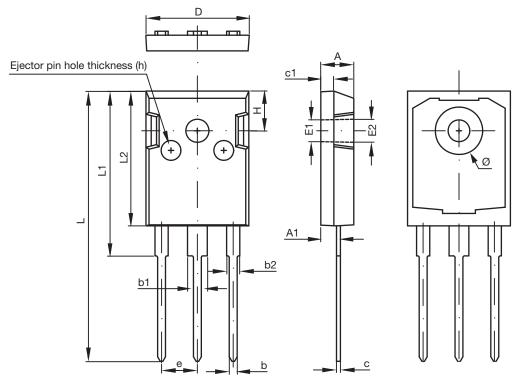
| ORDERING INFORMATION (Example) | | | | | |
|--------------------------------|------------------|------------------------|-------------------------|--|--|
| PREFERRED P/N | QUANTITY PER T/R | MINIMUM ORDER QUANTITY | PACKAGING DESCRIPTION | | |
| VS-CPU6006L-M3 | 30 | 300 | Antistatic plastic tube | | |

| LINKS TO RELATED DOCUMENTS | | | | |
|----------------------------|------------------|--------------------------|--|--|
| Dimensions | TO-247 3-pins LL | www.vishay.com/doc?95599 | | |
| Part marking information | TO-247 3-pins LL | www.vishay.com/doc?95593 | | |



TO-247 3 Pin Long Lead

DIMENSIONS in millimeters



| SYMBOL | DIMENSIONS | IN MILLIMETERS | DIMENSIONS IN INCHES | | |
|---------|----------------------|----------------|----------------------|--------|--|
| STWIBUL | MIN. | MAX. | MIN. | MAX. | |
| Α | 4.850 | 5.150 | 0.191 | 0.200 | |
| A1 | 2.200 | 2.600 | 0.087 | 0.102 | |
| b | 1.000 | 1.400 | 0.039 | 0.055 | |
| b1 | 2.800 | 3.200 | 0.110 | 0.126 | |
| b2 | 1.800 | 2.200 | 0.071 | 0.087 | |
| С | 0.500 | 0.700 | 0.020 | 0.028 | |
| c1 | 1.900 | 2.100 | 0.075 | 0.083 | |
| D | 15.450 | 15.750 | 0.608 | 0.620 | |
| E1 | 3.500 Ref. 0.138 Ref | | | Ref. | |
| E2 | 3.60 | 00 Ref. | 0.142 | 2 Ref. | |
| L | 40.900 | 41.300 | 1.610 | 1.626 | |
| L1 | 24.800 | 25.100 | 0.976 | 0.988 | |
| L2 | 20.300 | 20.600 | 0.799 | 0.811 | |
| Ø | 7.100 | 7.300 | 0.280 | 0.287 | |
| е | 5.450 Typ. | | 0.21 | 5 Тур. | |
| Н | 5.980 Typ. | | 0.23 | 5 Тур. | |
| h | 0.000 | 0.300 | 0.000 | 0.012 | |



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Vishay

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