

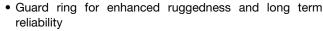
High Performance Schottky Rectifier, 200 A



PRODUCT SUMMARY				
I _{F(AV)}	200 A			
V _R	45 V			
Package	TO-244			
Circuit	Two diodes common cathode			

FEATURES

- 150 °C T_J operation
- · Center tap module
- · Low forward voltage drop
- High frequency operation



- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-200CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES				
I _{F(AV)}	Rectangular waveform	200	Α			
V_{RRM}		45	V			
I _{FSM}	t _p = 5 μs sine	26 000	Α			
V _F	100 A _{pk} , T _J = 125 °C (per leg)	0.52	V			
T _J	Range	-55 to +150	°C			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-200CNQ045PbF	UNITS	
Maximum DC reverse voltage	V_R	45	V	
Maximum working peak reverse voltage	V _{RWM}	45	V	

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	. TEST CONDITIONS		VALUES	UNITS		
Maximum average	per leg		50 % duty cycle at T _C = 116 °C, rectangular waveform		50.0/		100	
forward current See fig. 5	per device	I _{F(AV)}			200	A		
Maximum peak one cycle non-repetitive surge current per leg			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	26 000			
See fig. 7	rrent per leg	I _{FSM}	10 ms sine or 6 ms rect. pulse rated V _{RRM} applied		1550	A		
Non-repetitive avalanch	ne energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 17 A, L = 1 mH		135	mJ		
Repetitive avalanche cu	urrent per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		20	Α		



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	100 A	T ₁ = 25 °C	0.55	V
Maximum forward voltage drop per leg		200 A	11 = 23 0	0.73	
See fig. 1		100 A	T _{.1} = 125 °C	0.52	
		200 A	1j = 125 C	0.69	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	10	mA
See fig. 2		T _J = 125 °C	VR = nateu VR	800	
Threshold voltage	V _{F(TO)}	- T _J = T _J maximum		0.27	V
Forward slope resistance	r _t			2.0	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		5200	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}	- 55	-	150	°C	
Thermal registence, junction to acco	per leg	В	-	-	0.38		
Thermal resistance, junction to case	per module	R_{thJC}	-	-	0.19	°C/W	
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-		
Wester			_	68		g	
Weight			_	2.4	_	oz.	
Mounting torque			35.4 (4)	-	53.1 (6)		
Mounting torque center hole Terminal torque			30 (3.4)	-	40 (4.6)	lbf · in (N · m)	
			30 (3.4)	-	44.2 (5)	,	
Vertical pull			-	-	80	lbf ⋅ in	
2" lever pull			-	-	35	IDI · III	

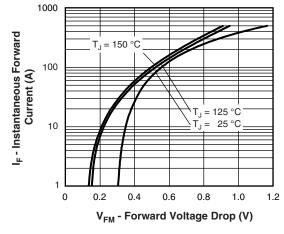


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

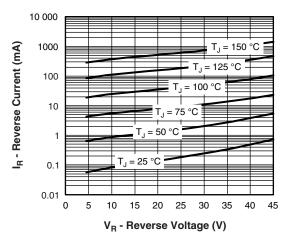


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

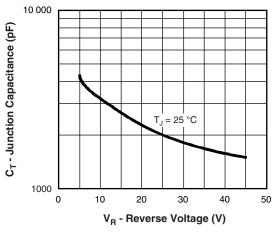


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

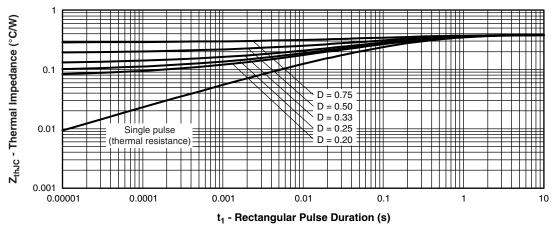


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

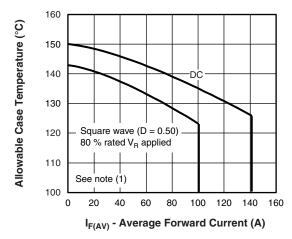


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

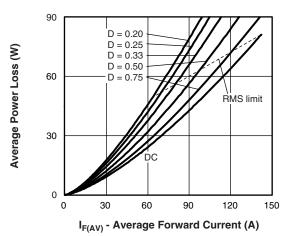


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

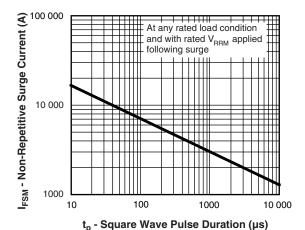


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

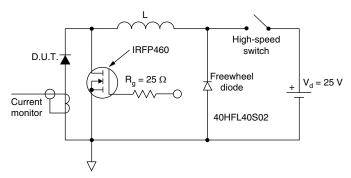


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

Vishay Semiconductors product

2 - Average current rating (x 10)

Product silicon identificationC = Circuit configuration

5 - N = Not isolated

G = Schottky rectifier diode

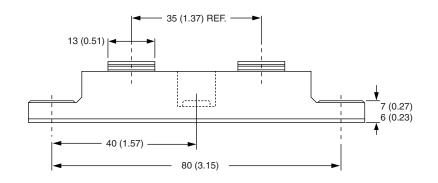
7 - Voltage rating (045 = 45 V)8 - Lead (Pb)-free

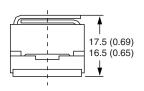
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

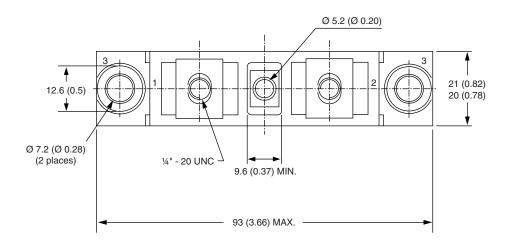


TO-244

DIMENSIONS in millimeters (inches)









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Vishay

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